

# VIPA GmbH

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91074 Herzogenaurach  
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Kunde :  
Anlagenbezeichnung : Produktmakros für System 100V  
Zeichnungsnummer : VIPA100V  
Kommission :

Hersteller :  
Pfad :  
Projektname :  
Fabrikat :  
Type :  
Installationsort :  
Projektverantwortlicher : Hr. Stich  
Teilebesonderheit :

Erstellt am : 27.03.03      Höchste Seitenzahl : 17  
Bearbeitet am : 10.05.05      von (Kürzel): ZBW      Anzahl der Seiten : 428

			Datum	07.05.05	Produktmakros für System 100V			Deckblatt	VIPA100V		=ALLGEMEIN	
			Bearb.	ZBW							+ALLGEMEIN	
			Geänd.									Allgemein
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.				17 Bl.	



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SYSTEM100V	112_4BH00	4	SPS-Übersicht Eingänge, CPU 112 DC24V, 112-4BH00	System 100V	07.05.05	ZBW
SYSTEM100V	112_4BH00	5	SPS-Übersicht Ein-/Ausgänge, CPU 112 DC24V, 112-4BH00	System 100V	07.05.05	ZBW
SYSTEM100V	112_4BH00	6	Frontansicht, CPU 112 DC24V, 112-4BH00	System 100V	07.05.05	ZBW
SYSTEM100V	112_4BH00	7	Anschlußbelegung, CPU 112 DC24V, 112-4BH00	System 100V	10.05.05	ZBW
SYSTEM100V	112_4BH00	8	Eingangsbyte 0, CPU 112 DC24V, 112-4BH00	System 100V	07.05.05	ZBW
SYSTEM100V	112_4BH00	9	Ausgangsbyte 0, CPU 112 DC24V, 112-4BH00	System 100V	07.05.05	ZBW
SYSTEM100V	112_4BH00	10	Eingangsbyte 0, CPU 112 DC24V, 112-4BH00	System 100V	07.05.05	ZBW
SYSTEM100V	112_4BH00	11	Eingangsbyte 2, CPU 112 DC24V, 112-4BH00	System 100V	07.05.05	ZBW
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SYSTEM100V	114_6BJ01	1	SPS-Übersicht Versorgung, CPU 114 DC24V, 114-6BJ01	System 100V	12.07.03	ZBW

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SYSTEM100V	114_6BJ50	1	SPS-Übersicht Versorgung, CPU 114R DC24V, 114-6BJ50	System 100V	09.04.03	ZBW
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SYSTEM100V	115_6BL21	7	Eingangsbyte 1, CPU 115DP DC24V, 115-6BL21	System 100V	14.07.03	ZBW
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SYSTEM100V	115_6BL22	9	Eingangsbyte 1, CPU 115DP DC24V, 115-6BL22	System 100V	07.05.05	ZBW
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SYSTEM100V	115_6BL22	11	Ausgangsbyte 1, CPU 115DP DC24V, 115-6BL22	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL22	12	Eingangsbyte 0, CPU 115DP DC24V, 115-6BL22	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL22	13	Eingangsbyte 1, CPU 115DP DC24V, 115-6BL22	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL22	14	Eingangsbyte 2, CPU 115DP DC24V, 115-6BL22	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL22	15	Ausgangsbyte 0, CPU 115DP DC24V, 115-6BL22	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL22	16	Ausgangsbyte 1, CPU 115DP DC24V, 115-6BL22	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL32	1	SPS-Übersicht Versorgung, CPU 115SER DC24V, 115-6BL32	System 100V	07.05.05	ZBW
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SYSTEM100V	115_6BL32	13	Eingangsbyte 1, CPU 115SER DC24V, 115-6BL32	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL32	14	Eingangsbyte 2, CPU 115SER DC24V, 115-6BL32	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL32	15	Ausgangsbyte 0, CPU 115SER DC24V, 115-6BL32	System 100V	07.05.05	ZBW
SYSTEM100V	115_6BL32	16	Ausgangsbyte 1, CPU 115SER DC24V, 115-6BL32	System 100V	07.05.05	ZBW

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SYSTEM100V	116_6BJ01	6	Anschlußbelegung, CPU 116 DC24V, 116-6BJ01	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ01	7	Eingangsbyte 0, CPU 116 DC24V, 116-6BJ01	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ01	8	Eingangsbyte 1, CPU 116 DC24V, 116-6BJ01	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ01	9	Analog Eingänge, CPU 116 DC24V, 116-6BJ01	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ01	10	Ausgangsbyte 0, CPU 116 DC24V, 116-6BJ01	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ11	1	SPS-Übersicht Versorgung, CPU 116SER DC24V, 116-6BJ11	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ11	2	SPS-Übersicht Eingänge, CPU 116SER DC24V, 116-6BJ11	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ11	3	SPS-Übersicht Eingänge analog, CPU 116SER DC24V, 116-6BJ11	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ11	4	SPS-Übersicht Ausgänge, CPU 116SER DC24V, 116-6BJ11	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ11	5	Frontansicht, CPU 116SER DC24V, 116-6BJ11	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ11	6	Anschlußbelegung, CPU 116SER DC24V, 116-6BJ11	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ11	7	Eingangsbyte 0, CPU 116SER DC24V, 116-6BJ11	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ11	8	Eingangsbyte 1, CPU 116SER DC24V, 116-6BJ11	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ11	9	Analog Eingänge, CPU 116SER DC24V, 116-6BJ11	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ11	10	Ausgangsbyte 0, CPU 116SER DC24V, 116-6BJ11	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ21	1	SPS-Übersicht Versorgung, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ21	2	SPS-Übersicht Eingänge, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ21	3	SPS-Übersicht Eingänge analog, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ21	4	SPS-Übersicht Ausgänge, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ21	5	Frontansicht, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	12.07.03	ZBW
SYSTEM100V	116_6BJ21	6	Anschlußbelegung, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ21	7	Eingangsbyte 0, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ21	8	Eingangsbyte 1, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ21	9	Analog Eingänge, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	14.07.03	ZBW
SYSTEM100V	116_6BJ21	10	Ausgangsbyte 0, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	System 100V	14.07.03	ZBW



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SYSTEM100V	123_4EH00	1	SPS-Übersicht Eingänge, EM 123 DC24V, 123-4EH00	System 100V	09.04.03	ZBW
SYSTEM100V	123_4EH00	2	SPS-Übersicht Ausgänge, EM 123 DC24V, 123-4EH00	System 100V	09.04.03	ZBW
SYSTEM100V	123_4EH00	3	Frontansicht, EM 123 DC24V, 123-4EH00	System 100V	09.04.03	ZBW
SYSTEM100V	123_4EH00	4	Eingangsbyte 0, EM 123 DC24V, 123-4EH00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EH00	5	Ausgangsbyte 0, EM 123 DC24V, 123-4EH00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EL00	1	SPS-Übersicht Eingänge, EM 123 DC24V, 123-4EL00	System 100V	09.04.03	ZBW
SYSTEM100V	123_4EL00	2	SPS-Übersicht Ausgänge, EM 123 DC24V, 123-4EL00	System 100V	09.04.03	ZBW
SYSTEM100V	123_4EL00	3	Frontansicht, EM 123 DC24V, 123-4EL00	System 100V	09.04.03	ZBW
SYSTEM100V	123_4EL00	4	Eingangsbyte 0, EM 123 DC24V, 123-4EL00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EL00	5	Eingangsbyte 1, EM 123 DC24V, 123-4EL00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EL00	6	Ausgangsbyte 0, EM 123 DC24V, 123-4EL00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EL00	7	Ausgangsbyte 1, EM 123 DC24V, 123-4EL00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ00	1	SPS-Übersicht Eingänge, EM 123 DC24V, 123-4EJ00	System 100V	15.04.03	ZBW
SYSTEM100V	123_4EJ00	2	SPS-Übersicht Ausgänge, EM 123 DC24V, 123-4EJ00	System 100V	15.04.03	ZBW
SYSTEM100V	123_4EJ00	3	Frontansicht, EM 123 DC24V, 123-4EJ00	System 100V	15.04.03	ZBW
SYSTEM100V	123_4EJ00	4	Eingangsbyte 0, EM 123 DC24V, 123-4EJ00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ00	5	Eingangsbyte 1, EM 123 DC24V, 123-4EJ00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ00	6	Ausgangsbyte 0, EM 123 DC24V, 123-4EJ00	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ10	1	SPS-Übersicht Eingänge, EM 123 DC24V, 123-4EJ10	System 100V	07.05.05	ZBW
SYSTEM100V	123_4EJ10	2	SPS-Übersicht Ausgänge, EM 123 DC24V, 123-4EJ10	System 100V	07.05.05	ZBW
SYSTEM100V	123_4EJ10	3	Frontansicht, EM 123 DC24V, 123-4EJ10	System 100V	07.05.05	ZBW
SYSTEM100V	123_4EJ10	4	Eingangsbyte 0, EM 123 DC24V, 123-4EJ10	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ10	5	Eingangsbyte 1, EM 123 DC24V, 123-4EJ10	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ10	6	Ausgangsbyte 0, EM 123 DC24V, 123-4EJ10	System 100V	07.05.05	ZBW
SYSTEM100V	123_4EJ20	1	SPS-Übersicht Eingänge, EM 123 AC60...230V, 123-4EJ20	System 100V	07.05.05	ZBW
SYSTEM100V	123_4EJ20	2	SPS-Übersicht Ausgänge, EM 123 AC60...230V, 123-4EJ20	System 100V	07.05.05	ZBW
SYSTEM100V	123_4EJ20	3	Frontansicht, EM 123 AC60...230V, 123-4EJ20	System 100V	07.05.05	ZBW
SYSTEM100V	123_4EJ20	4	Eingangsbyte 0, EM 123 AC60...230V, 123-4EJ20	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ20	5	Eingangsbyte 1, EM 123 AC60...230V, 123-4EJ20	System 100V	14.07.03	ZBW
SYSTEM100V	123_4EJ20	6	Ausgangsbyte 0, EM 123 AC60...230V, 123-4EJ20	System 100V	07.05.05	ZBW

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SYSTEM100V	131_4ED00	1	SPS-Übersicht Eingänge analog, EM 131 DC24V, 131-4ED00	System 100V	15.04.03	ZBW
SYSTEM100V	131_4ED00	2	Frontansicht, EM 131 DC24V, 131-4ED00	System 100V	15.04.03	ZBW
SYSTEM100V	131_4ED00	3	Analog Eingänge, EM 131 DC24V, 131-4ED00	System 100V	14.07.03	ZBW
SYSTEM100V	134_4EE00	1	SPS-Übersicht Ein-/Ausgänge analog, EM 134 DC24V, 134-4EE00	System 100V	15.04.03	ZBW
SYSTEM100V	134_4EE00	2	Frontansicht, EM 134 DC24V, 134-4EE00	System 100V	15.04.03	ZBW
SYSTEM100V	134_4EE00	3	Analog Eingänge, EM 134 DC24V, 134-4EE00	System 100V	14.07.03	ZBW
SYSTEM100V	134_4EE00	4	Analog Ausgänge, EM 134 DC24V, 134-4EE00	System 100V	14.07.03	ZBW
SYSTEM100V	151_4PH00	1	SPS-Übersicht Versorgung, SM 151 DC24V, 151-4PH00	System 100V	12.07.03	ZBW
SYSTEM100V	151_4PH00	2	SPS-Übersicht Eingänge, SM 151 DC24V, 151-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_4PH00	3	Frontansicht, SM 151 DC24V, 151-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_4PH00	4	Anschlußbelegung, SM 151 DC24V, 151-4PH00	System 100V	14.07.03	ZBW
SYSTEM100V	151_4PH00	5	Eingangsbyte 0, SM 151 DC24V, 151-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_4PH00	6	Eingangsbyte 1, SM 151 DC24V, 151-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PH00	1	SPS-Übersicht Versorgung, SM 151 DC24V, 151-6PH00	System 100V	12.07.03	ZBW
SYSTEM100V	151_6PH00	2	SPS-Übersicht Eingänge, SM 151 DC24V, 151-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PH00	3	SPS-Übersicht Eingänge, SM 151 DC24V, 151-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PH00	4	Frontansicht, SM 151 DC24V, 151-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PH00	5	Anschlußbelegung, SM 151 DC24V, 151-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	151_6PH00	6	Eingangsbyte 0, SM 151 DC24V, 151-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PH00	7	Eingangsbyte 0, SM 151 DC24V, 151-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PH00	8	Eingangsbyte 1, SM 151 DC24V, 151-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PH00	9	Eingangsbyte 1, SM 151 DC24V, 151-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PL00	1	SPS-Übersicht Versorgung, SM 151 DC24V, 151-6PL00	System 100V	12.07.03	ZBW
SYSTEM100V	151_6PL00	2	SPS-Übersicht Eingänge, SM 151 DC24V, 151-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PL00	3	SPS-Übersicht Eingänge, SM 151 DC24V, 151-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PL00	4	Frontansicht, SM 151 DC24V, 151-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PL00	5	Anschlußbelegung, SM 151 DC24V, 151-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	151_6PL00	6	Eingangsbyte 0, SM 151 DC24V, 151-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PL00	7	Eingangsbyte 1, SM 151 DC24V, 151-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	151_6PL00	8	Eingangsbyte 2, SM 151 DC24V, 151-6PL00	System 100V	07.05.05	ZBW

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SYSTEM100V	151_6PL00	9	Eingangsbyte 3, SM 151 DC24V, 151-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	152_4PH00	1	SPS-Übersicht Versorgung, SM 152 DC24V, 152-4PH00	System 100V	12.07.03	ZBW
SYSTEM100V	152_4PH00	2	SPS-Übersicht Ausgänge, SM 152 DC24V, 152-4PH00	System 100V	12.07.03	ZBW
SYSTEM100V	152_4PH00	3	Frontansicht, SM 152 DC24V, 152-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	152_4PH00	4	Anschlußbelegung, SM 152 DC24V, 152-4PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_4PH00	5	Ausgangsbyte 0, SM 152 DC24V, 152-4PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_4PH00	6	Ausgangsbyte 1, SM 152 DC24V, 152-4PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH00	1	SPS-Übersicht Versorgung, SM 152 DC24V, 152-6PH00	System 100V	12.07.03	ZBW
SYSTEM100V	152_6PH00	2	SPS-Übersicht Ausgänge, SM 152 DC24V, 152-6PH00	System 100V	12.07.03	ZBW
SYSTEM100V	152_6PH00	3	SPS-Übersicht Ausgänge, SM 152 DC24V, 152-6PH00	System 100V	12.07.03	ZBW
SYSTEM100V	152_6PH00	4	Frontansicht, SM 152 DC24V, 152-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	152_6PH00	5	Anschlußbelegung, SM 152 DC24V, 152-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH00	6	Ausgangsbyte 0, SM 152 DC24V, 152-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH00	7	Ausgangsbyte 0, SM 152 DC24V, 152-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH00	8	Ausgangsbyte 1, SM 152 DC24V, 152-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH00	9	Ausgangsbyte 1, SM 152 DC24V, 152-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PL00	1	SPS-Übersicht Versorgung, SM 152 DC24V, 152-6PL00	System 100V	12.07.03	ZBW
SYSTEM100V	152_6PL00	2	SPS-Übersicht Ausgänge, SM 152 DC24V, 152-6PL00	System 100V	12.07.03	ZBW
SYSTEM100V	152_6PL00	3	SPS-Übersicht Ausgänge, SM 152 DC24V, 152-6PL00	System 100V	12.07.03	ZBW
SYSTEM100V	152_6PL00	4	Frontansicht, SM 152 DC24V, 152-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	152_6PL00	5	Anschlußbelegung, SM 152 DC24V, 152-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PL00	6	Ausgangsbyte 0, SM 152 DC24V, 152-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PL00	7	Ausgangsbyte 1, SM 152 DC24V, 152-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PL00	8	Ausgangsbyte 2, SM 152 DC24V, 152-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PL00	9	Ausgangsbyte 3, SM 152 DC24V, 152-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH50	1	SPS-Übersicht Versorgung, SM 152 DC24, 152-6PH50	System 100V	12.07.03	ZBW
SYSTEM100V	152_6PH50	2	SPS-Übersicht Ausgänge, SM 152 DC24, 152-6PH50	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH50	3	SPS-Übersicht Ausgänge, SM 152 DC24, 152-6PH50	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH50	4	Frontansicht, SM 152 DC24, 152-6PH50	System 100V	07.05.05	ZBW
SYSTEM100V	152_6PH50	5	Anschlußbelegung, SM 152 DC24, 152-6PH50	System 100V	14.07.03	ZBW

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SYSTEM100V	152_6PH50	6	Ausgangsbyte 0, SM 152 DC24, 152-6PH50	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH50	7	Ausgangsbyte 0, SM 152 DC24, 152-6PH50	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH50	8	Ausgangsbyte 1, SM 152 DC24, 152-6PH50	System 100V	14.07.03	ZBW
SYSTEM100V	152_6PH50	9	Ausgangsbyte 1, SM 152 DC24, 152-6PH50	System 100V	14.07.03	ZBW
SYSTEM100V	153_4PF00	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PF00	2	SPS-Übersicht Eingänge, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PF00	3	SPS-Übersicht Ausgänge, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PF00	4	Frontansicht, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PF00	5	Anschlußbelegung, SM 153 DC24V, 153-4PF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4PF00	6	Eingangsbyte 0, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PF00	7	Eingangsbyte 0, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PF00	8	Ausgangsbyte 0, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PF00	9	Ausgangsbyte 0, SM 153 DC24V, 153-4PF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PH00	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-4PH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4PH00	2	SPS-Übersicht Ein-/Ausgänge, SM 153 DC24V, 153-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PH00	3	Frontansicht, SM 153 DC24V, 153-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PH00	4	Anschlußbelegung, SM 153 DC24V, 153-4PH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4PH00	5	Eingangsbyte 0, SM 153 DC24V, 153-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4PH00	6	Ausgangsbyte 0, SM 153 DC24V, 153-4PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PH00	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PH00	2	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PH00	3	SPS-Übersicht Ausgänge, SM 153 DC24V, 153-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PH00	4	Frontansicht, SM 153 DC24V, 153-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PH00	5	Anschlußbelegung, SM 153 DC24V, 153-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PH00	6	Eingangsbyte 0, SM 153 DC24V, 153-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PH00	7	Eingangsbyte 0, SM 153 DC24V, 153-6PH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PH00	8	Ausgangsbyte 0, SM 153 DC24V, 153-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PH00	9	Ausgangsbyte 0, SM 153 DC24V, 153-6PH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL00	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL00	2	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6PL00	System 100V	07.05.05	ZBW

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SYSTEM100V	153_6PL00	3	SPS-Übersicht Ausgänge, SM 153 DC24V, 153-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL00	4	Frontansicht, SM 153 DC24V, 153-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL00	5	Anschlußbelegung, SM 153 DC24V, 153-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL00	6	Eingangsbyte 0, SM 153 DC24V, 153-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL00	7	Eingangsbyte 1, SM 153 DC24V, 153-6PL00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL00	8	Ausgangsbyte 0, SM 153 DC24V, 153-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL00	9	Ausgangsbyte 1, SM 153 DC24V, 153-6PL00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL10	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-6PL10	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL10	2	SPS-Übersicht Ausgänge, SM 153 DC24V, 153-6PL10	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL10	3	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6PL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL10	4	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6PL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL10	5	Frontansicht, SM 153 DC24V, 153-6PL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL10	6	Anschlußbelegung, SM 153 DC24V, 153-6PL10	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL10	7	Ausgangsbyte 0, SM 153 DC24V, 153-6PL10	System 100V	14.07.03	ZBW
SYSTEM100V	153_6PL10	8	Eingangsbyte 0, SM 153 DC24V, 153-6PL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL10	9	Eingangsbyte 1, SM 153 DC24V, 153-6PL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6PL10	10	Eingangsbyte 2, SM 153 DC24V, 153-6PL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CF00	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CF00	2	SPS-Übersicht Eingänge, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CF00	3	SPS-Übersicht Ausgänge, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CF00	4	Frontansicht, SM 153 DC24V, 153-4CF00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CF00	5	Anschlußbelegung, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CF00	6	Eingangsbyte 0, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CF00	7	Eingangsbyte 0, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CF00	8	Ausgangsbyte 0, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CF00	9	Ausgangsbyte 0, SM 153 DC24V, 153-4CF00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CH00	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-4CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CH00	2	SPS-Übersicht Ein-/Ausgänge, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CH00	3	SPS-Übersicht Ein-/Ausgänge, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CH00	4	Frontansicht, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW

			Datum	10.05.05	Produktmakros für System 100V		Inhaltsverzeichnis	VIPA100V		=ALLGEMEIN	
			Bearb.	ZBW						+ALLGEMEIN	
			Geänd.								
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.	Allgemein		B1.	13
										17 B1.	

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VIPA.SKJ 12.12.2002

Anlage	Ort	Seite	Seitenbenennung	Seitenzusatzfeld	Datum	Bearbeiter
SYSTEM100V	153_4CH00	5	Anschlußbelegung, SM 153 DC24V, 153-4CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_4CH00	6	Eingangsbyte 0, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CH00	7	Eingangsbyte 1, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CH00	8	Ausgangsbyte 0, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CH00	9	Eingangsbyte 0, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_4CH00	10	Ausgangsbyte 0, SM 153 DC24V, 153-4CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CH00	2	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	3	SPS-Übersicht Ein-/Ausgänge, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CH00	4	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	5	SPS-Übersicht Ausgänge, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CH00	6	Frontansicht, SM 153 DC24V, 153-6CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	7	Anschlußbelegung, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CH00	8	Eingangsbyte 0, SM 153 DC24V, 153-6CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	9	Eingangsbyte 0, SM 153 DC24V, 153-6CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	10	Eingangsbyte 1, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CH00	11	Ausgangsbyte 0, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CH00	12	Eingangsbyte 0, SM 153 DC24V, 153-6CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	13	Eingangsbyte 0, SM 153 DC24V, 153-6CH00	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CH00	14	Ausgangsbyte 0, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CH00	15	Ausgangsbyte 0, SM 153 DC24V, 153-6CH00	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CL10	1	SPS-Übersicht Versorgung, SM 153 DC24V, 153-6CL10	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CL10	2	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6CL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CL10	3	SPS-Übersicht Eingänge, SM 153 DC24V, 153-6CL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CL10	4	SPS-Übersicht Ausgänge, SM 153 DC24V, 153-6CL10	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CL10	5	Frontansicht, SM 153 DC24V, 153-6CL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CL10	6	Anschlußbelegung, SM 153 DC24V, 153-6CL10	System 100V	14.07.03	ZBW
SYSTEM100V	153_6CL10	7	Eingangsbyte 0, SM 153 DC24V, 153-6CL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CL10	8	Eingangsbyte 1, SM 153 DC24V, 153-6CL10	System 100V	07.05.05	ZBW
SYSTEM100V	153_6CL10	9	Eingangsbyte 2, SM 153 DC24V, 153-6CL10	System 100V	07.05.05	ZBW

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VIPA.SKJ 12.12.2002

Anlage	Ort	Seite	Seitenbenennung	Seitenzusatzfeld	Datum	Bearbeiter
SYSTEM100V	153_6CL10	10	Ausgangsbyte 0, SM 153 DC24V, 153-6CL10	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	1	SPS-Übersicht Versorgung, SM 136 DC24V, 136-4BB60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BB60	2	SPS-Übersicht Eingänge digital, SM 136 DC24V, 136-4BB60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BB60	3	SPS-Übersicht Ein-/Ausgänge analog, SM 136 DC24V, 136-4BB60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BB60	4	SPS-Übersicht Ausgänge digital, SM 136 DC24V, 136-4BB60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BB60	5	SPS-Übersicht Ein-/Ausgänge analog, SM 136 DC24V, 136-4BB60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BB60	6	Frontansicht, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	7	Anschlußbelegung, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	8	Eingangsbyte 0, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	9	Analog Eingänge, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	10	Analog Ausgänge, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	11	Ausgangsbyte 0, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	12	Analog Eingänge, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BB60	13	Analog Ausgänge, SM 136 DC24V, 136-4BB60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD60	1	SPS-Übersicht Versorgung, SM 136 DC24V, 136-4BD60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD60	2	SPS-Übersicht Eingänge digital, SM 136 DC24V, 136-4BD60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD60	3	SPS-Übersicht Eingänge analog, SM 136 DC24V, 136-4BD60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD60	4	SPS-Übersicht Ausgänge digital, SM 136 DC24V, 136-4BD60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD60	5	SPS-Übersicht Eingänge analog, SM 136 DC24V, 136-4BD60	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD60	6	Frontansicht, SM 136 DC24V, 136-4BD60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD60	7	Anschlußbelegung, SM 136 DC24V, 136-4BD60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD60	8	Eingangsbyte 0, SM 136 DC24V, 136-4BD60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD60	9	Analog Eingänge, SM 136 DC24V, 136-4BD60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD60	10	Ausgangsbyte 0, SM 136 DC24V, 136-4BD60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD60	11	Analog Eingänge, SM 136 DC24V, 136-4BD60	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	1	SPS-Übersicht Versorgung, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD70	2	SPS-Übersicht Eingänge digital, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD70	3	SPS-Übersicht Eingänge analog, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	4	SPS-Übersicht Eingänge digital, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD70	5	SPS-Übersicht Ausgänge analog, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW

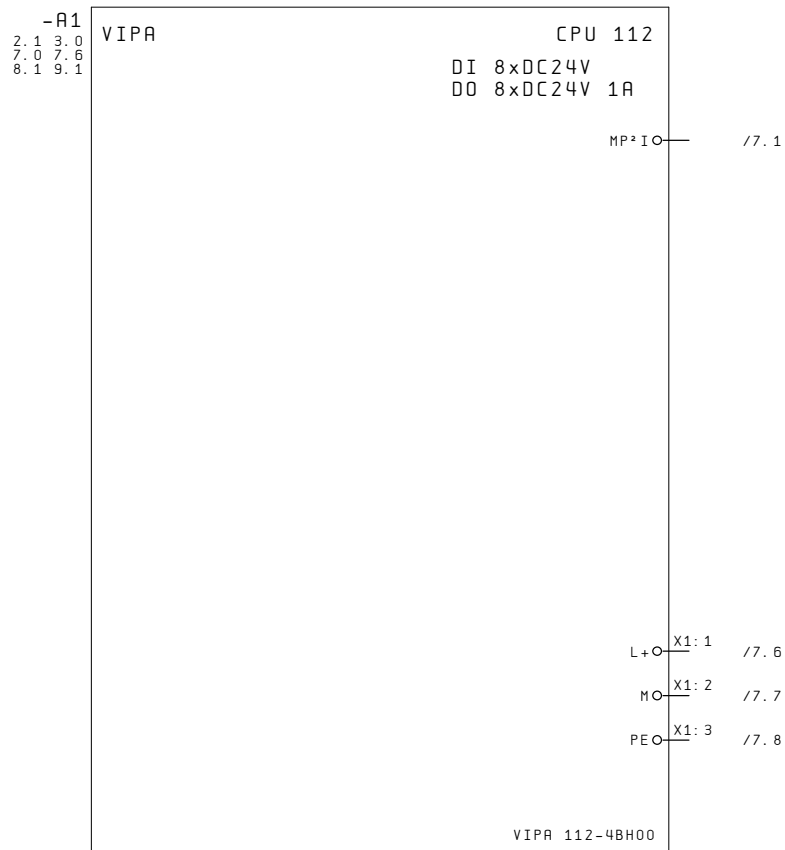
Inhaltsverzeichnis

VIPA.SKJ 12.12.2002

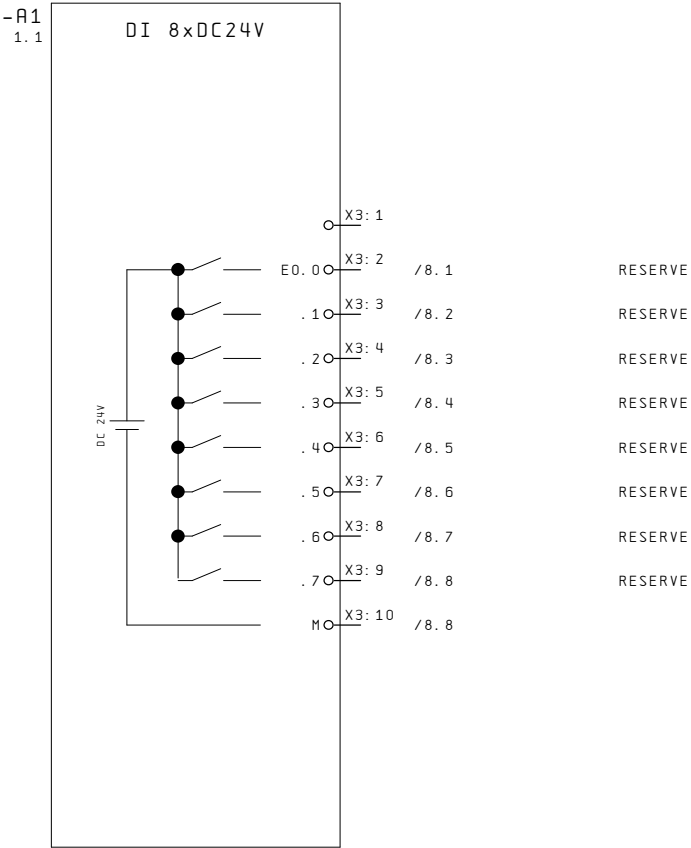
Anlage	Ort	Seite	Seitenbenennung	Seitenzusatzfeld	Datum	Bearbeiter
SYSTEM100V	136_4BD70	6	SPS-Übersicht Ausgänge digital, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD70	7	SPS-Übersicht Eingänge analog, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD70	8	SPS-Übersicht Ausgänge digital, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD70	9	SPS-Übersicht Ausgänge analog, SM 136 DC24V, 136-4BD70	System 100V	28.04.03	ZBW
SYSTEM100V	136_4BD70	10	Frontansicht, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	11	Anschlußbelegung, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	12	Eingangsbyte 0, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	13	Analog Eingänge, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	14	Eingangsbyte 0, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	15	Analog Ausgänge, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	16	Ausgangsbyte 0, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	17	Analog Eingänge, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	18	Ausgangsbyte 0, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	136_4BD70	19	Analog Ausgänge, SM 136 DC24V, 136-4BD70	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	1	SPS-Übersicht Eingänge, CM 101, 101-4FH50	System 100V	28.04.03	ZBW
SYSTEM100V	101_4FH50	2	SPS-Übersicht Ausgänge, CM 101, 101-4FH50	System 100V	28.04.03	ZBW
SYSTEM100V	101_4FH50	3	Frontansicht, CM 101, 101-4FH50	System 100V	28.04.03	ZBW
SYSTEM100V	101_4FH50	4	Eingangsbyte 0, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	5	Eingangsbyte 0, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	6	Eingangsbyte 1, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	7	Eingangsbyte 1, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	8	Ausgangsbyte 0, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	9	Ausgangsbyte 0, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	10	Ausgangsbyte 1, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_4FH50	11	Ausgangsbyte 1, CM 101, 101-4FH50	System 100V	14.07.03	ZBW
SYSTEM100V	101_6FH50	1	SPS-Übersicht Eingänge, CM 101, 101-6FH50	System 100V	28.04.03	ZBW
SYSTEM100V	101_6FH50	2	SPS-Übersicht Eingänge, CM 101, 101-6FH50	System 100V	28.04.03	ZBW
SYSTEM100V	101_6FH50	3	SPS-Übersicht Ausgänge, CM 101, 101-6FH50	System 100V	28.04.03	ZBW
SYSTEM100V	101_6FH50	4	SPS-Übersicht, CM 101, 101-6FH50	System 100V	28.04.03	ZBW
SYSTEM100V	101_6FH50	5	Frontansicht, CM 101, 101-6FH50	System 100V	28.04.03	ZBW





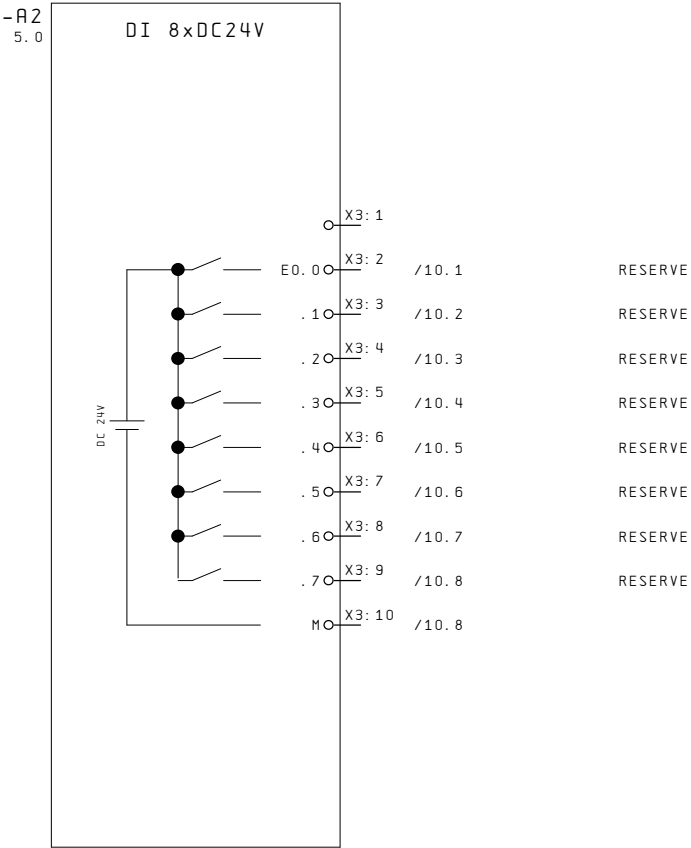


Variante 1: 8 Eingänge/8Ausgänge

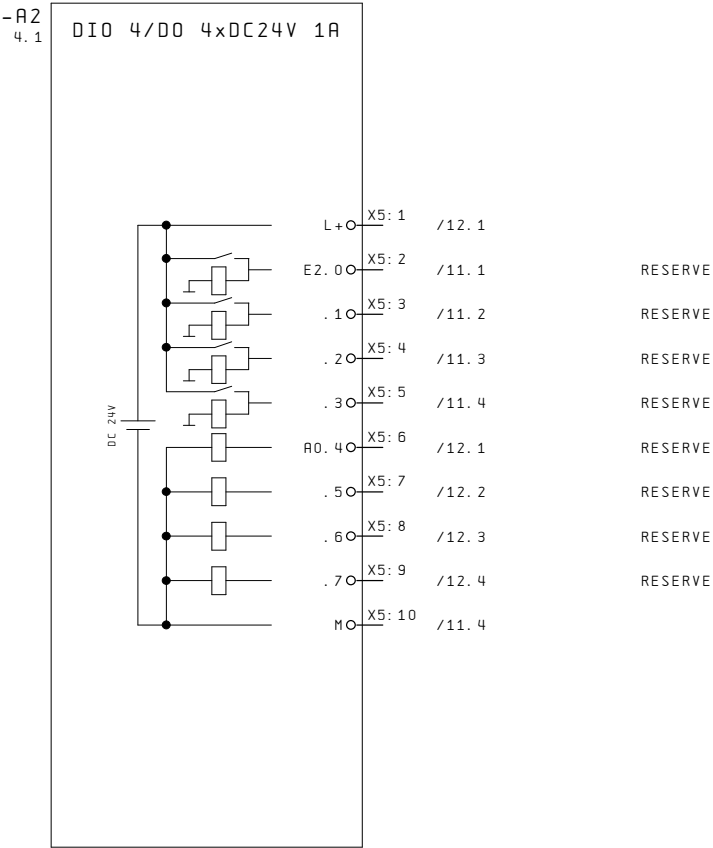


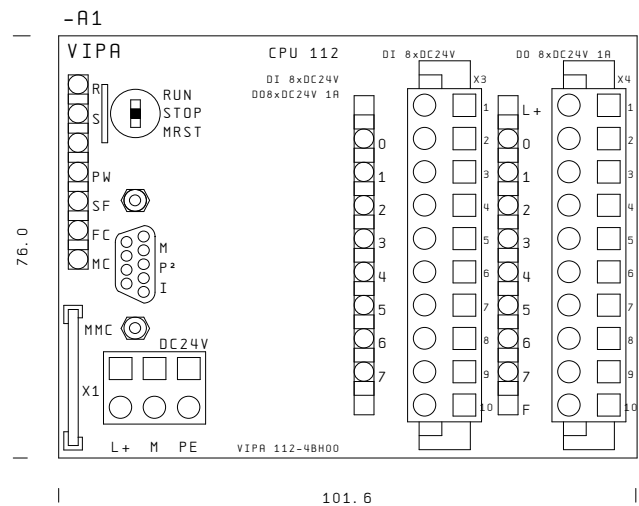


Variante 2: 12 Eingänge/4Ausgänge



Variante 2: 12 Eingänge/4 Ausgänge





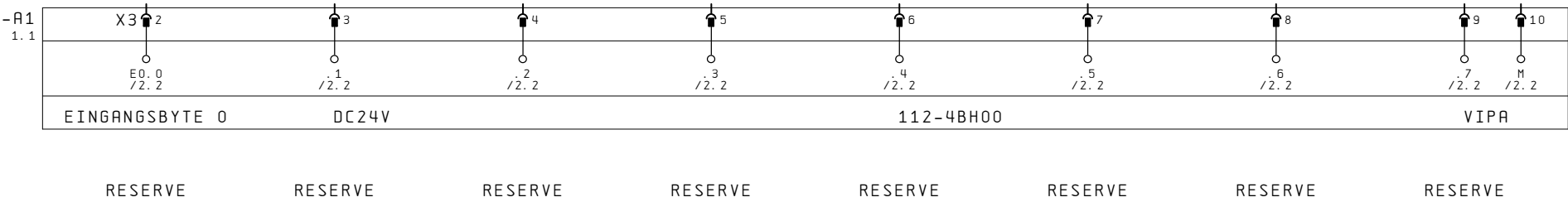
CPU 112  
Arbeitsspeicher 8kB  
Ladespeicher 16kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48





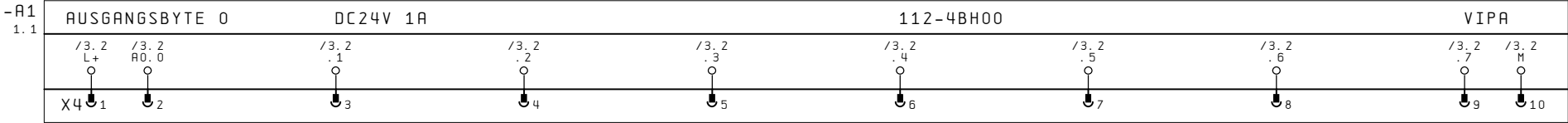
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Variante 1: 8 Eingänge/8 Ausgänge



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Variante 1: 8 Eingänge/8 Ausgänge



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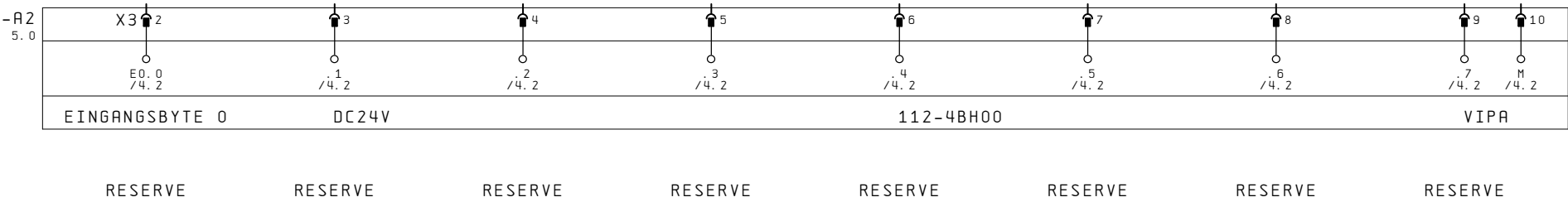
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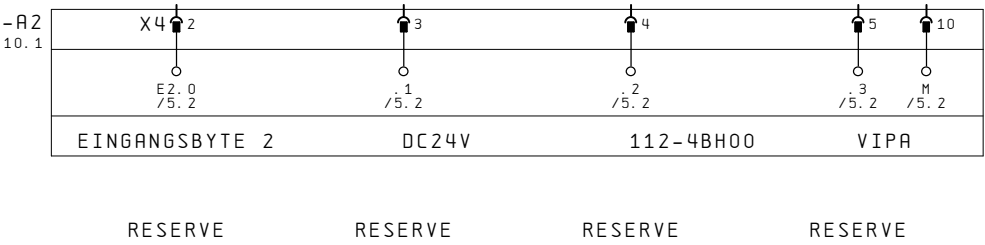
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Variante 2: 12 Eingänge/4 Ausgänge



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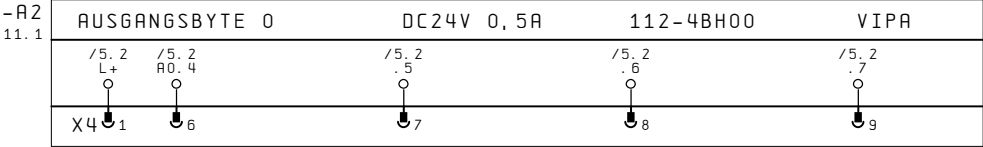
Variante 2: 12 Eingänge/4 Ausgänge



10										12									
			Datum	07.05.05	Produktmakros für System 100V					Eingangsbyte 2, CPU 112 DC24V, 112-4BH00			VIPA100V			=SYSTEM100V			
			Bearb.	ZBW												+112_4BH00			
			Geänd.																
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.				System 100V			B1.	11				
																12 B1.			

0	1	2	3	4	5	6	7	8	9
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Variante 2: 12 Eingänge/4 Ausgänge



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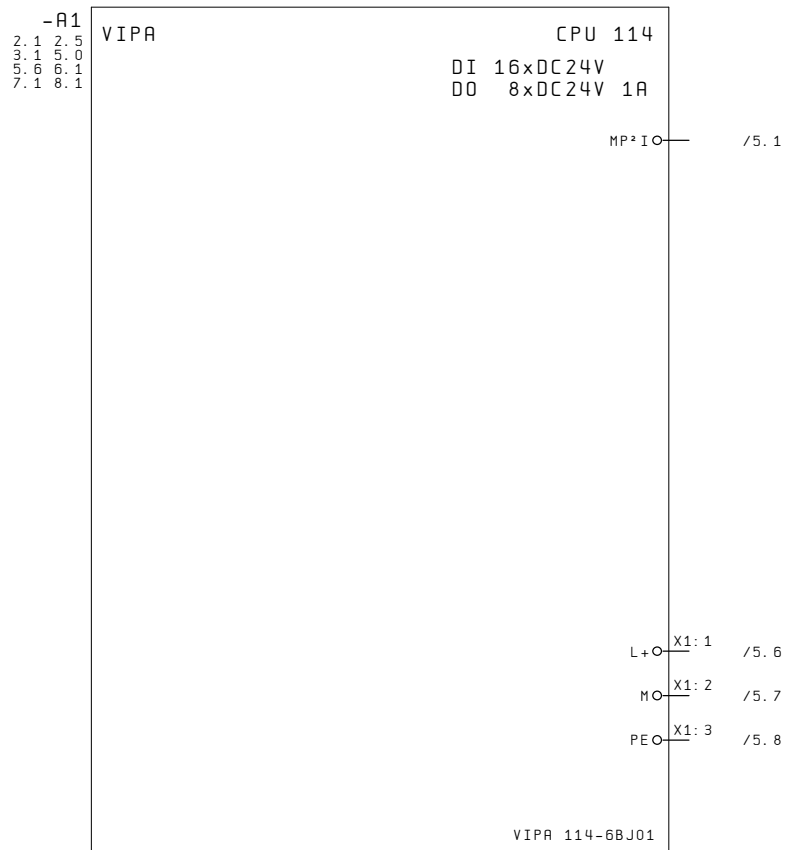
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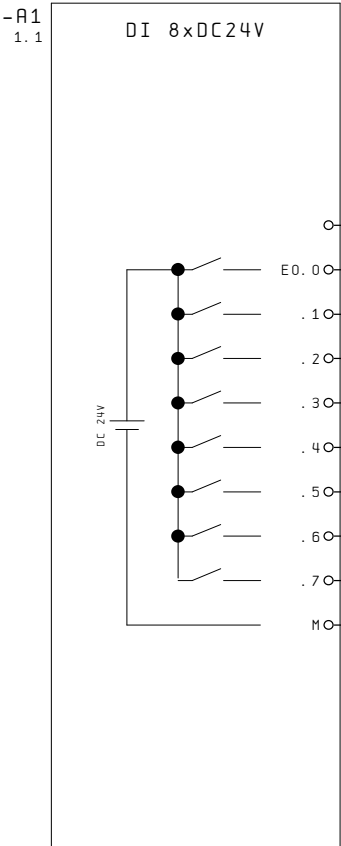
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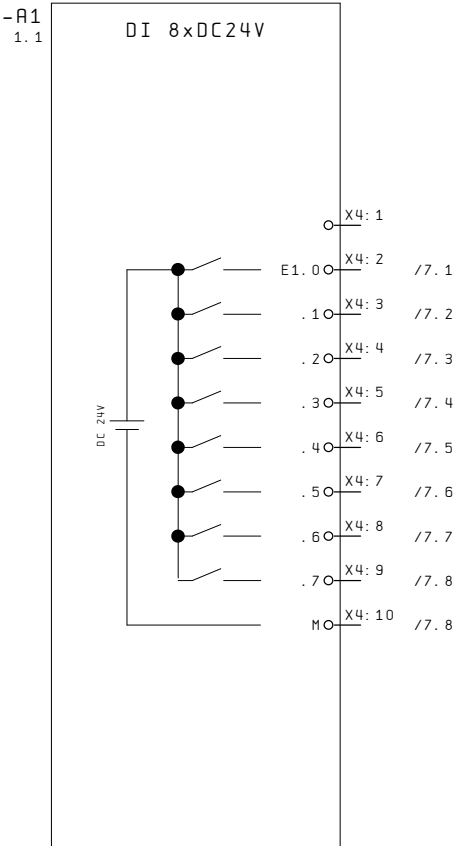
			Datum	07.05.05	Produktmakros für System 100V			Ausgangsbyte 0, CPU 112 DC24V, 112-4BH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+112_4BH00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.	System 100V		B1.	12	
										12 B1.		

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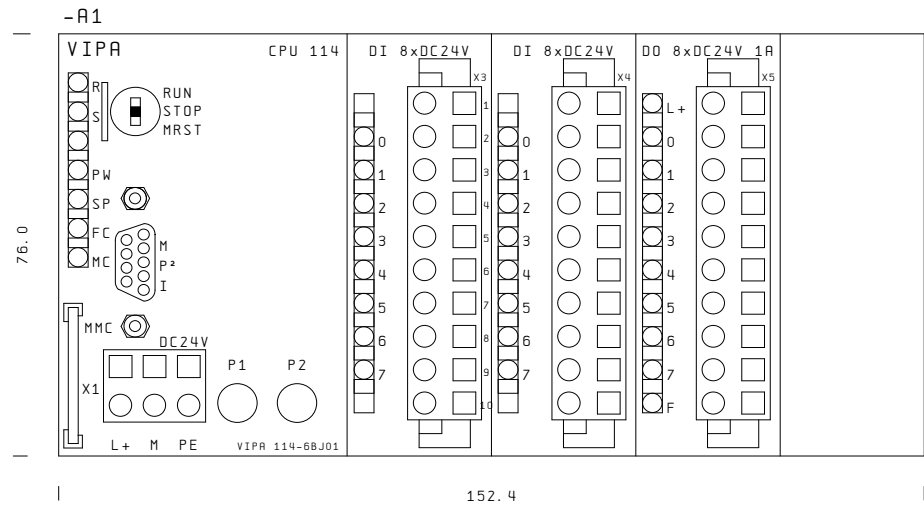
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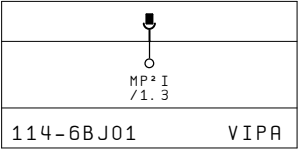




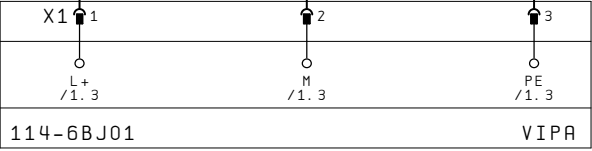
CPU 114  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

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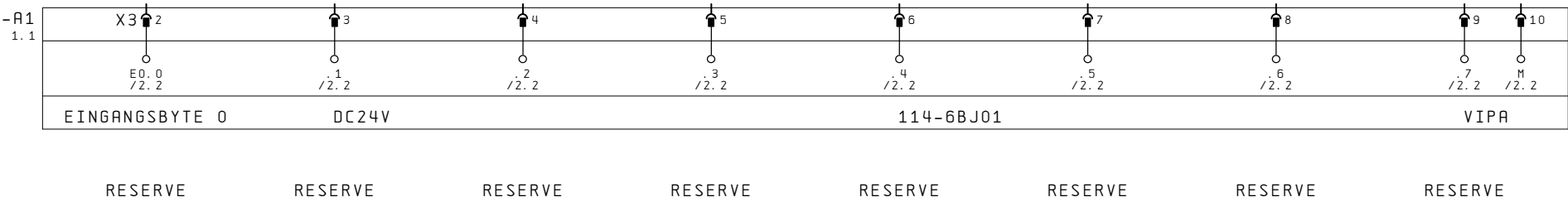


-R1  
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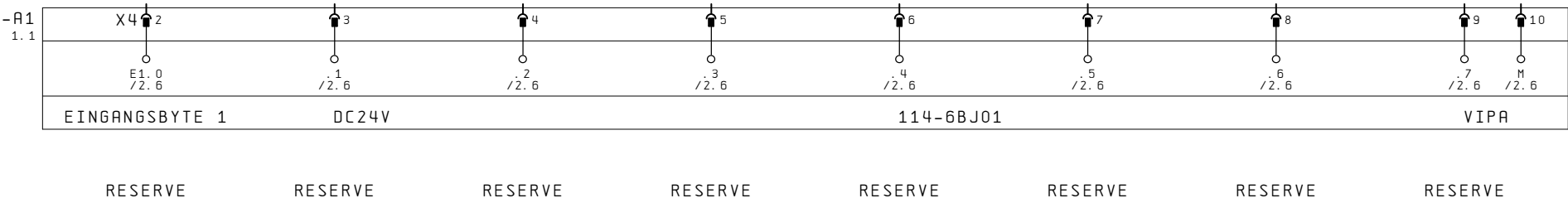


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			Bearb.	ZBW							+114_6BJ01	
			Geänd.								B1.	5
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		8 B1.	

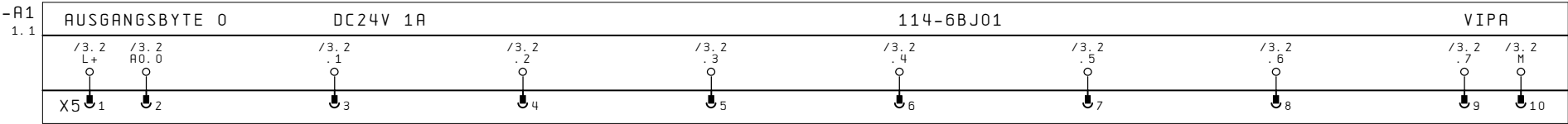
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0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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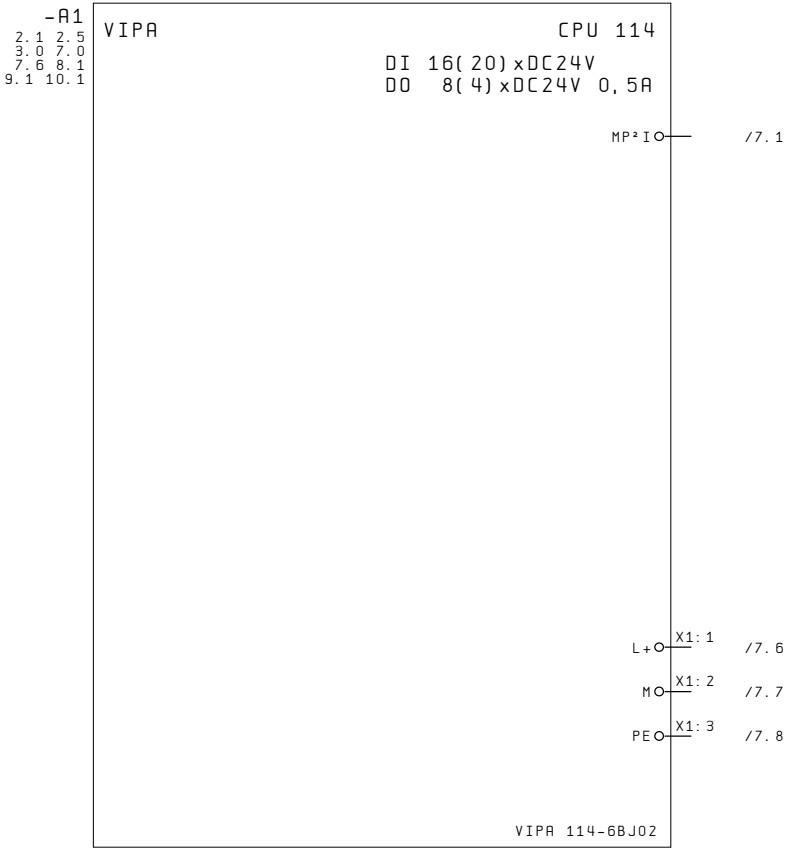
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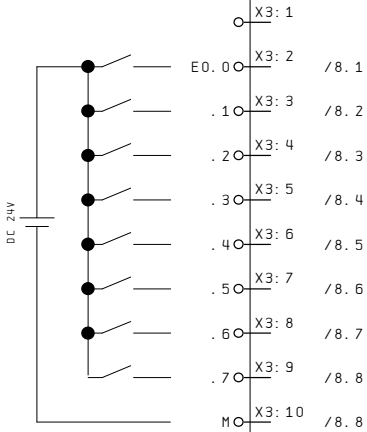
RESERVE



Variante 1: 16 Eingänge/8 Ausgänge

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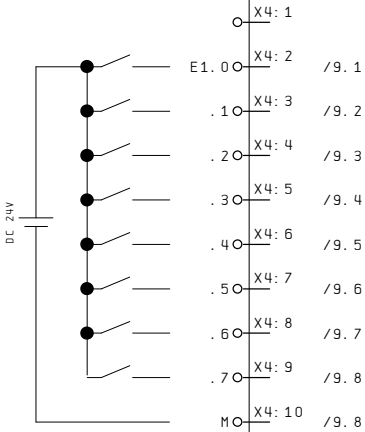
DI 8xDC24V



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RESERVE  
RESERVE

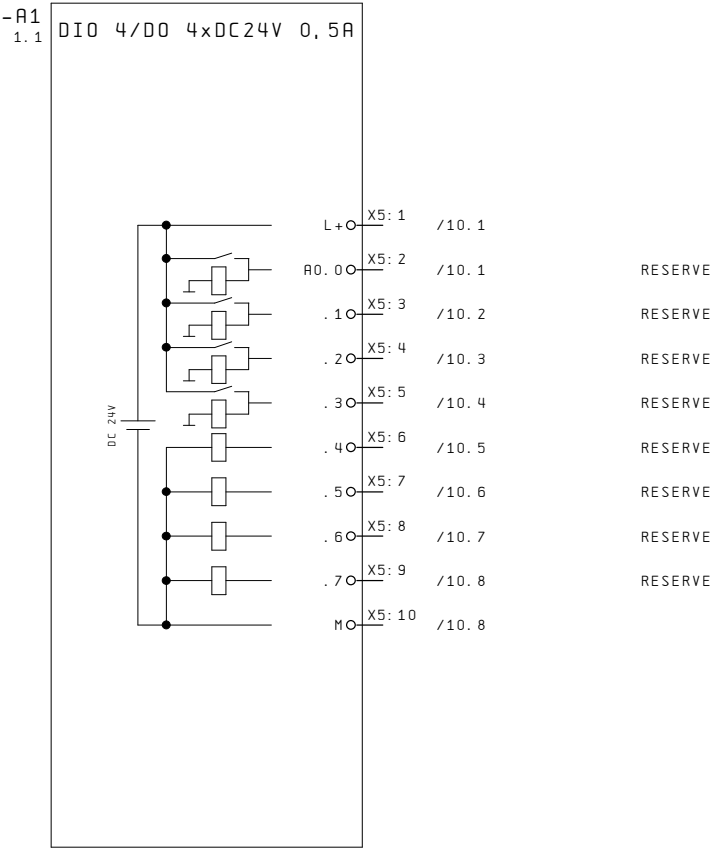
-R1  
1.1

DI 8xDC24V



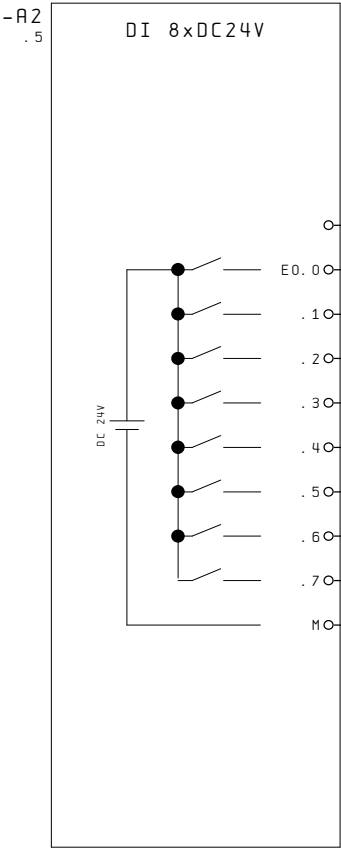
RESERVE  
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RESERVE  
RESERVE  
RESERVE

Variante 1: 16 Eingänge/8 Ausgänge

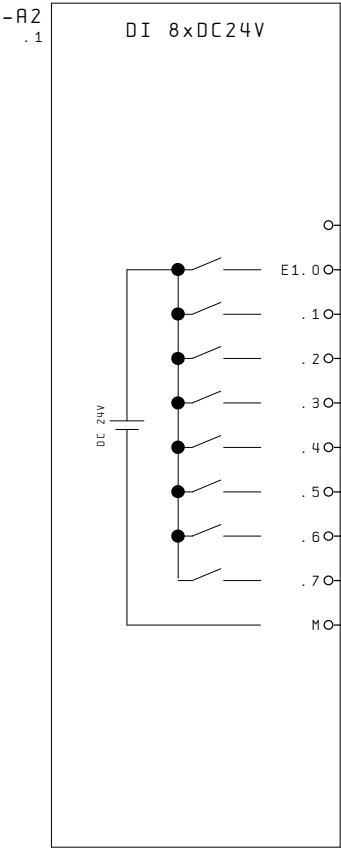




Variante 2: 20 Eingänge/4 Ausgänge

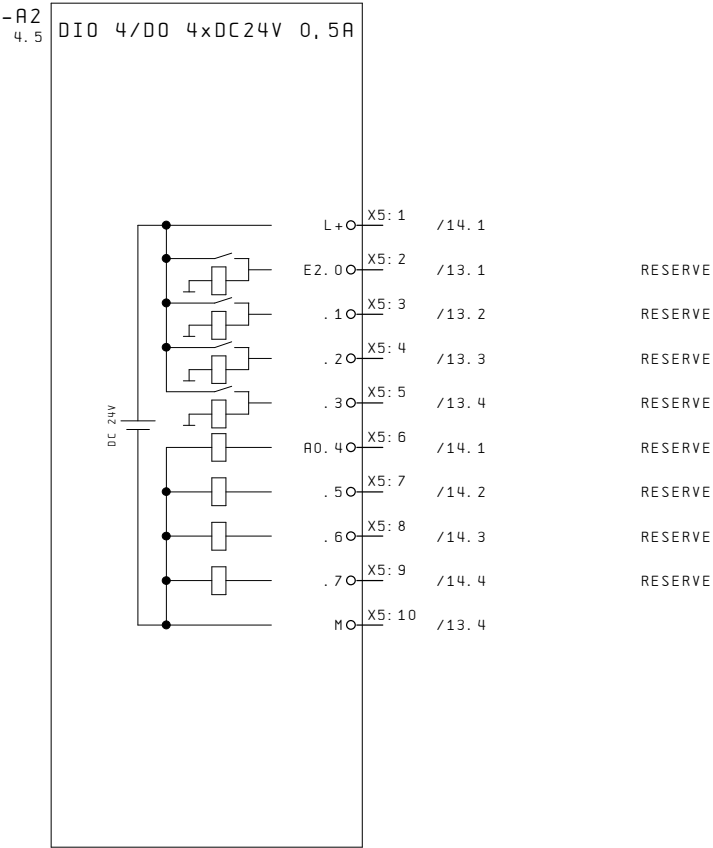


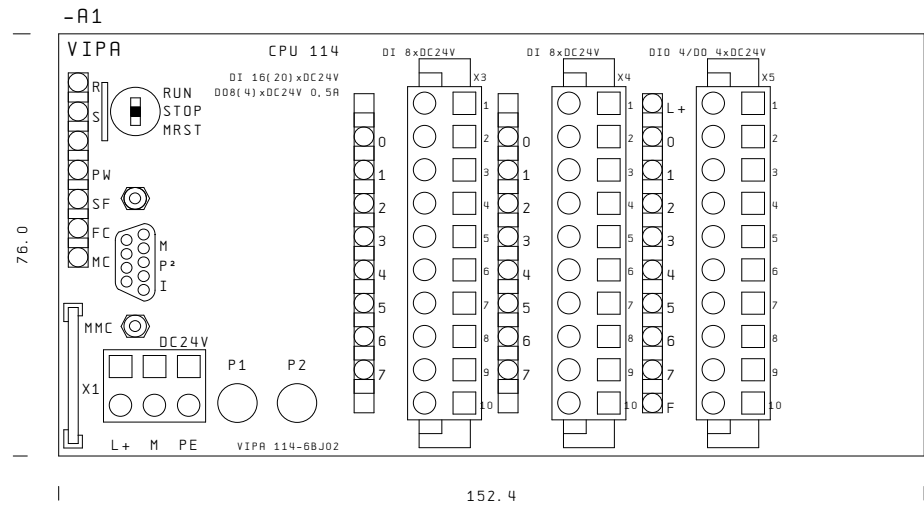
RESERVE  
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RESERVE  
RESERVE



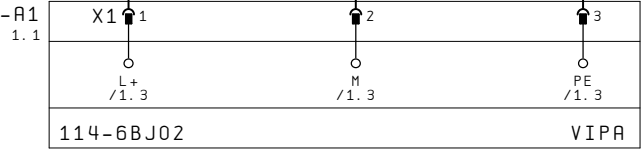
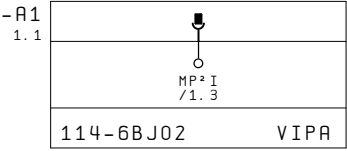
RESERVE  
RESERVE  
RESERVE  
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RESERVE  
RESERVE  
RESERVE  
RESERVE

Variante 2: 20 Eingänge/4 Ausgänge



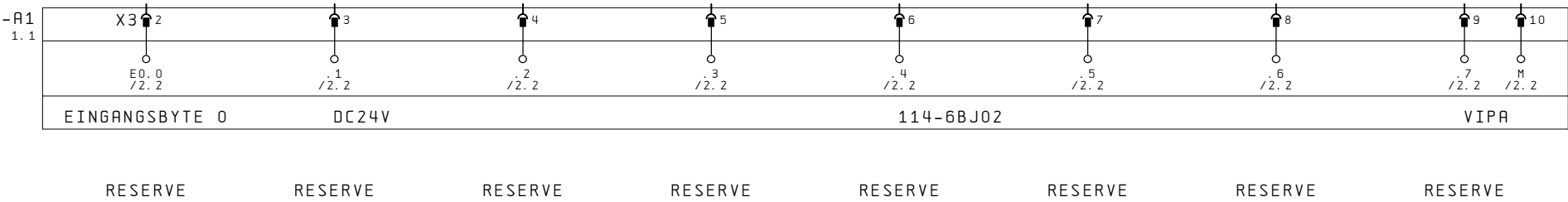


CPU 114  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48



0	1	2	3	4	5	6	7	8	9
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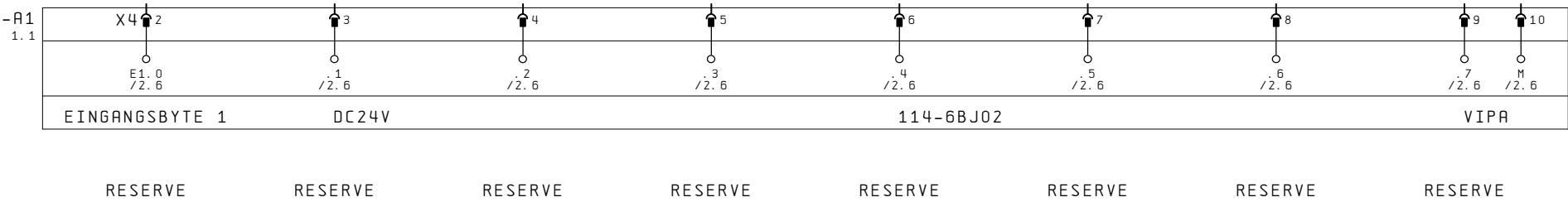
Variante 1: 16 Eingänge/8 Ausgänge



			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 114 DC24V, 114-6BJ02	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+114_6BJ02	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	8
											14 B1.	

0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/8 Ausgänge

4  
/2.65  
/2.66  
/2.67  
/2.68  
/2.69  
/2.610  
M  
/2.6

EINGANGSBYTE 1

DC24V

114-6BJ02

VIPA

RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

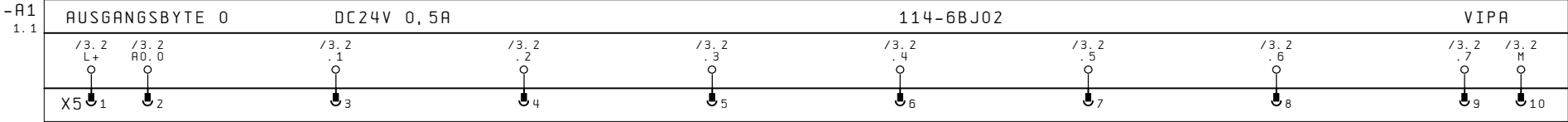
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/8 Ausgänge



RESERVE

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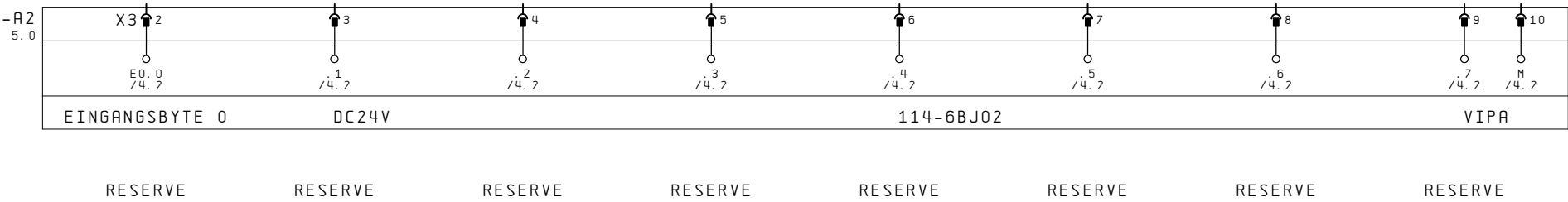
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/4 Ausgänge

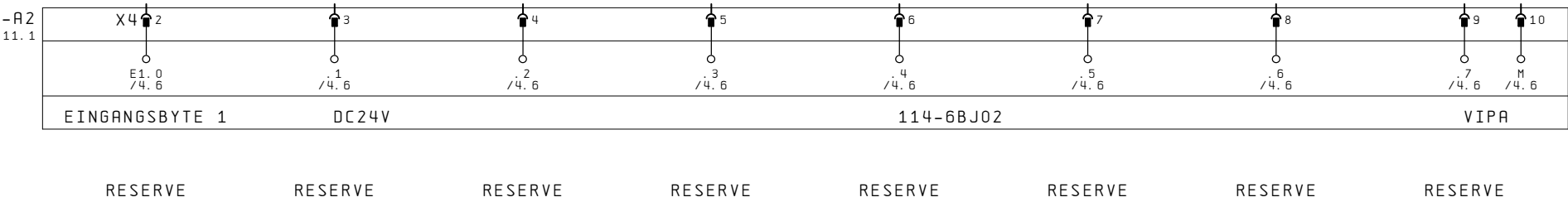


			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 114 DC24V, 114-6BJ02	VIPA100V		=SYSTEM100V +114_6BJ02			
			Bearb.	ZBW									B1.	11
			Geänd.											14 B1.
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V				



0	1	2	3	4	5	6	7	8	9
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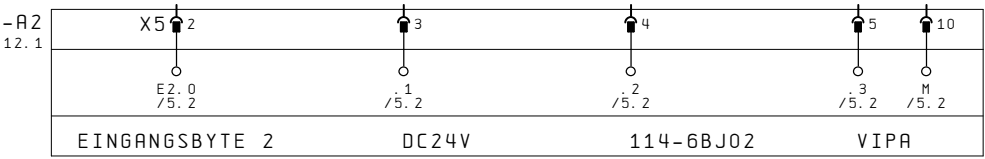
Variante 2: 20 Eingänge/4 Ausgänge



			Datum	07.05.05	Produktmakros für System 100V		<div>VIPA® art of automation</div>	Eingangsbyte 1, CPU 114 DC24V, 114-6BJ02	VIPA100V		=SYSTEM100V +114_6BJ02			
			Bearb.	ZBW										
			Geänd.											
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	12	14 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/4 Ausgänge



RESERVE

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RESERVE

12												14	
			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 2, CPU 114 DC24V, 114-6BJ02		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+114_6BJ02	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	13
												14 B1.	

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/4 Ausgänge

A2 13.1	AUSGANGSBYTE 0		DC24V 0,5A		114-6BJ02		VIPA	
	/5.2 L+	/5.2 AO.4	/5.2 .5	/5.2 .6	/5.2 .7			
	X5 1	6	7	8	9			

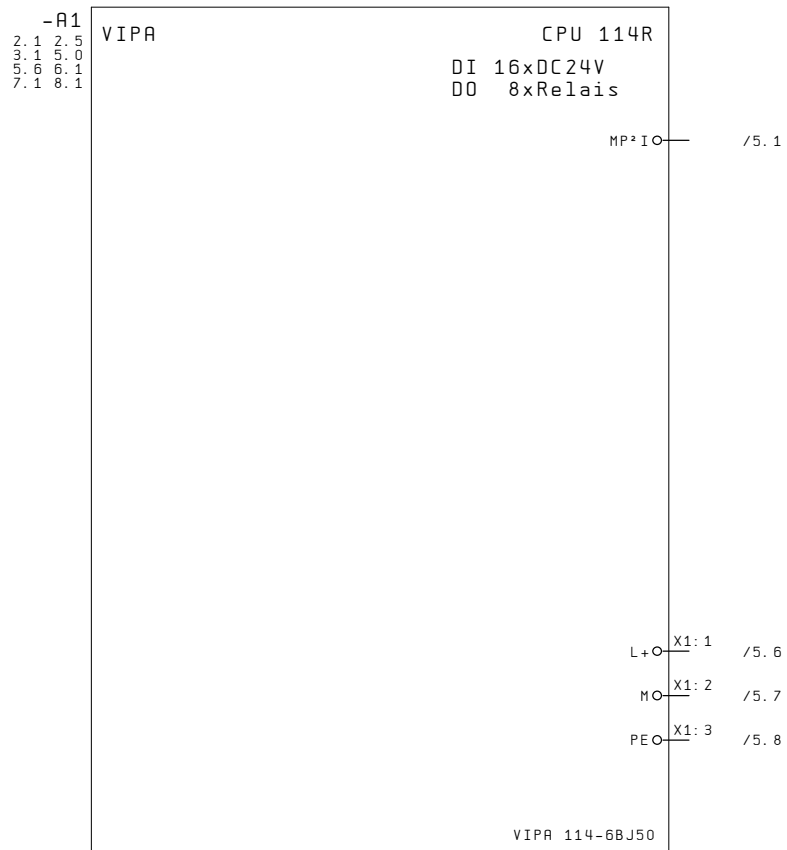
RESERVE

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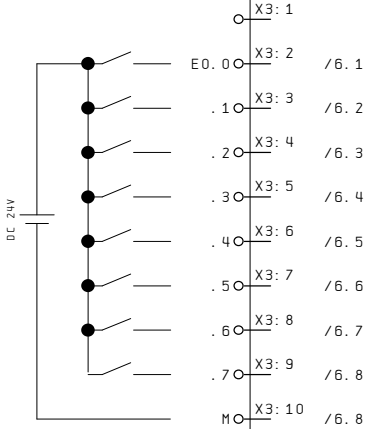
RESERVE

			Datum	07.05.05	Produktmakros für System 100V			Ausgangsbyte 0, CPU 114 DC24V, 114-6BJ02	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+114_6BJ02	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	14
											14 B1.	



-R1  
1.1

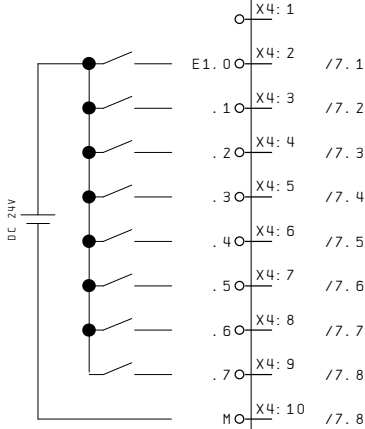
DI 8xDC24V



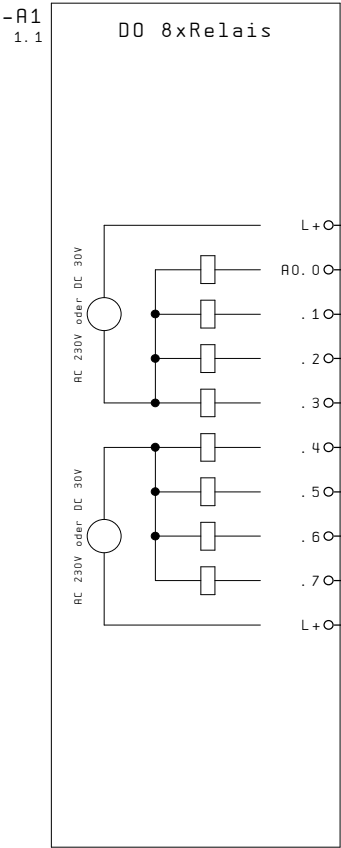
RESERVE  
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RESERVE

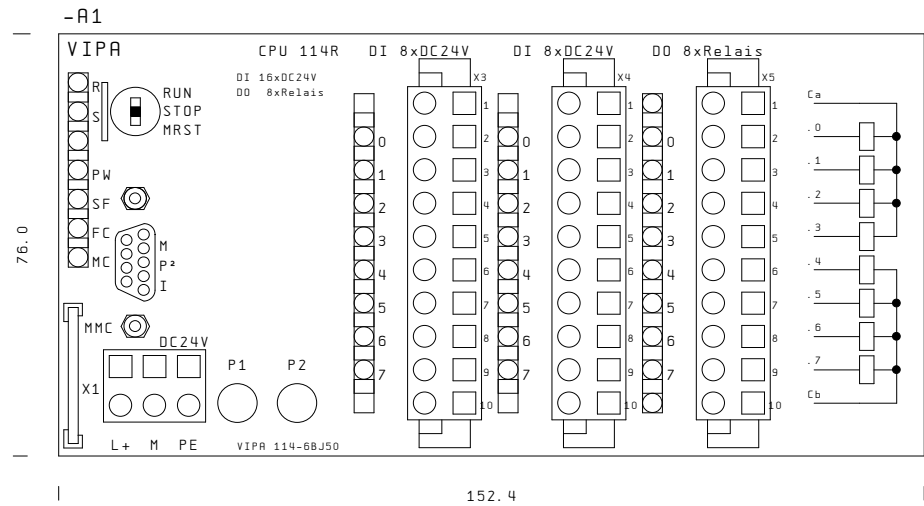
-R1  
1.1

DI 8xDC24V

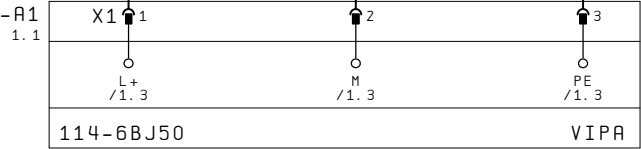
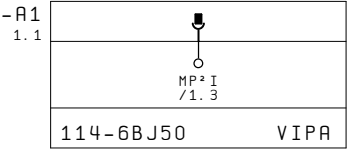


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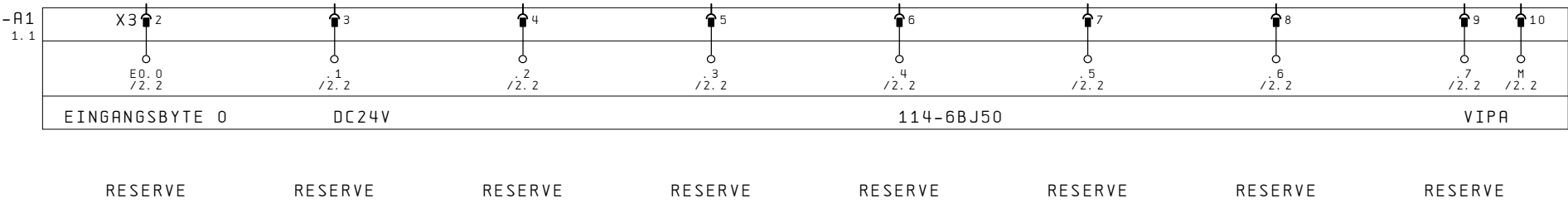


CPU 114R  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

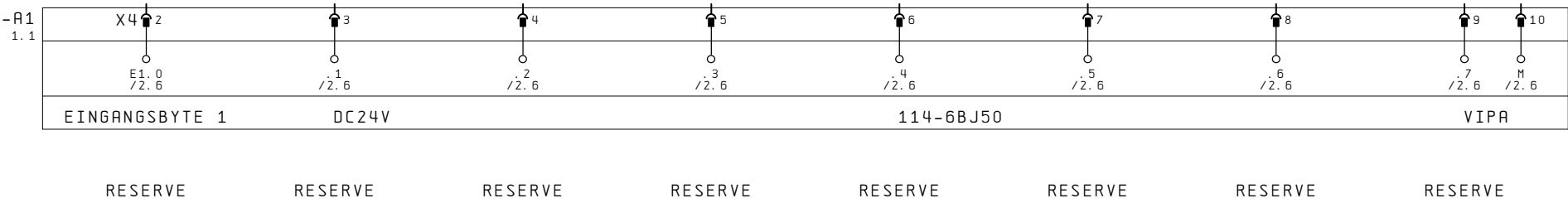




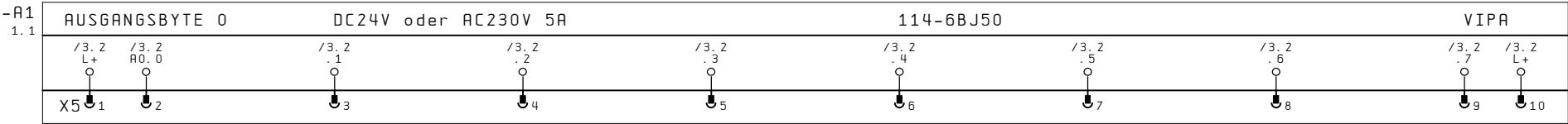
0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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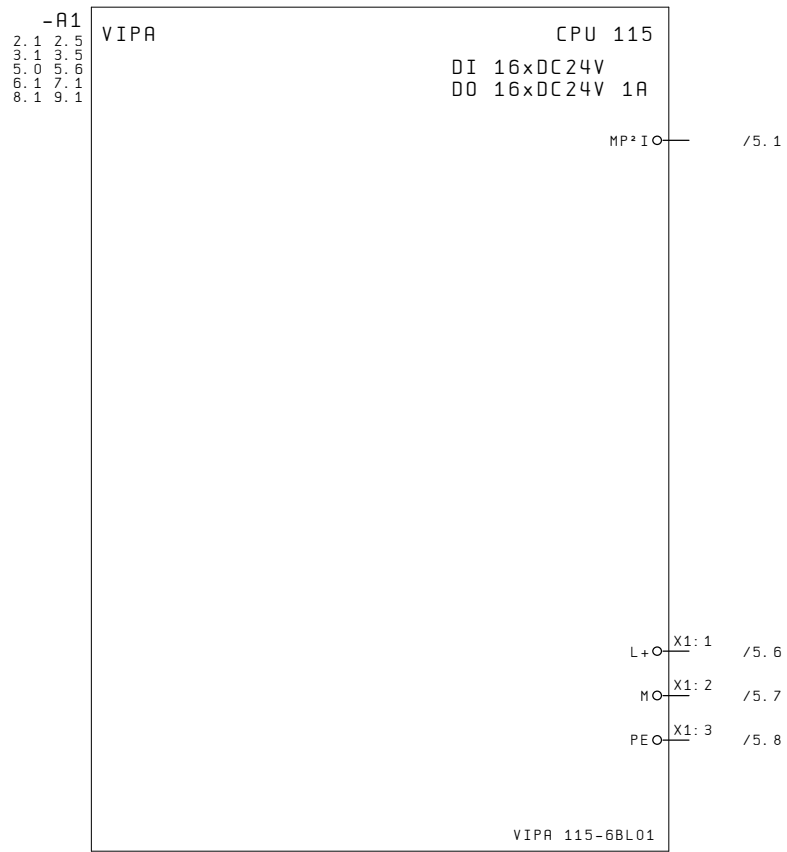
RESERVE

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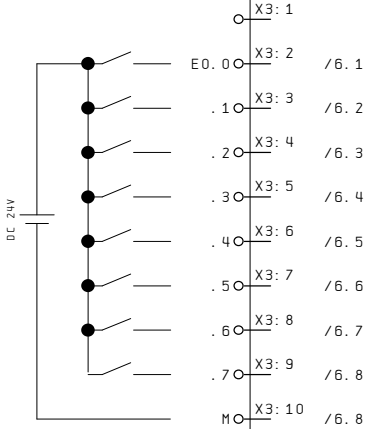
RESERVE

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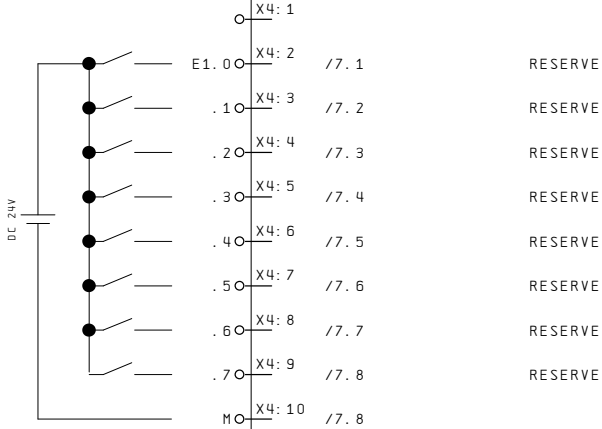
-R1  
1.1

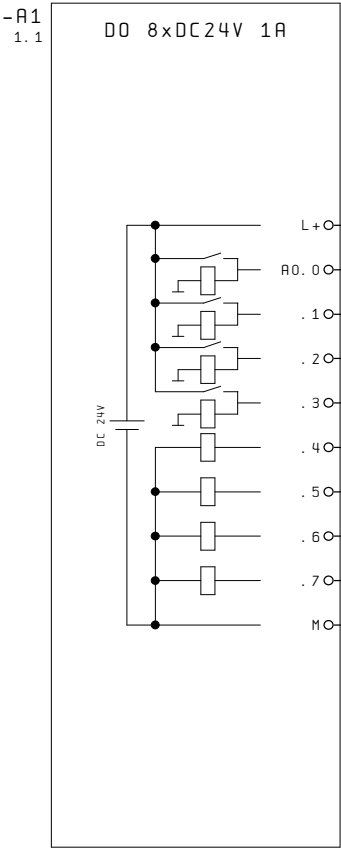
DI 8xDC24V



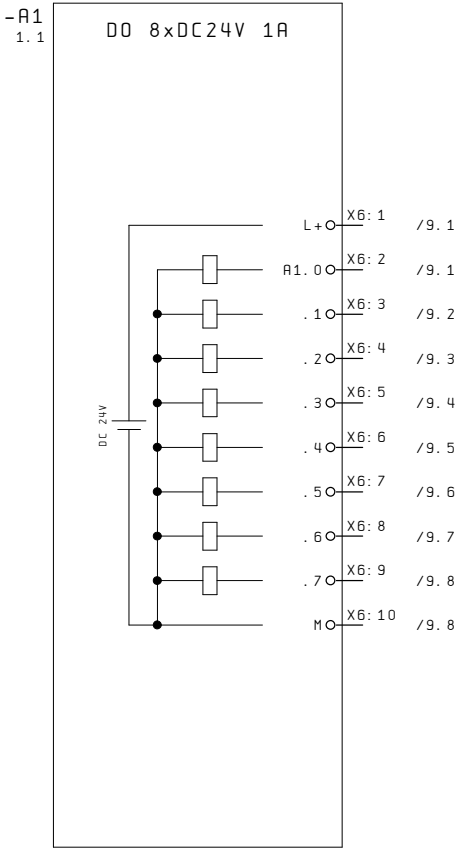
-R1  
1.1

DI 8xDC24V

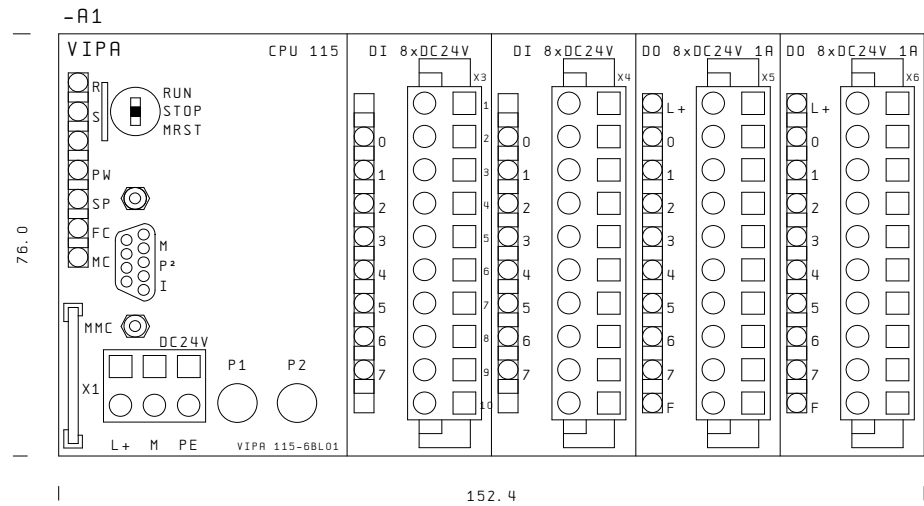




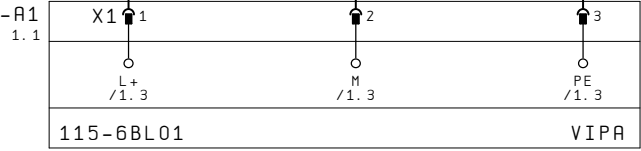
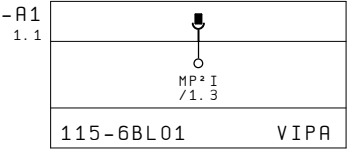
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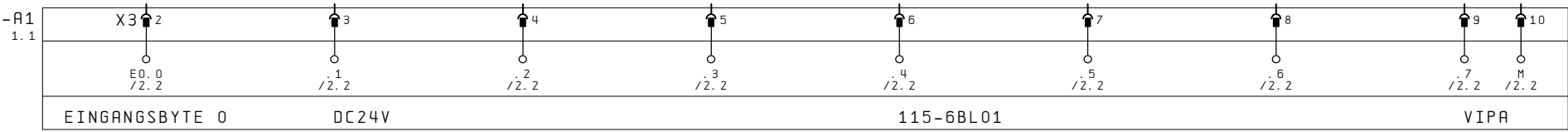


CPU 115  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48





0	1	2	3	4	5	6	7	8	9
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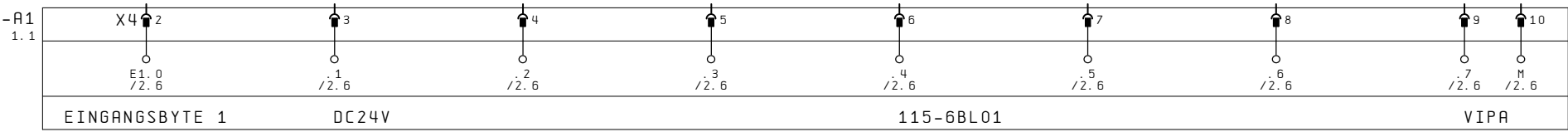
RESERVE

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0	1	2	3	4	5	6	7	8	9
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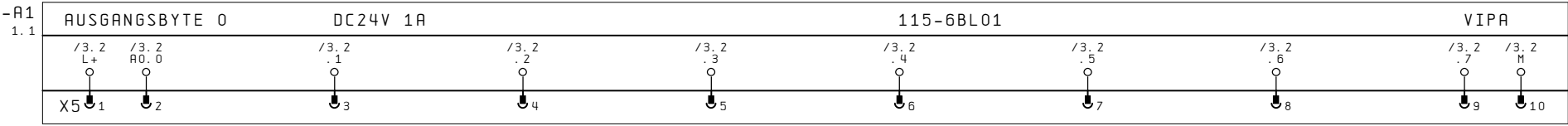
RESERVE

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0	1	2	3	4	5	6	7	8	9
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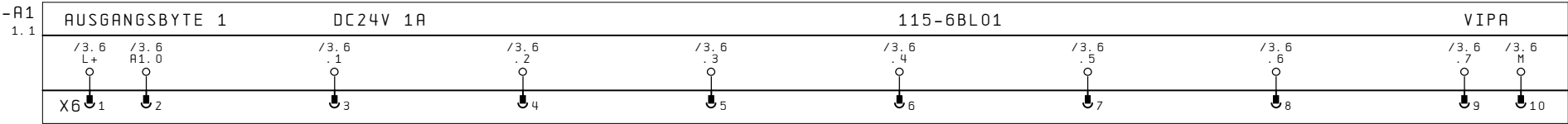
RESERVE

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0	1	2	3	4	5	6	7	8	9
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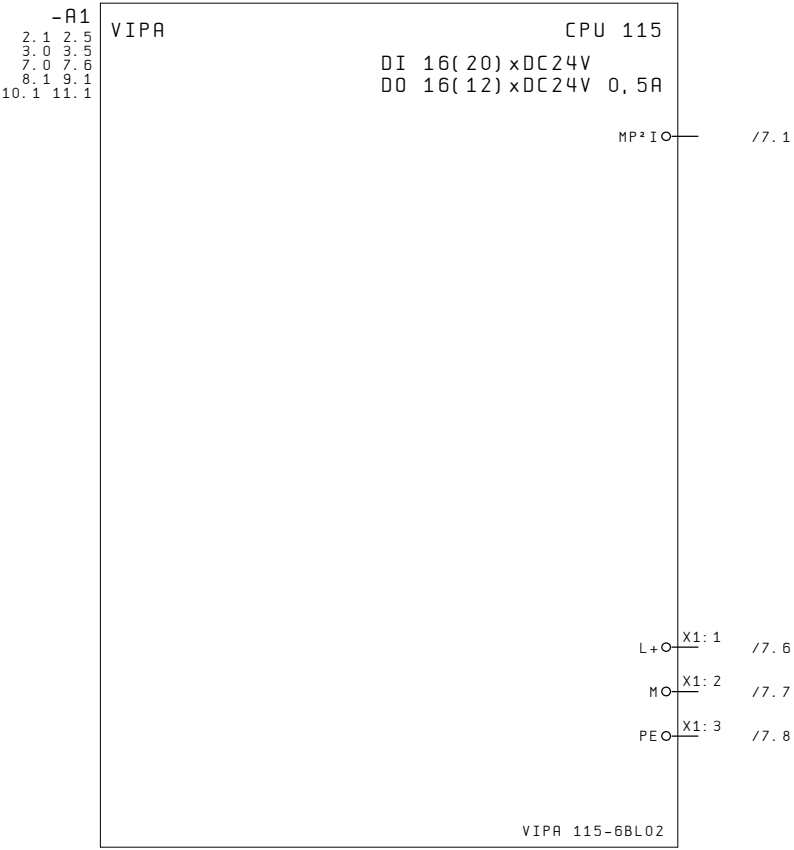
RESERVE

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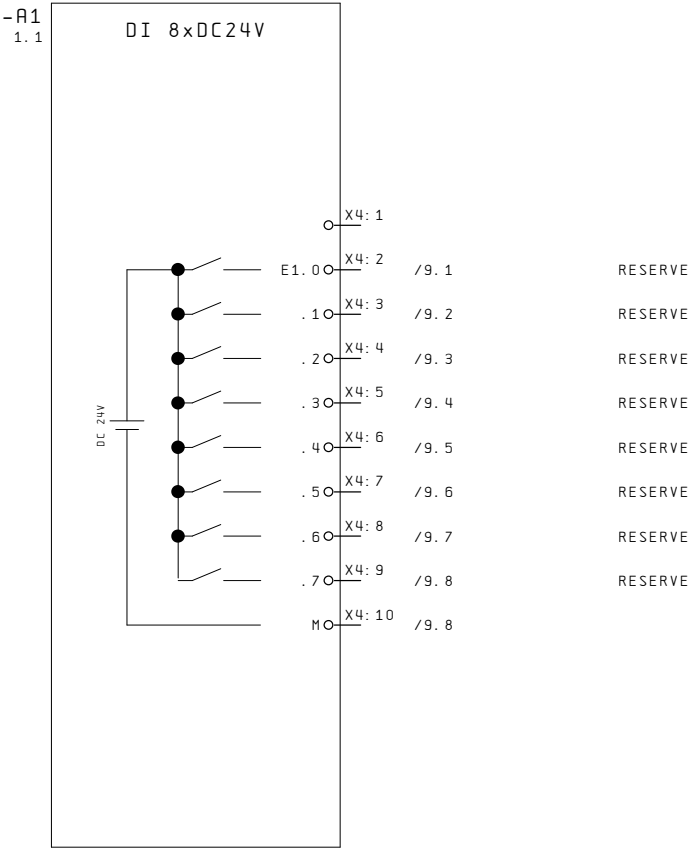
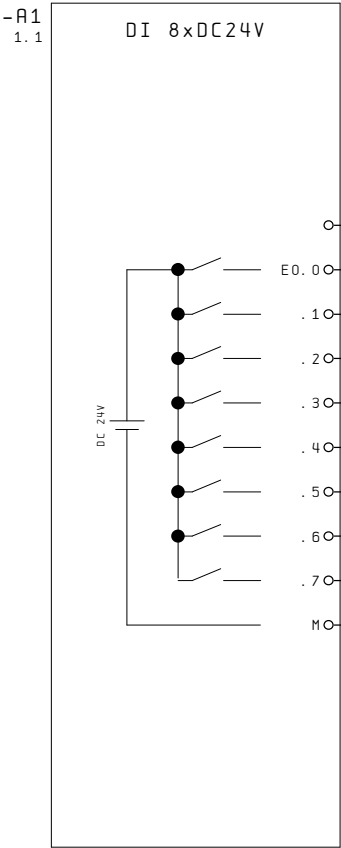
RESERVE

RESERVE



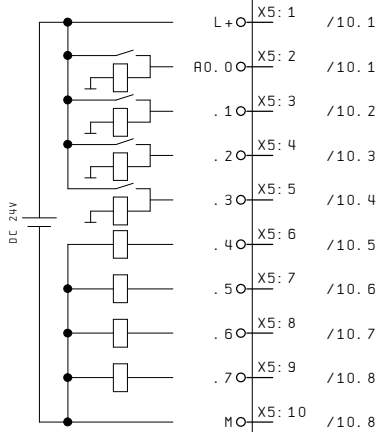
			Datum	07.05.05	Produktmakros für System 100V			SPS-Übersicht Versorgung, CPU 115 DC24V, 115-6BL02	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL02	
			Geänd.								B1.	1
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		16 B1.

Variante 1: 16 Eingänge/16 Ausgänge



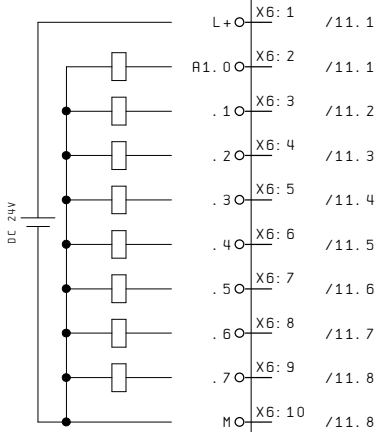
Variante 1: 16 Eingänge/16 Ausgänge

-R1  
1.1 DIO 4/DO 4xDC24V 0,5A



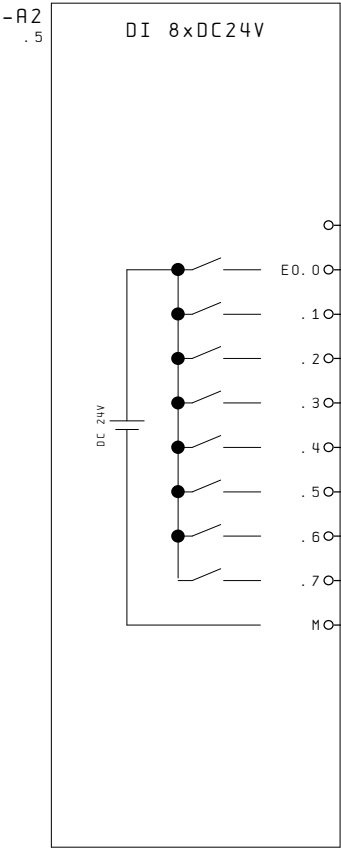
RESERVE  
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RESERVE

-R1  
1.1 DO 8xDC24V 0,5A

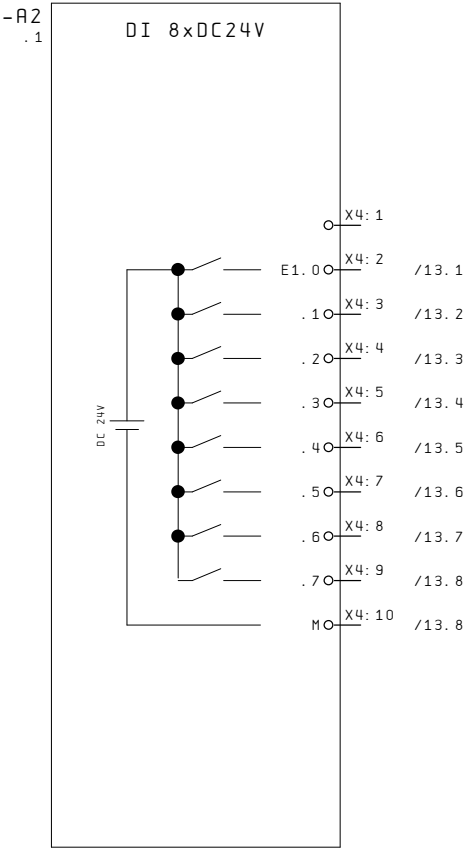


RESERVE  
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Variante 2: 20 Eingänge/12 Ausgänge



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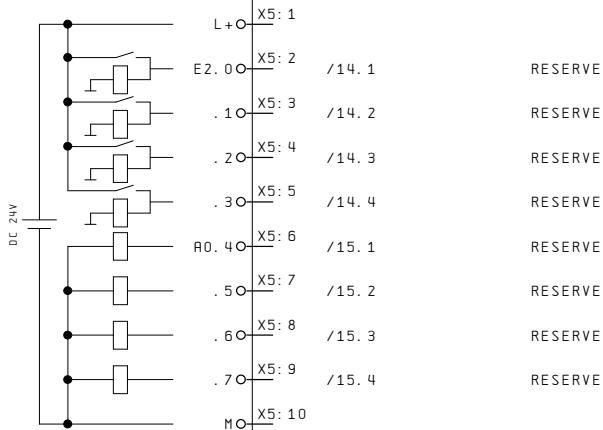


RESERVE  
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RESERVE  
RESERVE

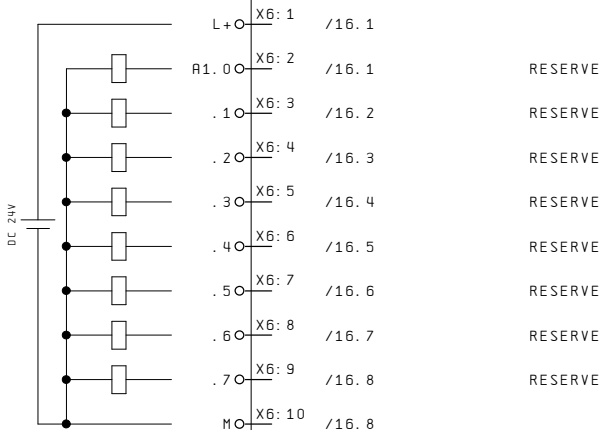


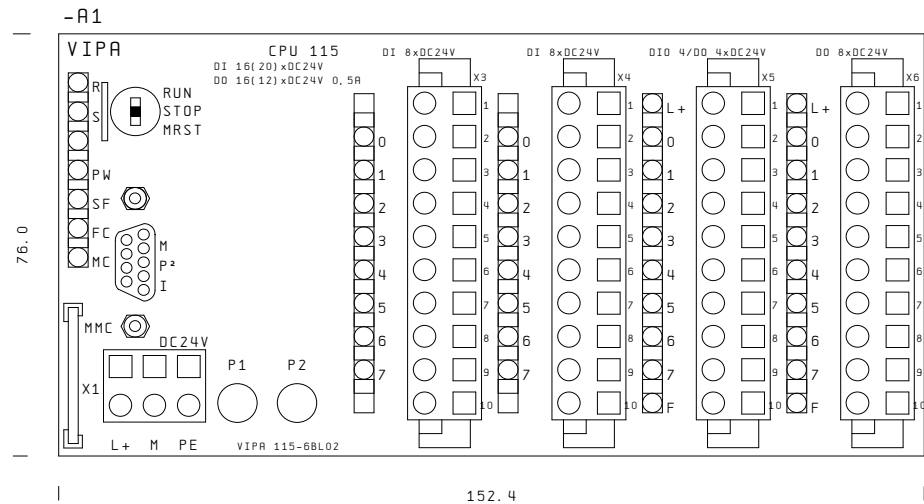
Variante 2: 20 Eingänge/12 Ausgänge

-A2  
4.5 DIO 4/DO 4xDC24V 0,5A

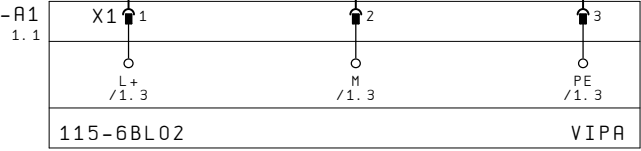
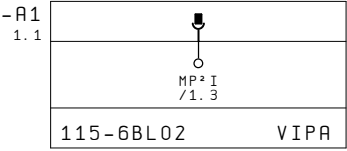


-A2  
1.0 DO 8xDC24V 0,5A





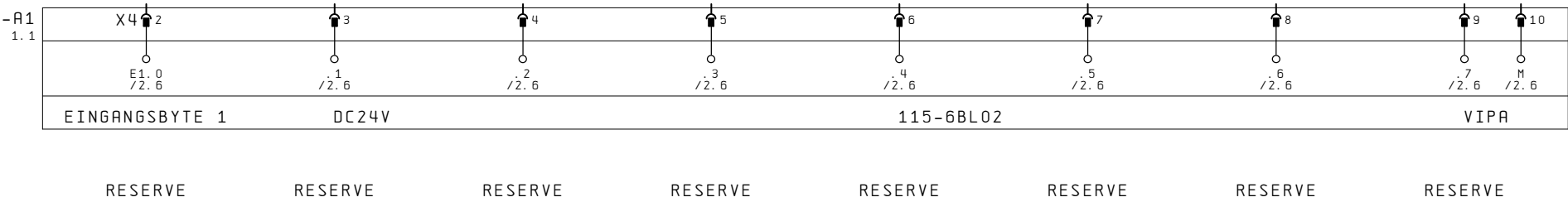
CPU 115  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48





0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge

.4  
/2.6.5  
/2.6.6  
/2.6.7  
/2.6M  
/2.6

EINGANGSBYTE 1

DC24V

115-6BL02

VIPA

RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

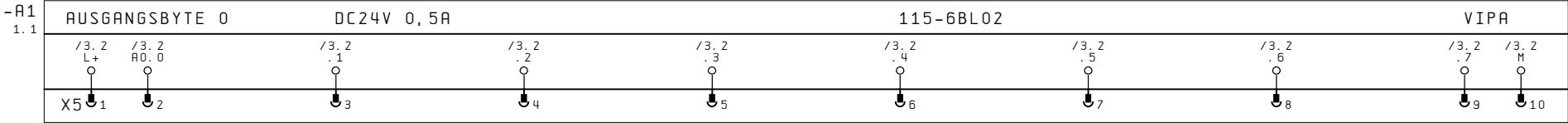
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

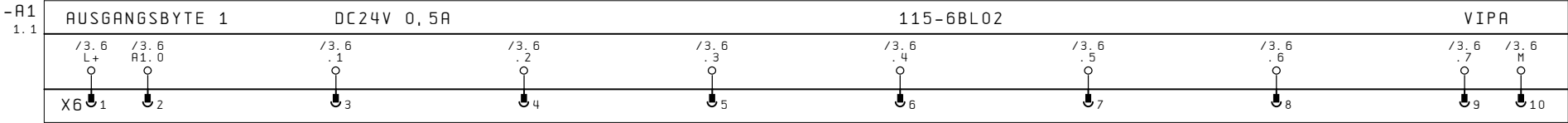
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



RESERVE

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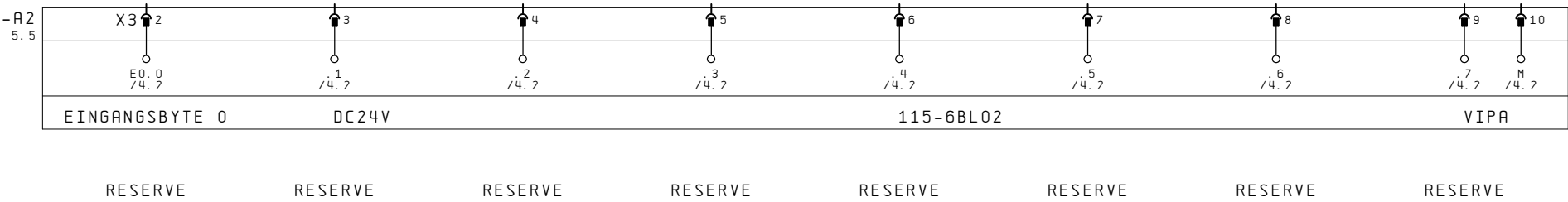
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge

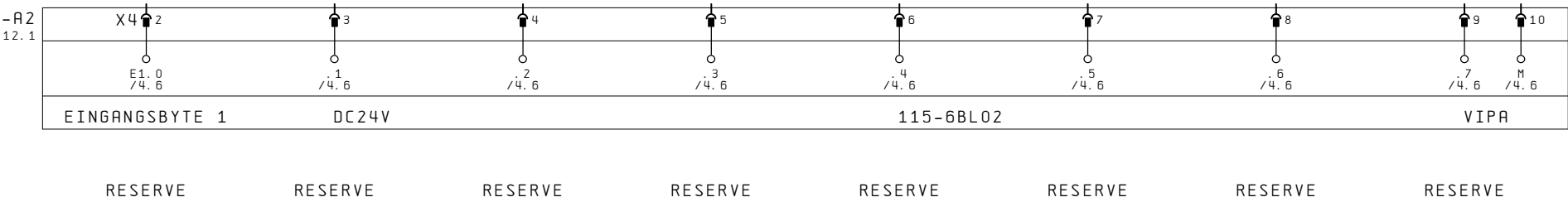


			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 115 DC24V, 115-6BL02	VIPA100V		=SYSTEM100V +115_6BL02			
			Bearb.	ZBW									B1.	12
			Geänd.											16 B1.
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V				



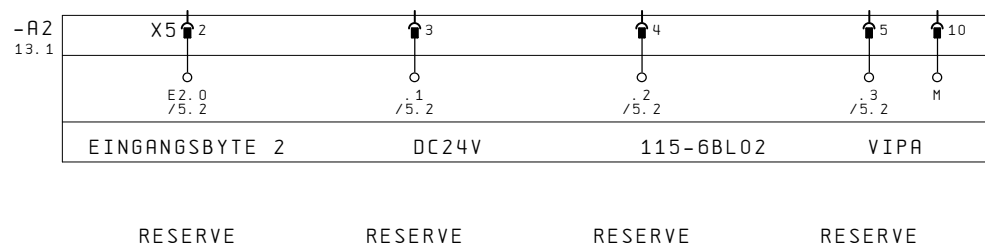
0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge



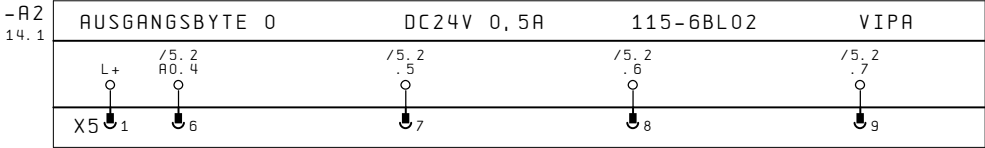
			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 1, CPU 115 DC24V, 115-6BL02	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL02	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1. 13 16 B1.

Variante 2: 20 Eingänge/12 Ausgänge



0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge



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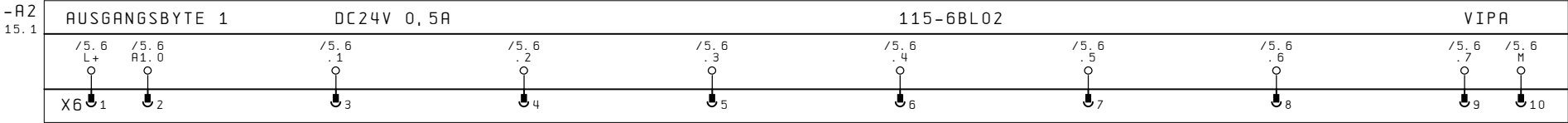
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			Datum	07.05.05	Produktmakros für System 100V			Ausgangsbyte 0, CPU 115 DC24V, 115-6BL02		VIPA100V		=SYSTEM100V			
			Bearb.	ZBW								+115_6BL02			
			Geänd.												
Änderung	Datum	Name	Form		Unspr.	Ers. f.	Ers. d.		System 100V		B1.	15			
											16 B1.				

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge



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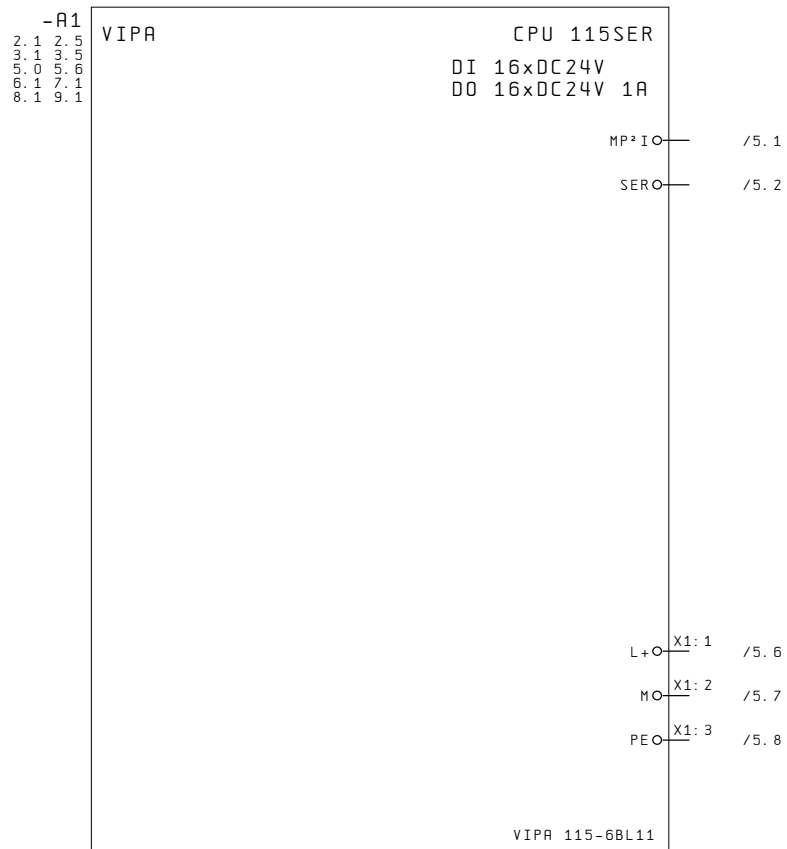
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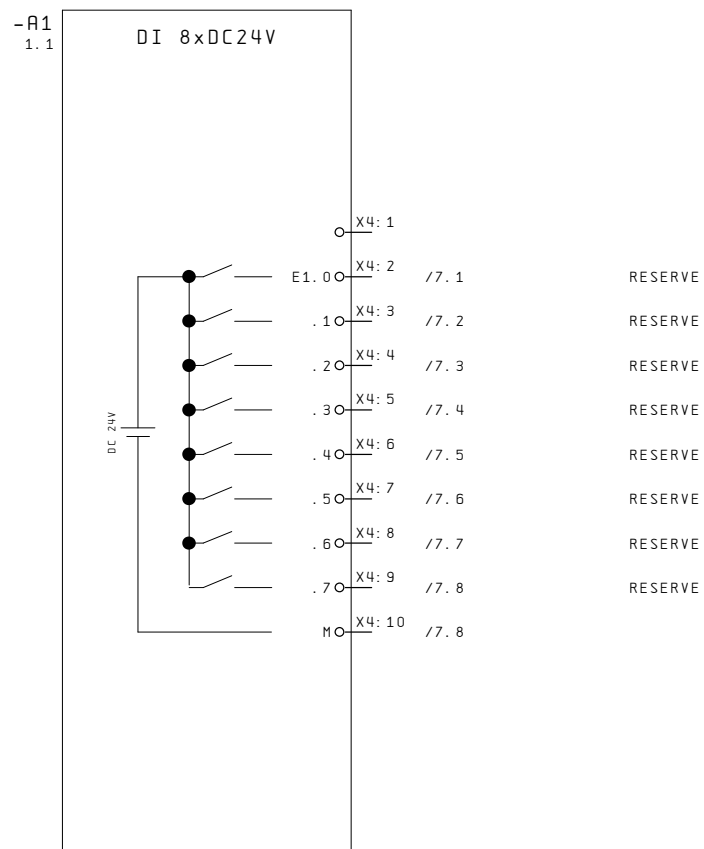
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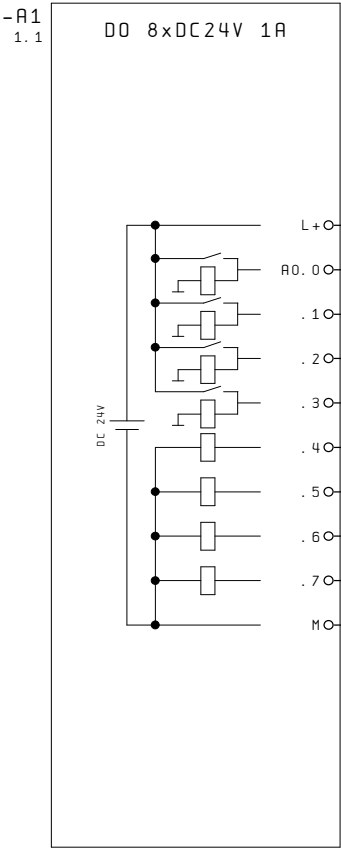
			Datum	07.05.05	Produktmakros für System 100V			Ausgangsbyte 1, CPU 115 DC24V, 115-6BL02	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL02	
			Geänd.							System 100V		B1. 16
Änderung	Datum	Name	Form		Unspr.	Ers. f.	Ers. d.					16 B1.

0	1	2	3	4	5	6	7	8	9
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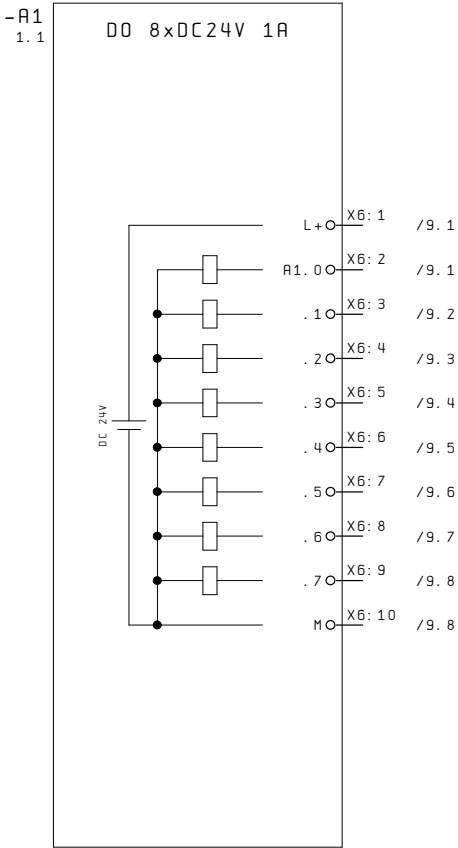


+115_6BL02/16													2
			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, CPU 115SER DC24V, 115-6BL11		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+115_6BL11	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	1	
											9 B1.		

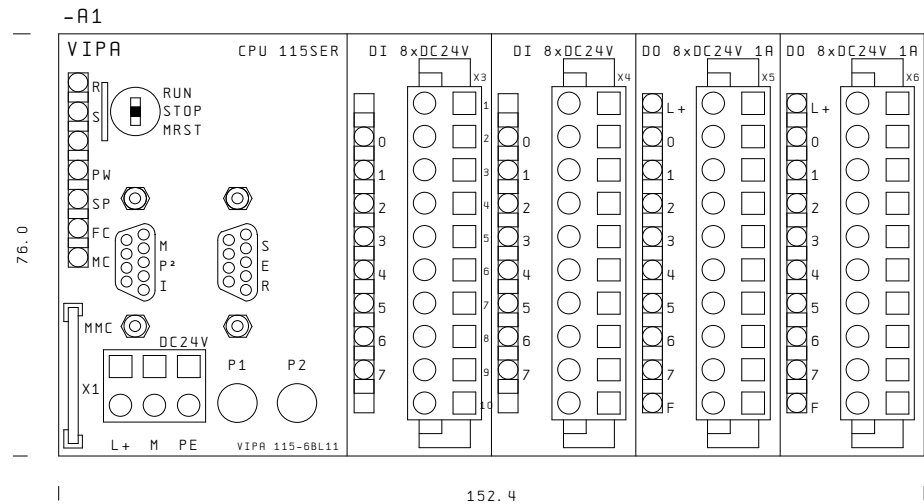




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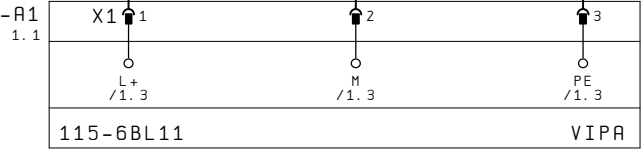
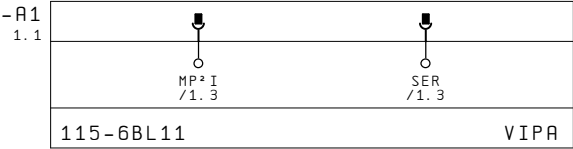


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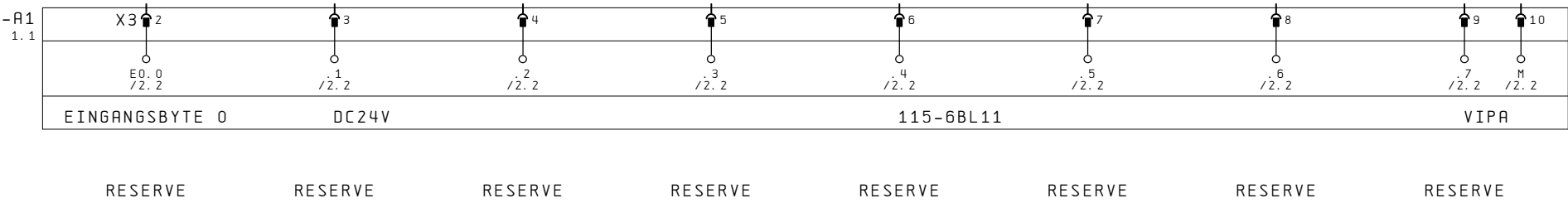


CPU 115SER  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

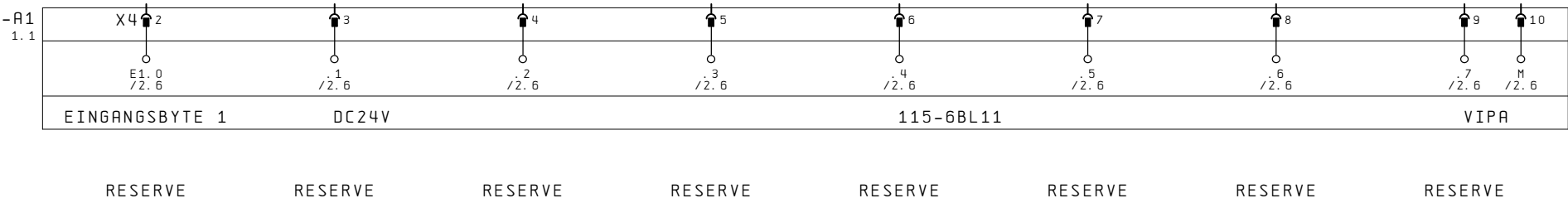




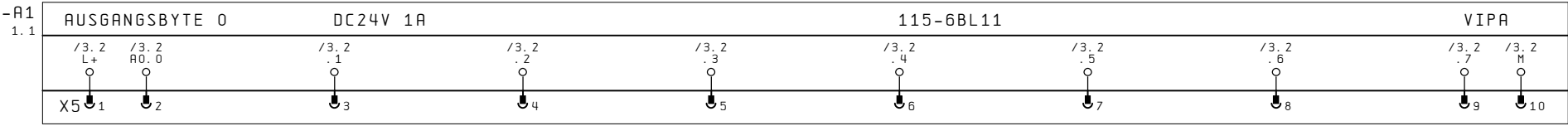
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0	1	2	3	4	5	6	7	8	9
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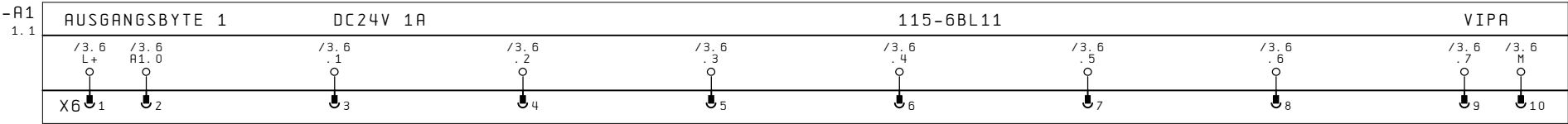
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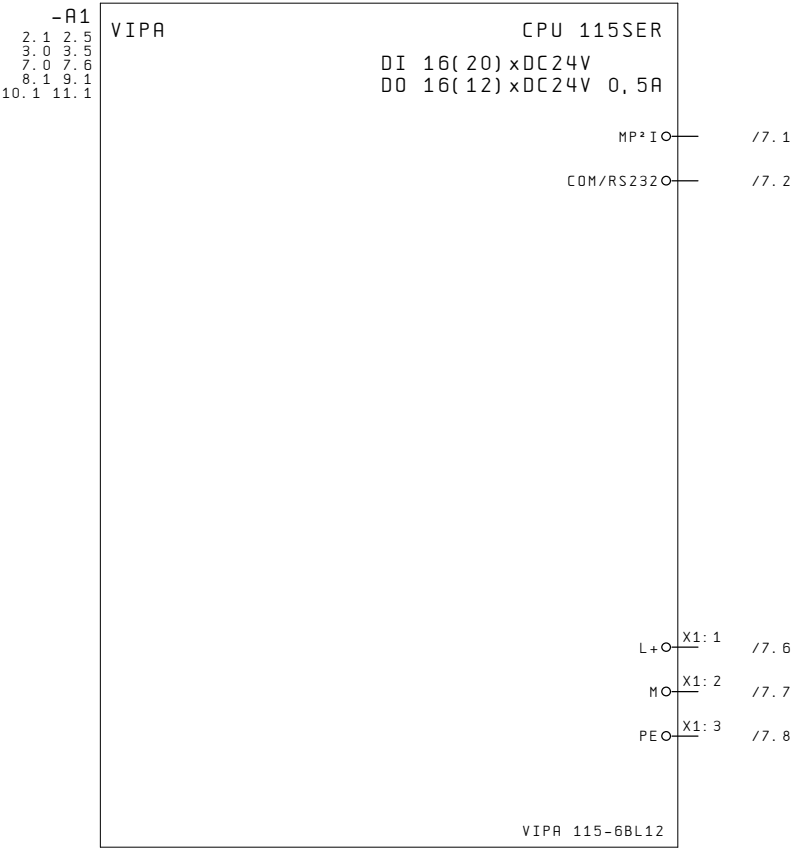
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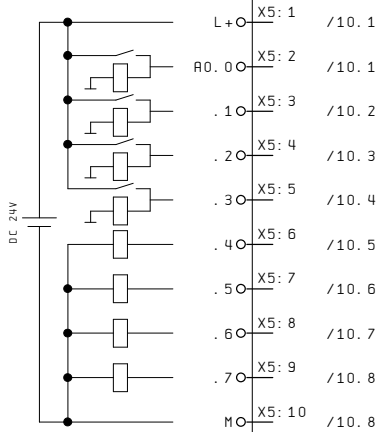


			Datum	07.05.05	Produktmakros für System 100V			SPS-Übersicht Versorgung, CPU 115SER DC24V, 115-6BL12	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL12	
			Geänd.								B1.	1
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		16 B1.



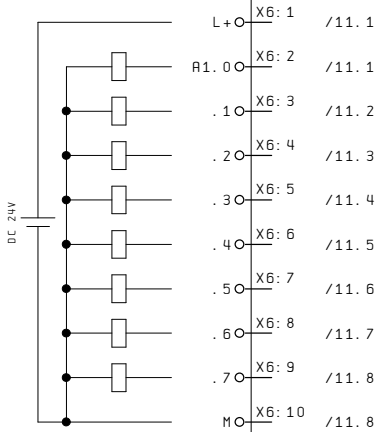
Variante 1: 16 Eingänge/16 Ausgänge

-R1  
1.1 DIO 4/DO 4xDC24V 0,5A



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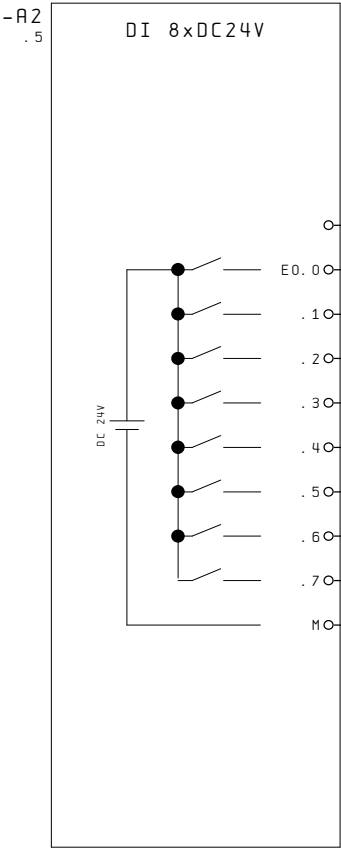
-R1  
1.1 DO 8xDC24V 0,5A



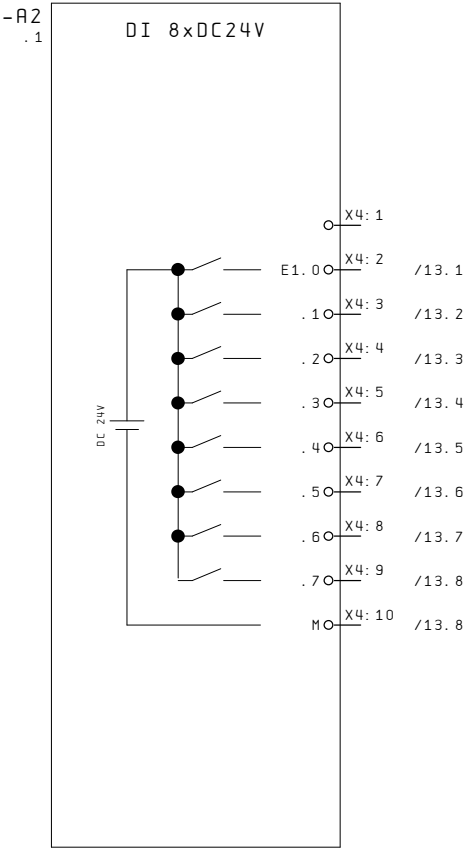
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Variante 2: 20 Eingänge/12 Ausgänge



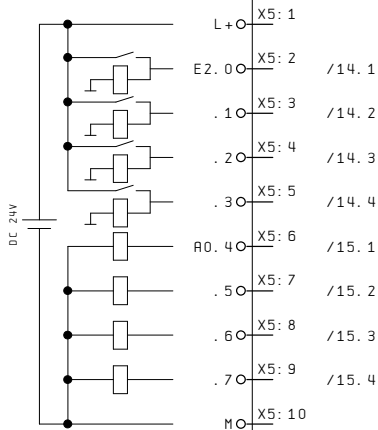
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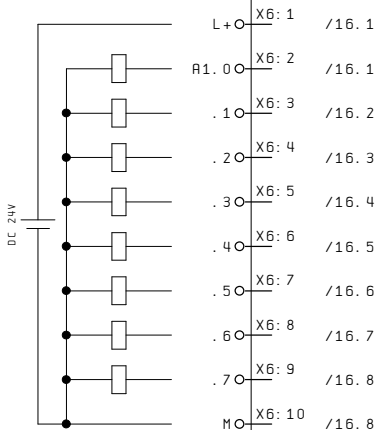
Variante 2: 20 Eingänge/12 Ausgänge

-A2  
4.5 DIO 4/DO 4xDC24V 0,5A

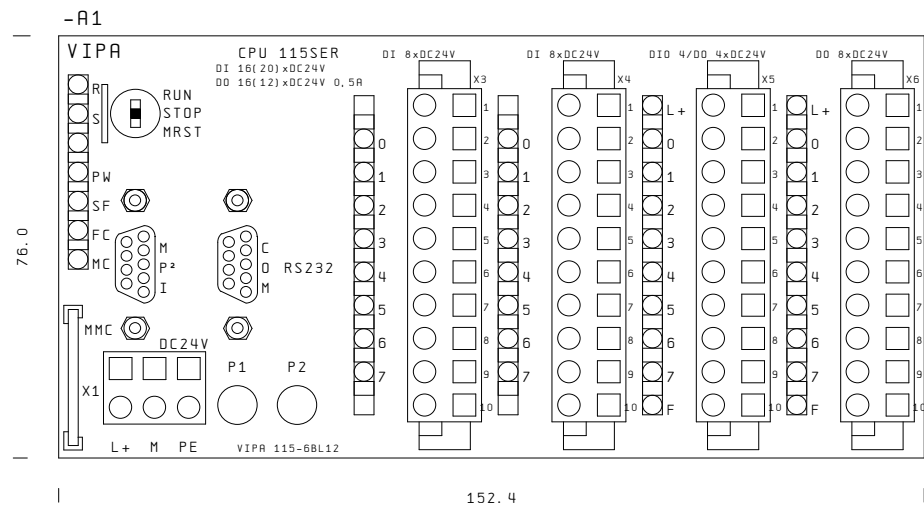


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-A2  
1.0 DO 8xDC24V 0,5A

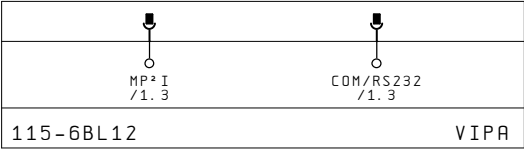


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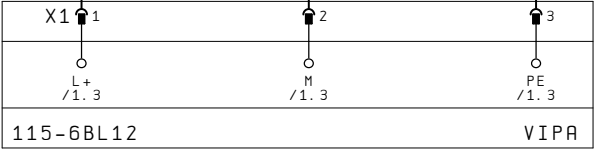


CPU 115SER  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

-R1  
1.1

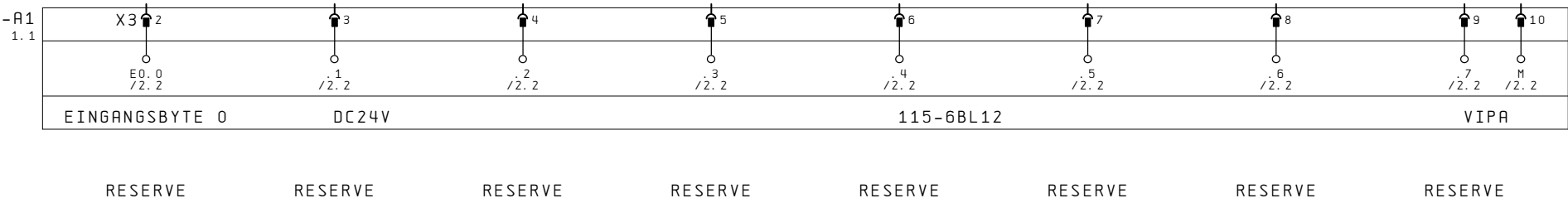


-R1  
1.1



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Variante 1: 16 Eingänge/16 Ausgänge

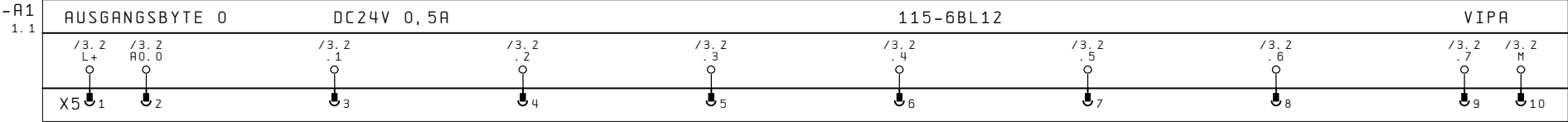


			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 115SER DC24V, 115-6BL12	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL12	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1. 8 16 B1.



0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



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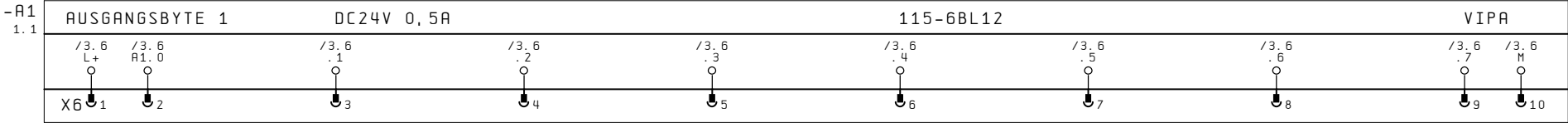
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Variante 1: 16 Eingänge/16 Ausgänge



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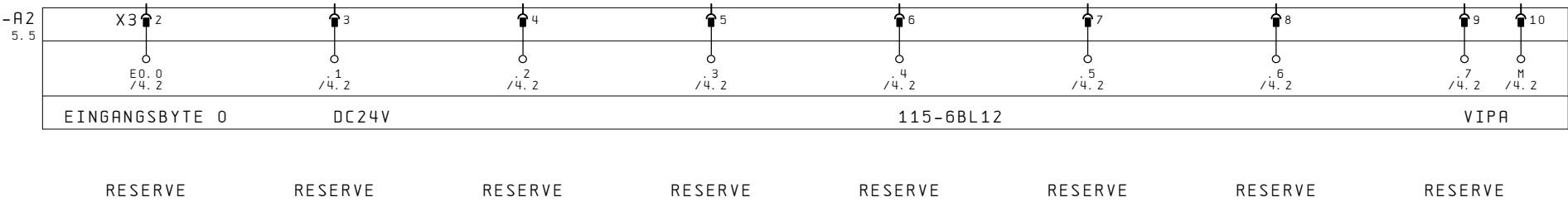
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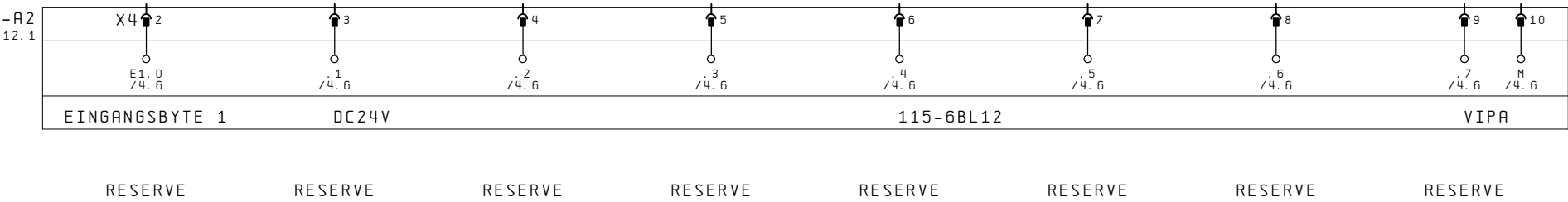
Variante 2: 20 Eingänge/12 Ausgänge



			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 115SER DC24V, 115-6BL12	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL12	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1. 12 16 B1.

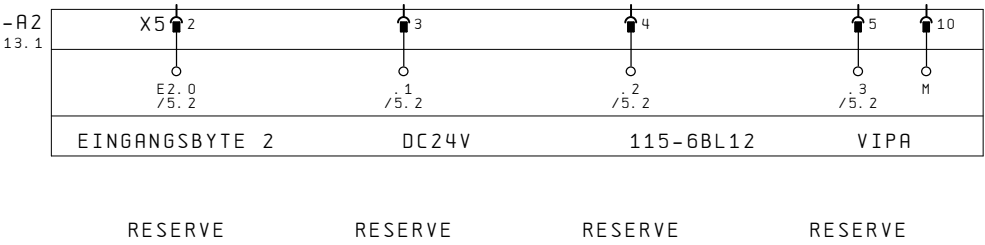
0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge



			Datum	07.05.05	Produktmakros für System 100V		 art of automation	Eingangsbyte 1, CPU 115SER DC24V, 115-6BL12	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL12	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	13
												16 B1.

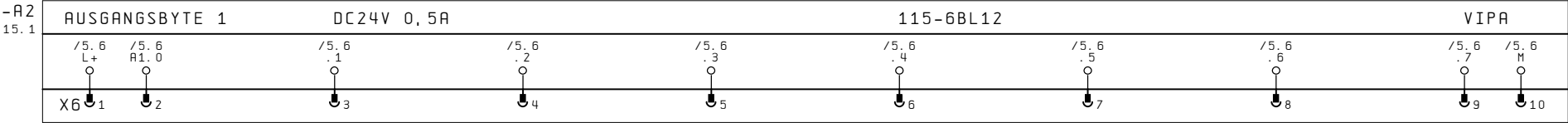
Variante 2: 20 Eingänge/12 Ausgänge





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Variante 2: 20 Eingänge/12 Ausgänge



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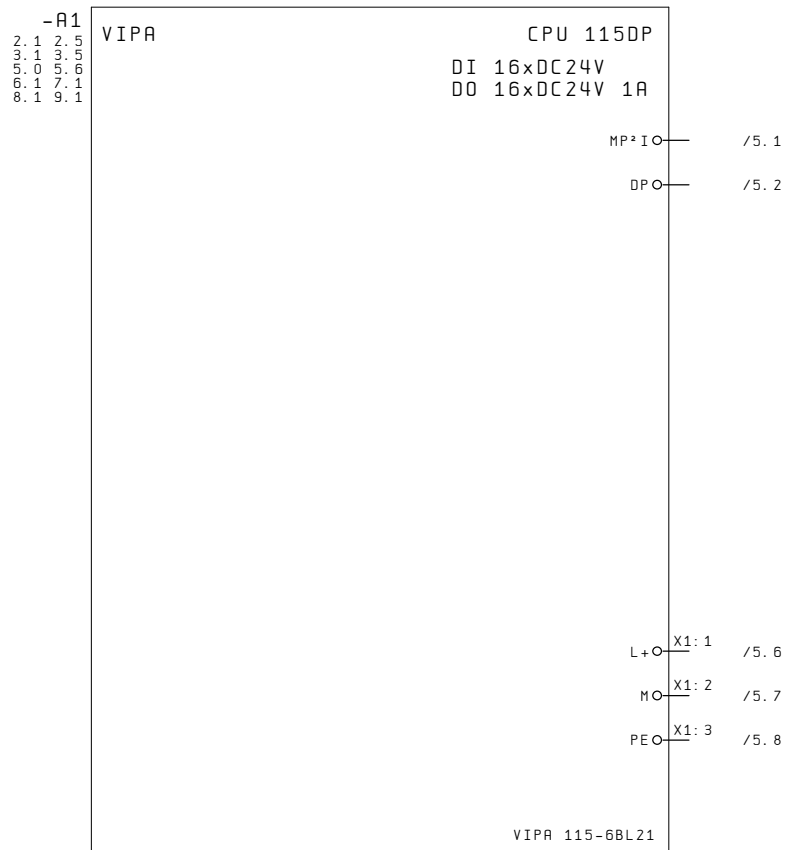
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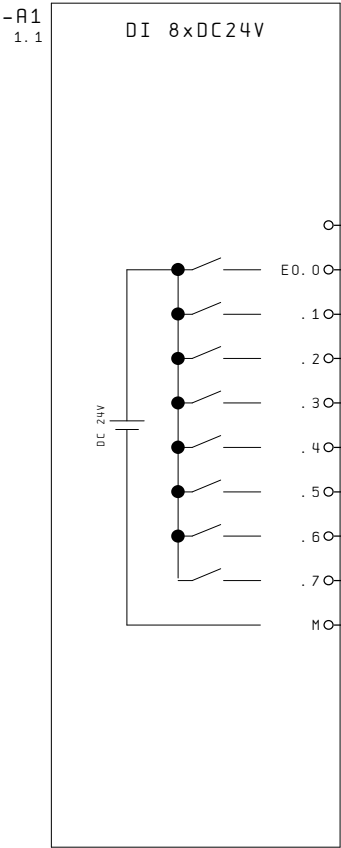
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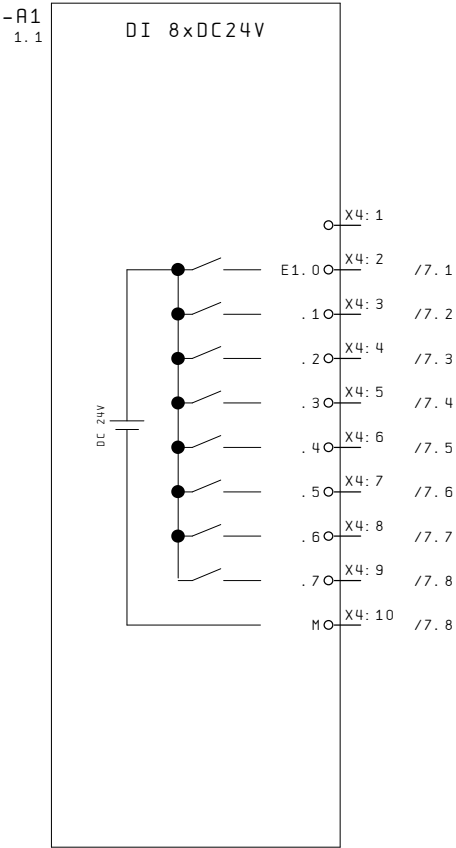
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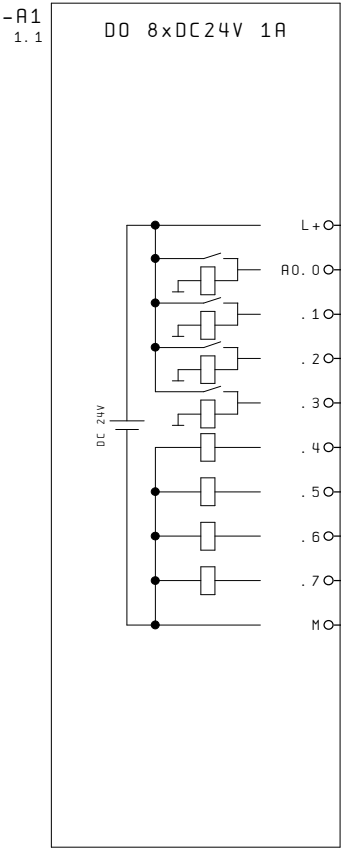




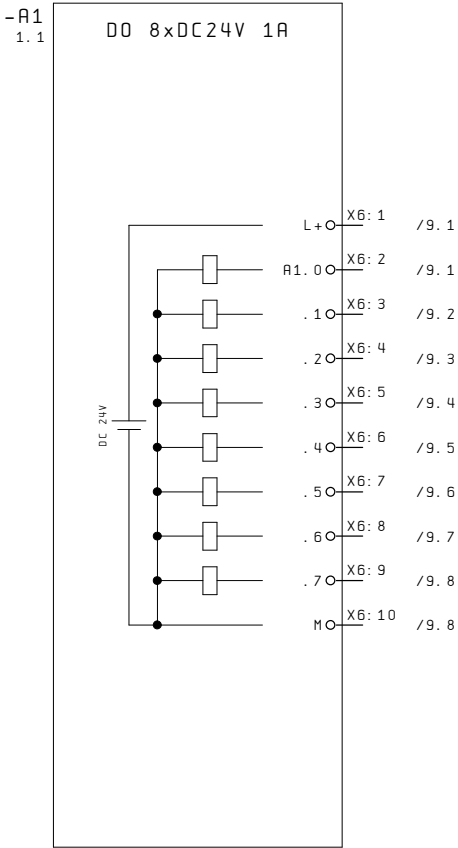
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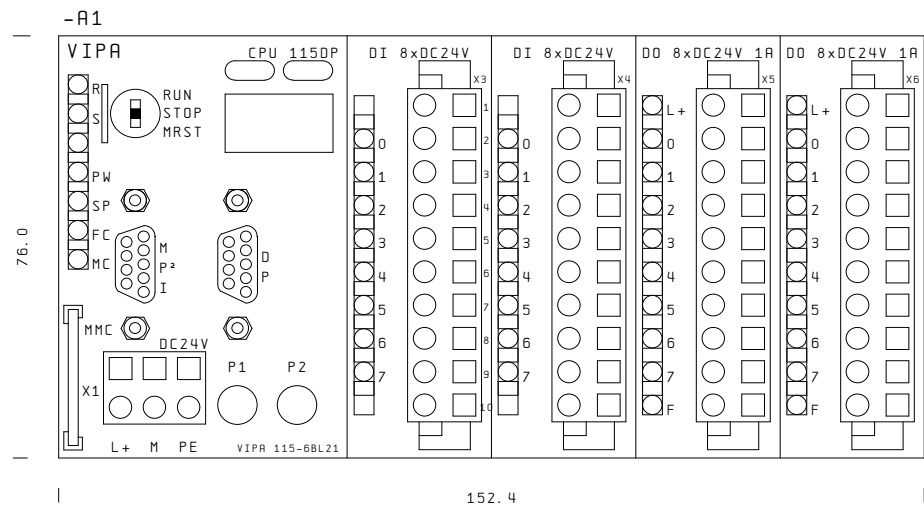


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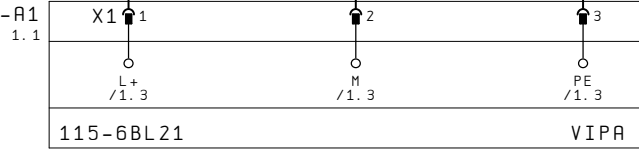
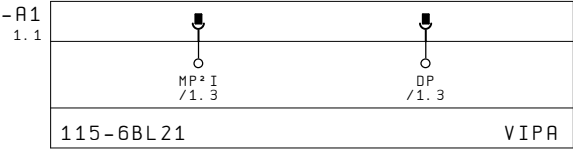


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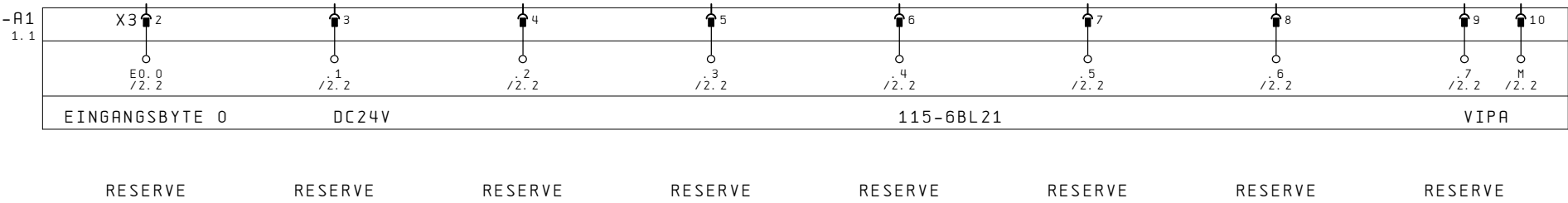




CPU 115DP  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

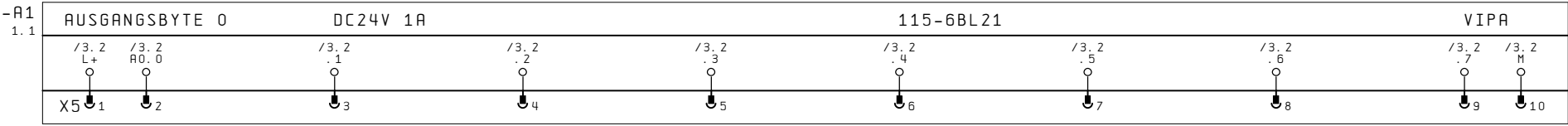


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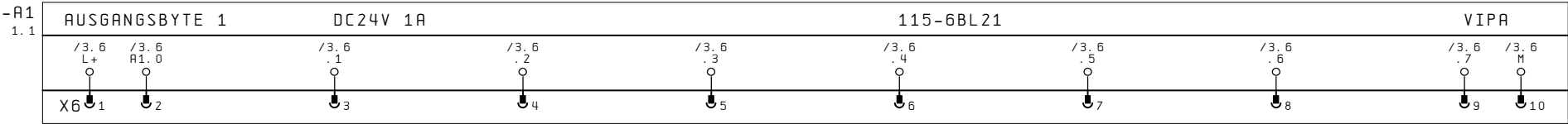
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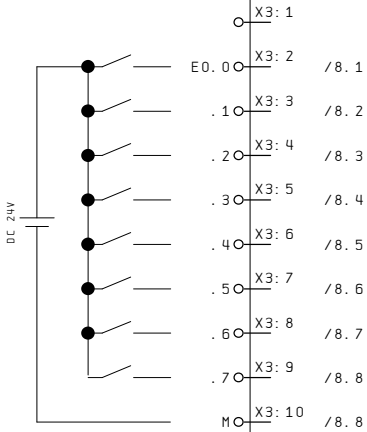


			Datum	07.05.05	Produktmakros für System 100V			SPS-Übersicht Versorgung, CPU 115DP DC24V, 115-6BL22	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL22	
			Geänd.								B1.	1
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		16 B1.

Variante 1: 16 Eingänge/16 Ausgänge

-R1  
1.1

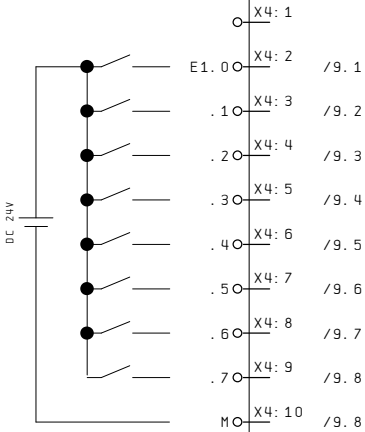
DI 8xDC24V



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-R1  
1.1

DI 8xDC24V

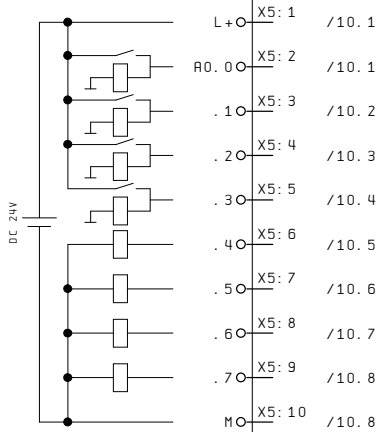


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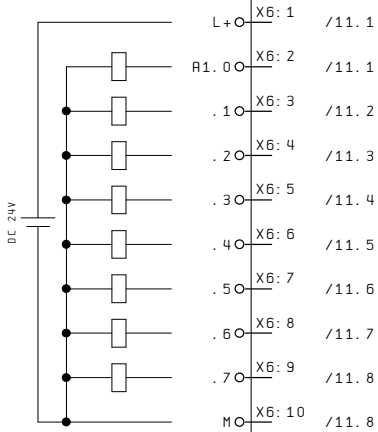
Variante 1: 16 Eingänge/16 Ausgänge

-R1  
1.1 DIO 4/DO 4xDC24V 0,5A



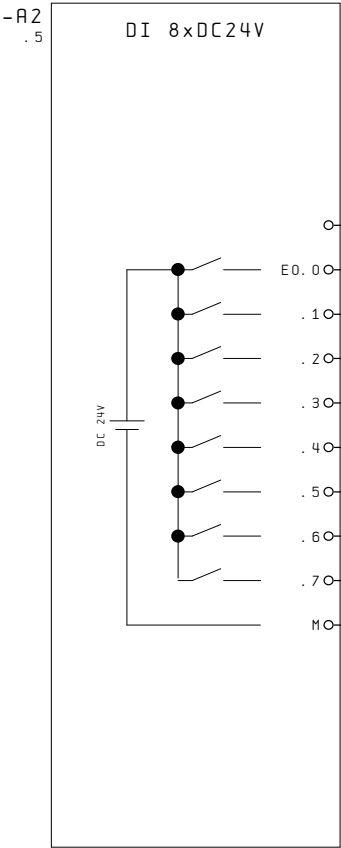
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-R1  
1.1 DO 8xDC24V 0,5A

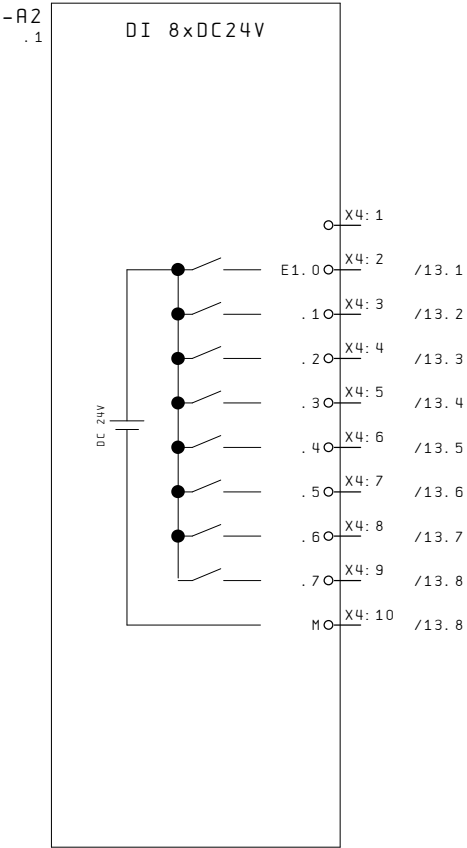


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Variante 2: 20 Eingänge/12 Ausgänge



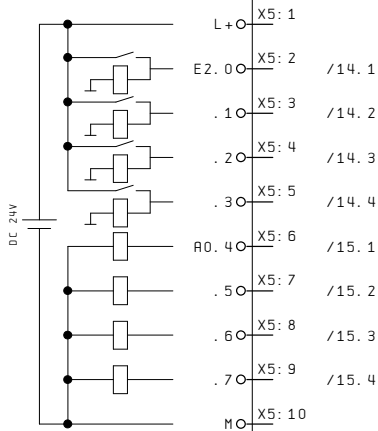
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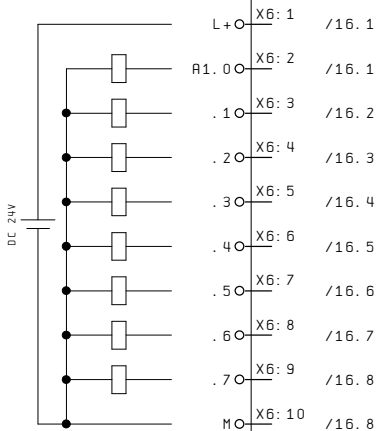
Variante 2: 20 Eingänge/12 Ausgänge

-A2  
4.5 DIO 4/DO 4xDC24V 0,5A

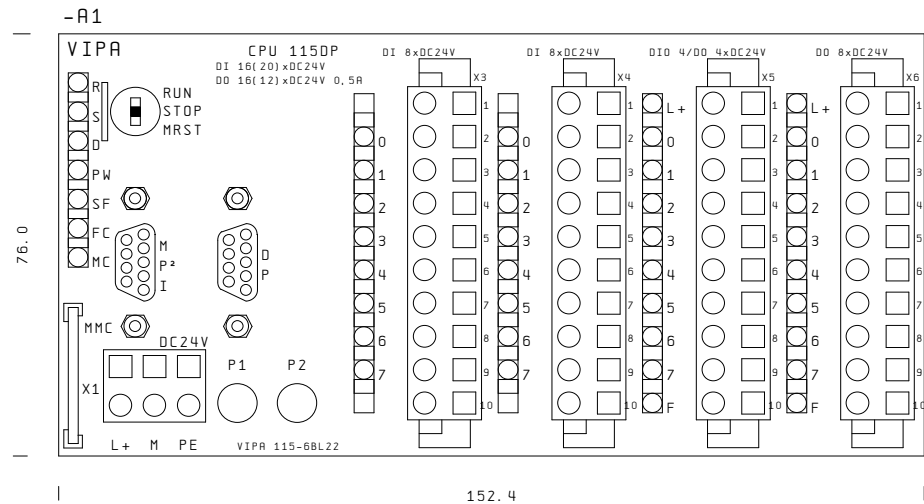


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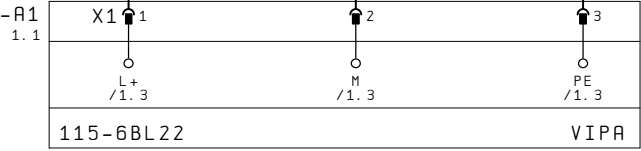
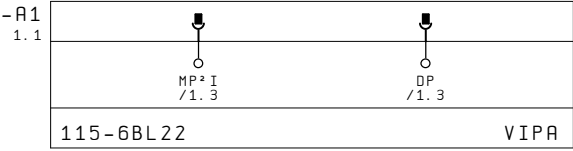
-A2  
1.0 DO 8xDC24V 0,5A



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RESERVE

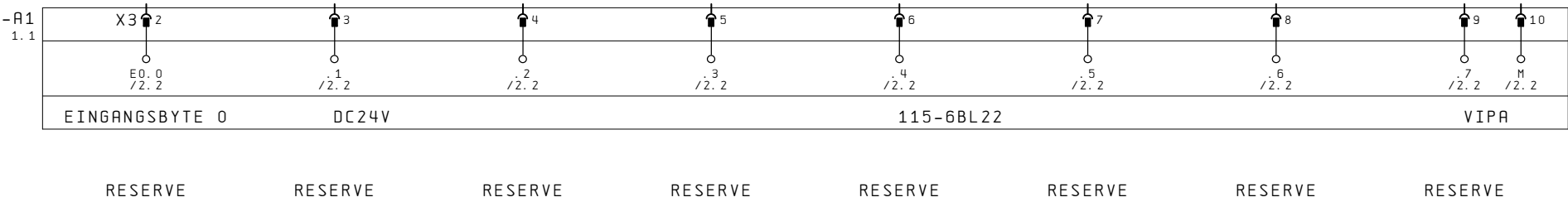


CPU 115DP  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48



0	1	2	3	4	5	6	7	8	9
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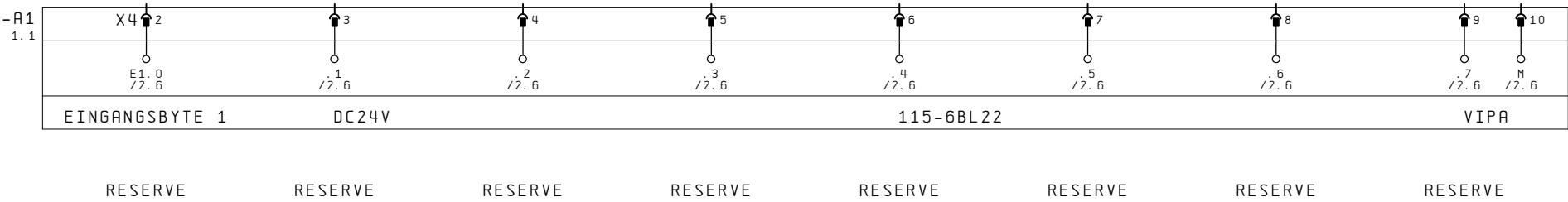
Variante 1: 16 Eingänge/16 Ausgänge



			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 115DP DC24V, 115-6BL22	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL22	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	8
											16 B1.	

0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge

.4  
/2.6.5  
/2.6.6  
/2.6.7  
/2.6M  
/2.6

EINGANGSBYTE 1

DC24V

115-6BL22

VIPA

RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

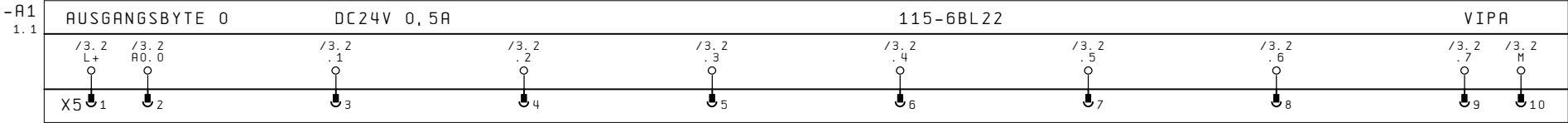
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



RESERVE

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RESERVE

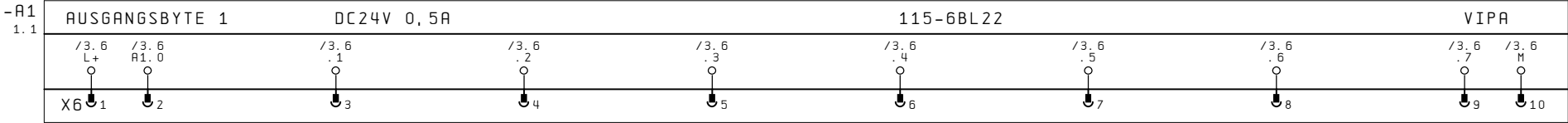
RESERVE

RESERVE



0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



RESERVE

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RESERVE

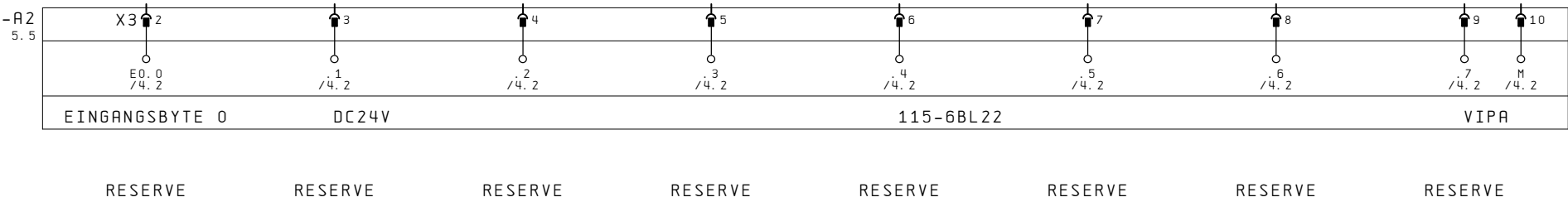
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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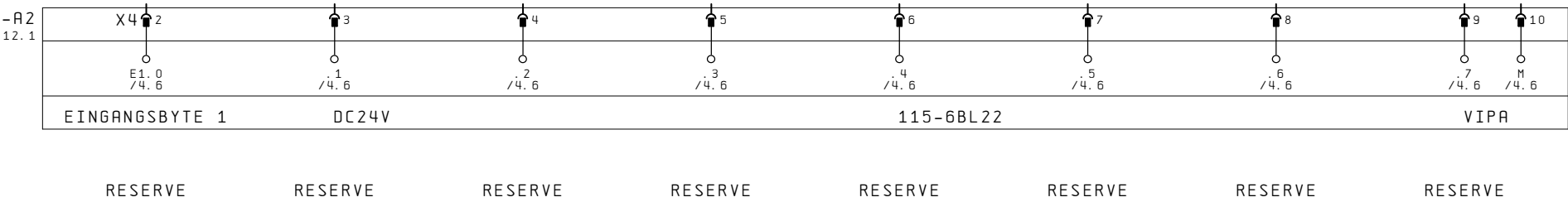
Variante 2: 20 Eingänge/12 Ausgänge



			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 115DP DC24V, 115-6BL22	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL22	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	12
											16 B1.	

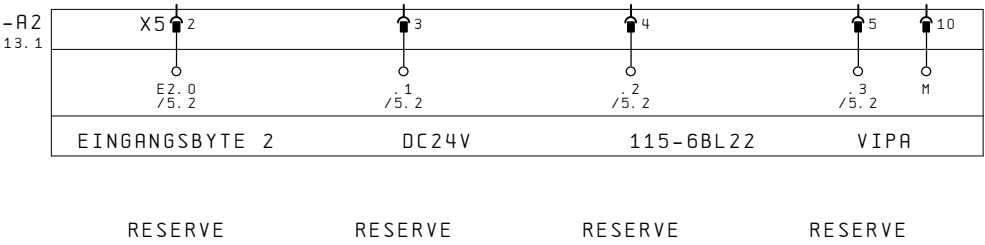
0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge



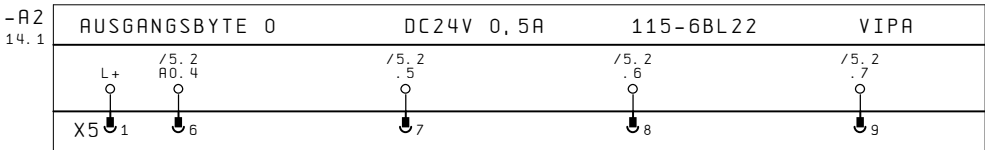
			Datum	07.05.05	Produktmakros für System 100V				Eingangsbyte 1, CPU 115DP DC24V, 115-6BL22	VIPA100V		=SYSTEM100V		
			Bearb.	ZBW								+115_6BL22		
			Geänd.											
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	13	
													16 B1.	

Variante 2: 20 Eingänge/12 Ausgänge



15

Variante 2: 20 Eingänge/12 Ausgänge



RESERVE

RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge

-A2 15.1	AUSGANGSBYTE 1		DC24V 0,5A		115-6BL22			VIPA		
	/5.6 L+	/5.6 R1.0	/5.6 .1	/5.6 .2	/5.6 .3	/5.6 .4	/5.6 .5	/5.6 .6	/5.6 .7	/5.6 M
	X6 1	2	3	4	5	6	7	8	9	10

RESERVE

RESERVE

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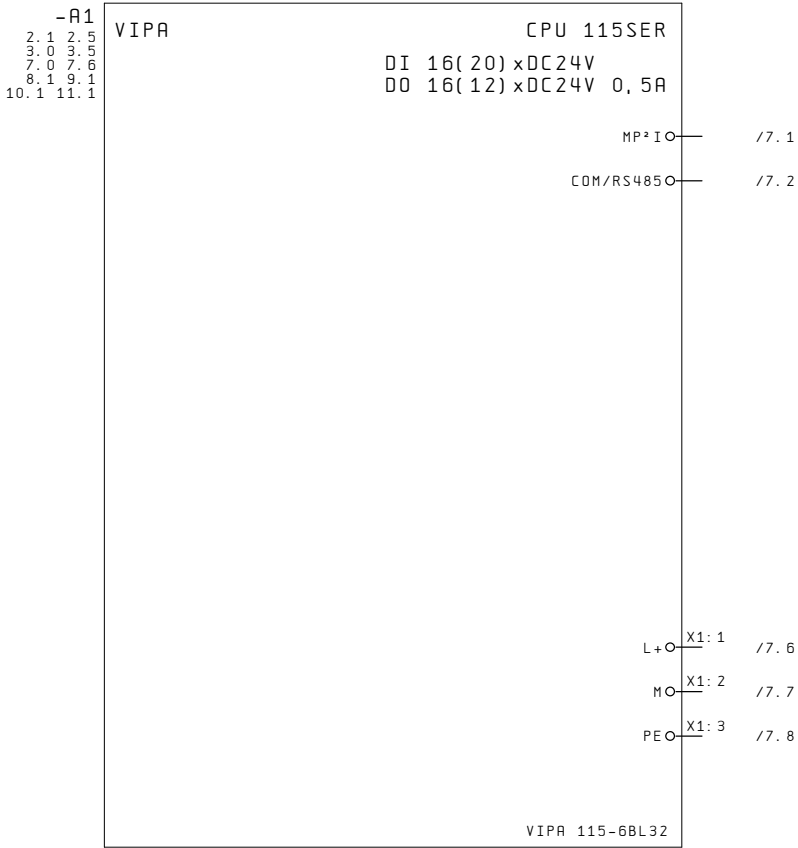
RESERVE

RESERVE

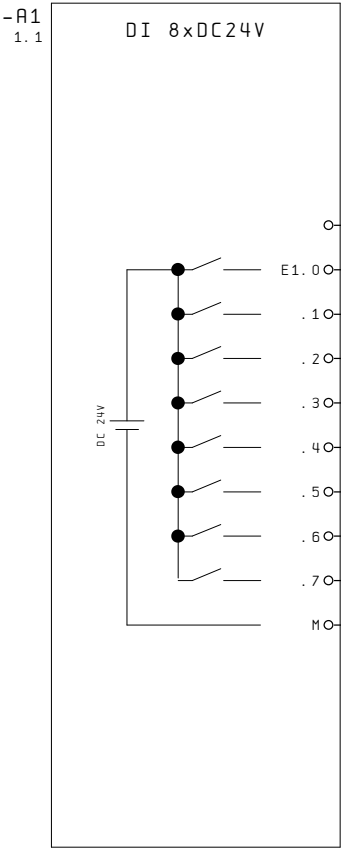
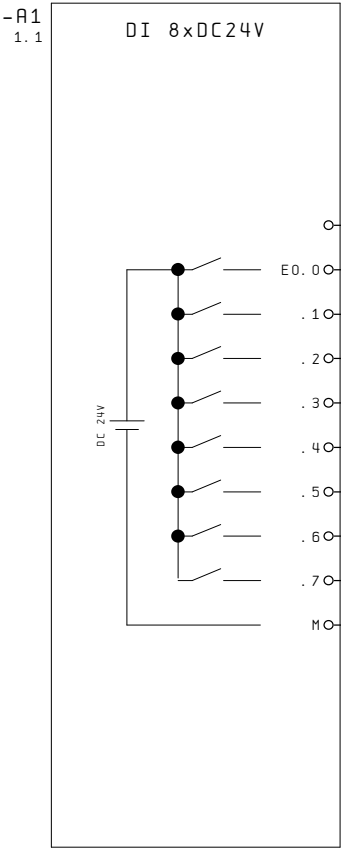
RESERVE

RESERVE

			Datum	07.05.05	Produktmakros für System 100V			Ausgangsbyte 1, CPU 115DP DC24V, 115-6BL22	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL22	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	16
											16 B1.	



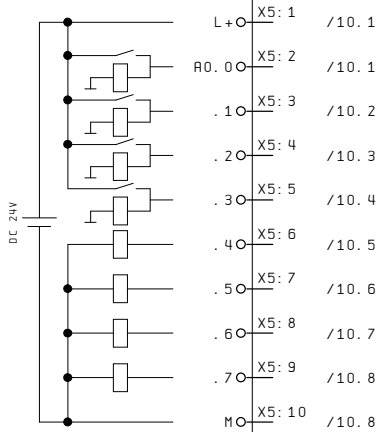
Variante 1: 16 Eingänge/16 Ausgänge





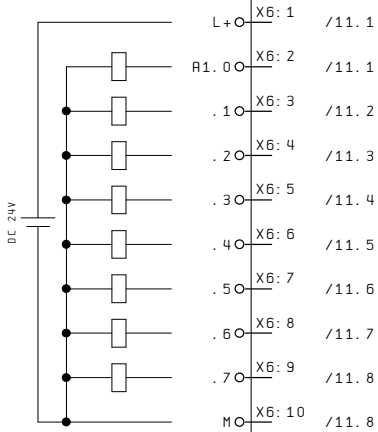
Variante 1: 16 Eingänge/16 Ausgänge

-R1  
1.1 DIO 4/DO 4xDC24V 0,5A



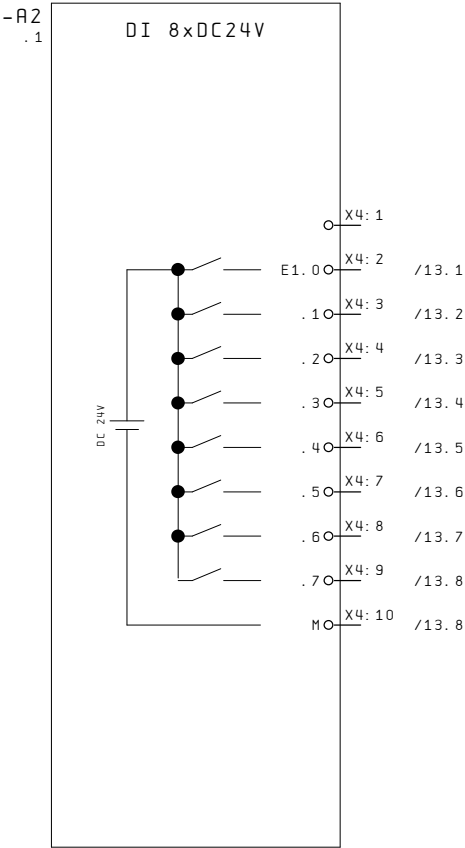
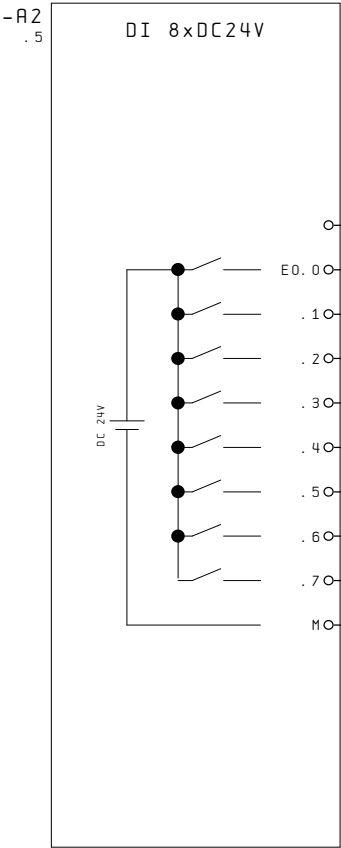
RESERVE  
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RESERVE  
RESERVE

-R1  
1.1 DO 8xDC24V 0,5A



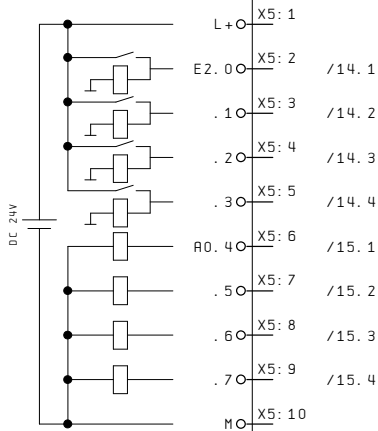
RESERVE  
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RESERVE

Variante 2: 20 Eingänge/12 Ausgänge



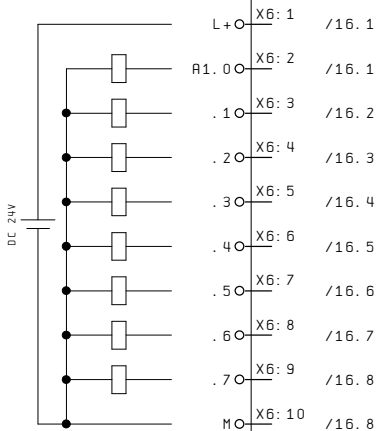
Variante 2: 20 Eingänge/12 Ausgänge

-A2  
4.5 DIO 4/DO 4xDC24V 0,5A

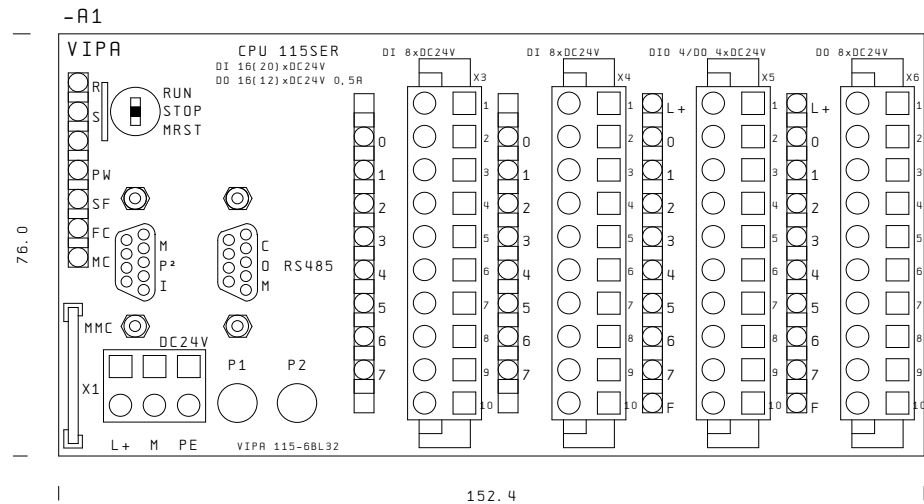


RESERVE  
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RESERVE  
RESERVE  
RESERVE  
RESERVE

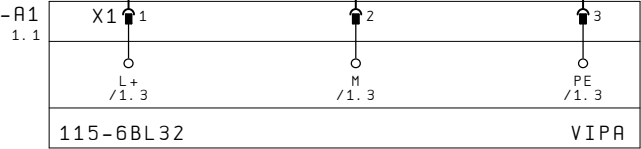
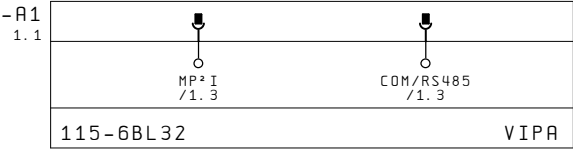
-A2  
1.0 DO 8xDC24V 0,5A



RESERVE  
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RESERVE  
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RESERVE

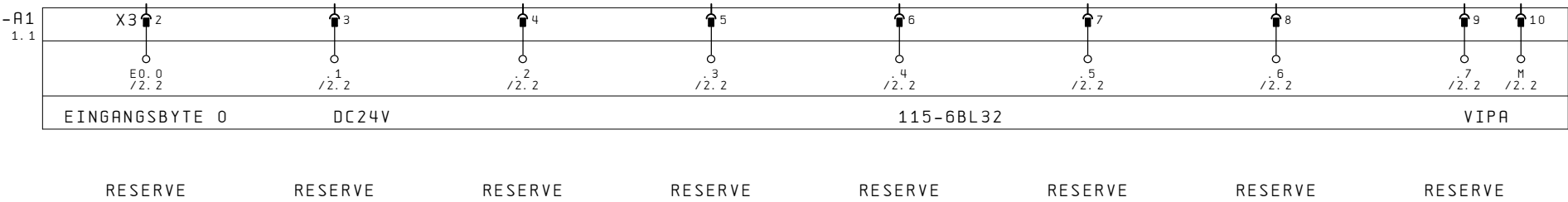


CPU 115SER  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48



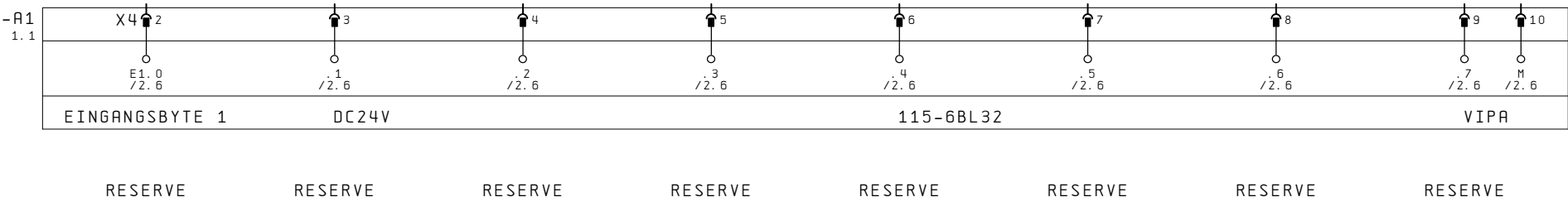
0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge

.4  
/2.6.5  
/2.6.6  
/2.6.7  
/2.6M  
/2.6

EINGANGSBYTE 1

DC24V

115-6BL32

VIPA

RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

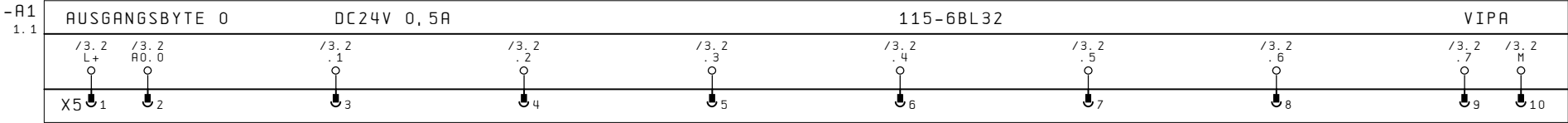
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



RESERVE

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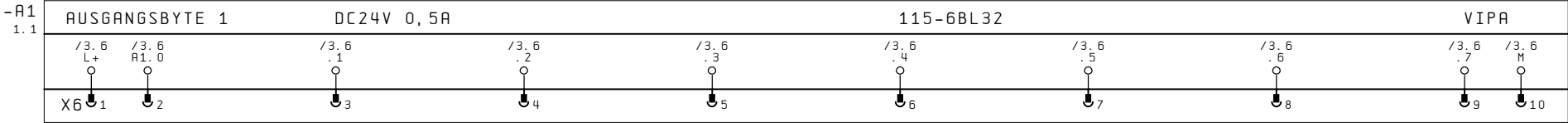
RESERVE

RESERVE



0	1	2	3	4	5	6	7	8	9
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Variante 1: 16 Eingänge/16 Ausgänge



RESERVE

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RESERVE

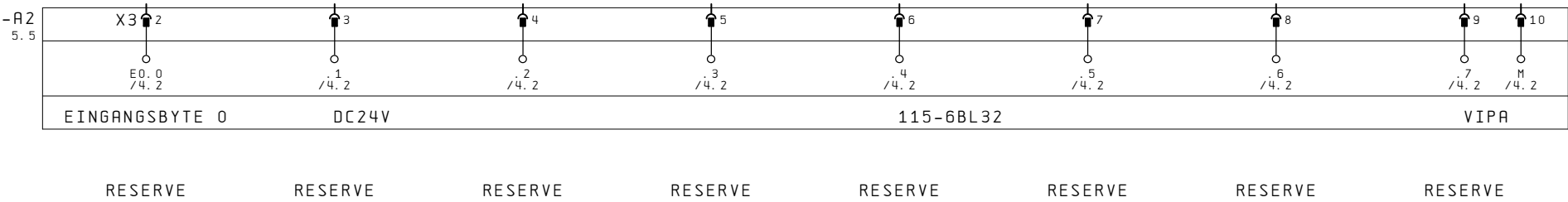
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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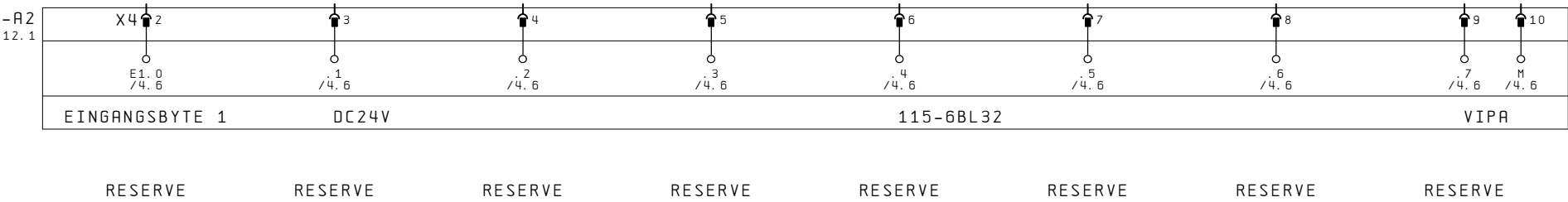
Variante 2: 20 Eingänge/12 Ausgänge



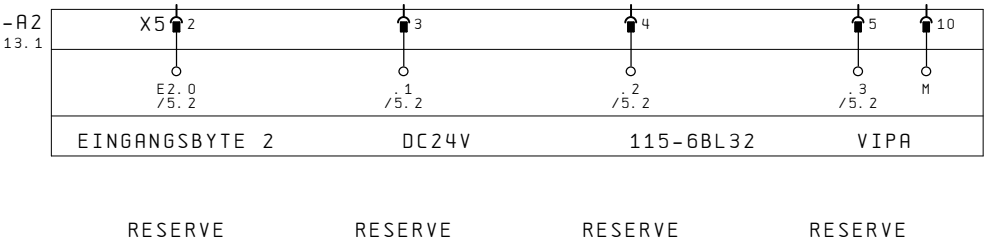
			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, CPU 115SER DC24V, 115-6BL32	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+115_6BL32	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1. 12 16 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge

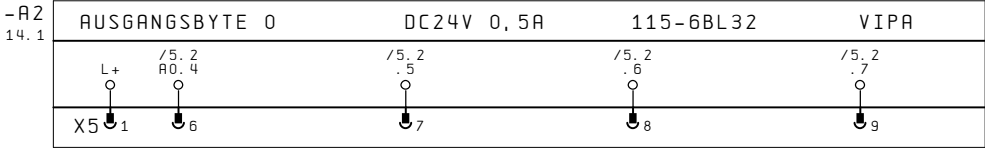


Variante 2: 20 Eingänge/12 Ausgänge



0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge



RESERVE

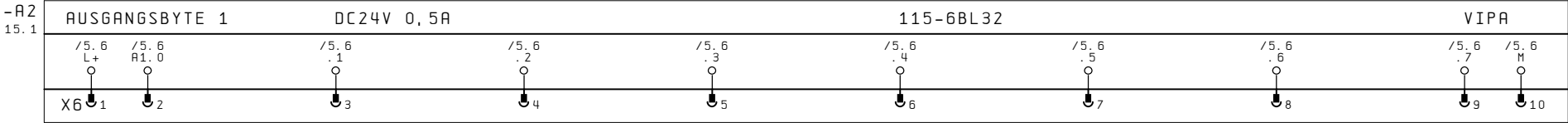
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 20 Eingänge/12 Ausgänge



RESERVE

RESERVE

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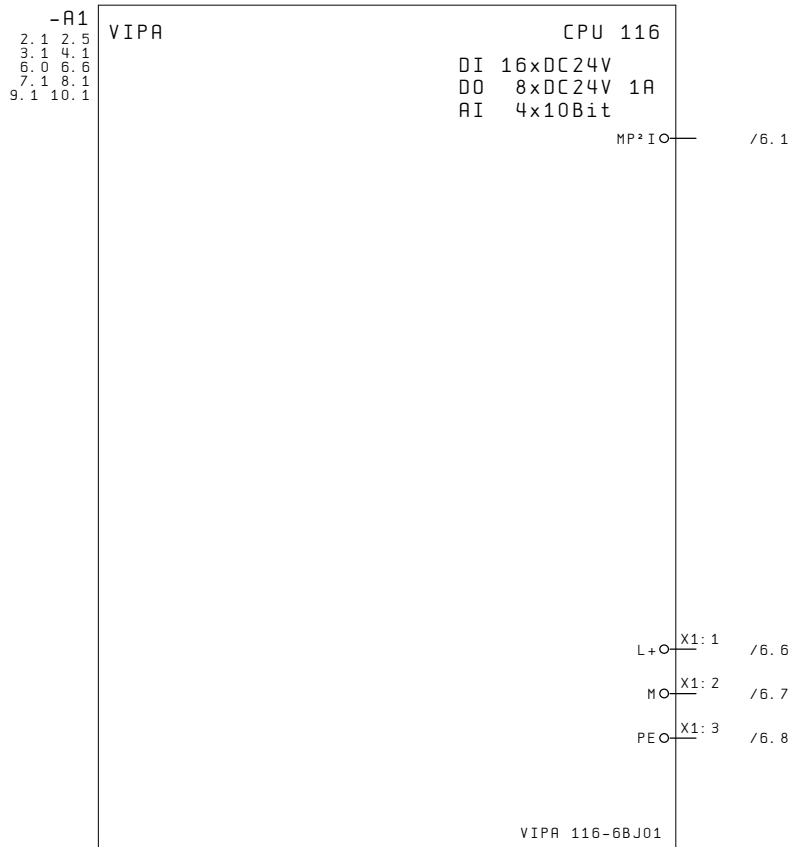
RESERVE

RESERVE

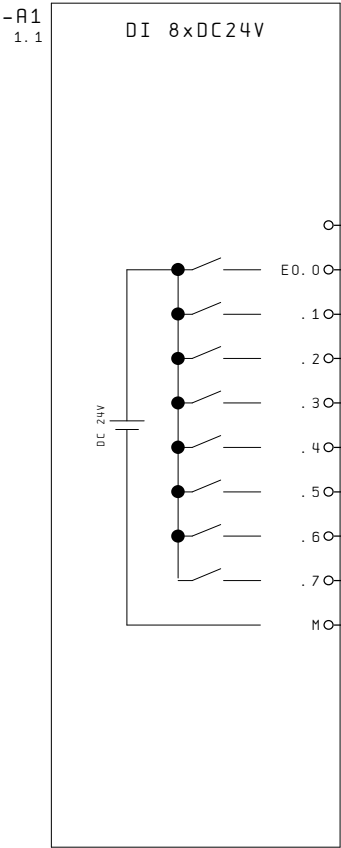
RESERVE

			Datum	07.05.05	Produktmakros für System 100V		Ausgangsbyte 1, CPU 115SER DC24V, 115-6BL32	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW						+115_6BL32	
			Geänd.						System 100V		B1. 16
Änderung	Datum	Name	Form		Unspr.	Ers. f.	Ers. d.				16 B1.

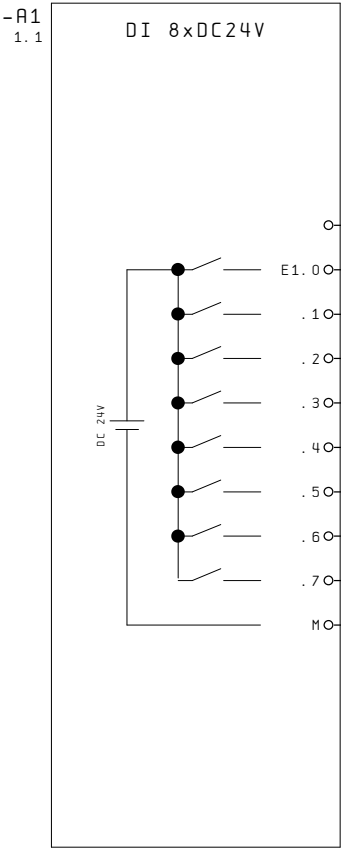
0	1	2	3	4	5	6	7	8	9
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+115_6BL32/16												2	
			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, CPU 116 DC24V, 116-6BJ01		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+116_6BJ01	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	1	
											10 Bl.		

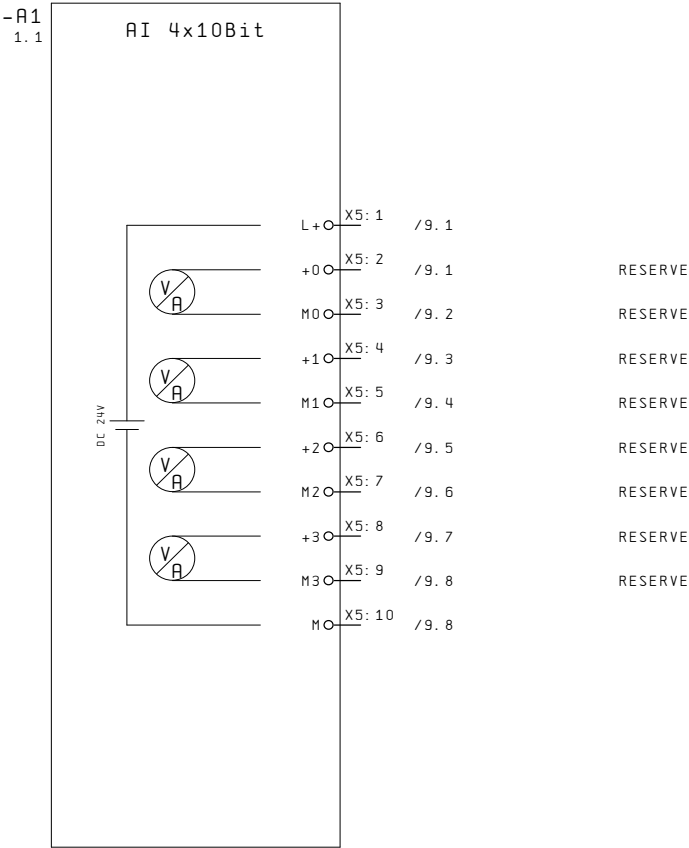


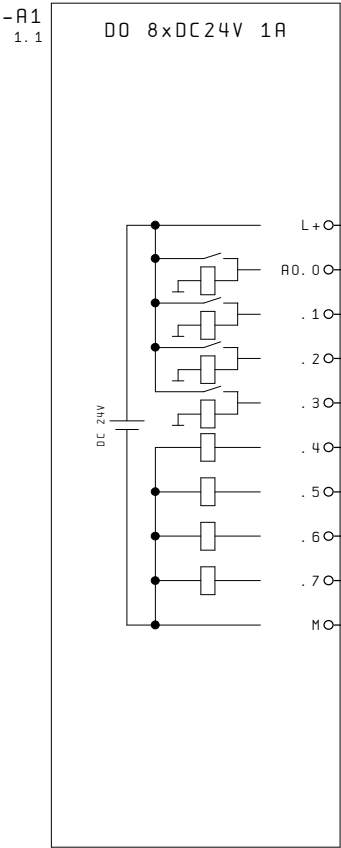
RESERVE  
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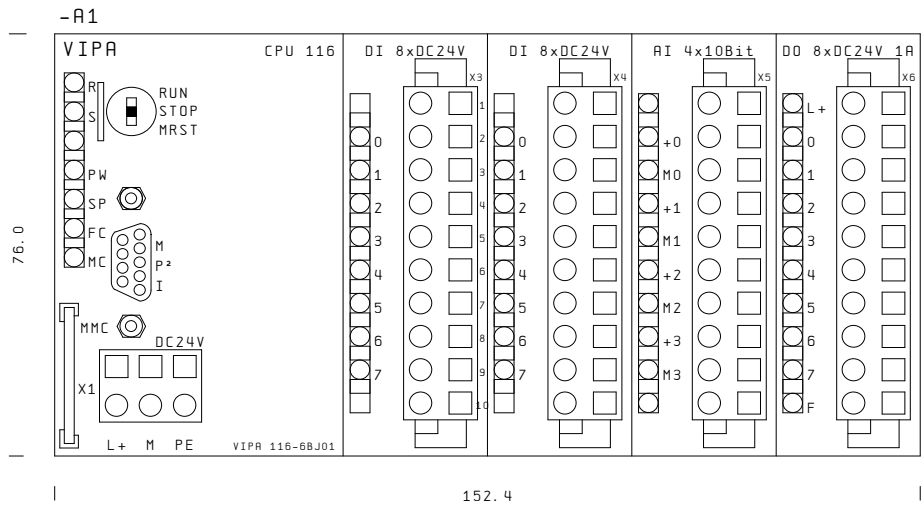


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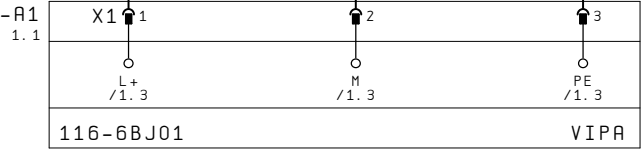
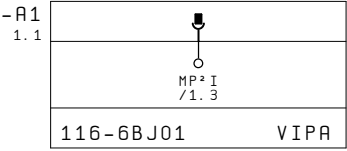




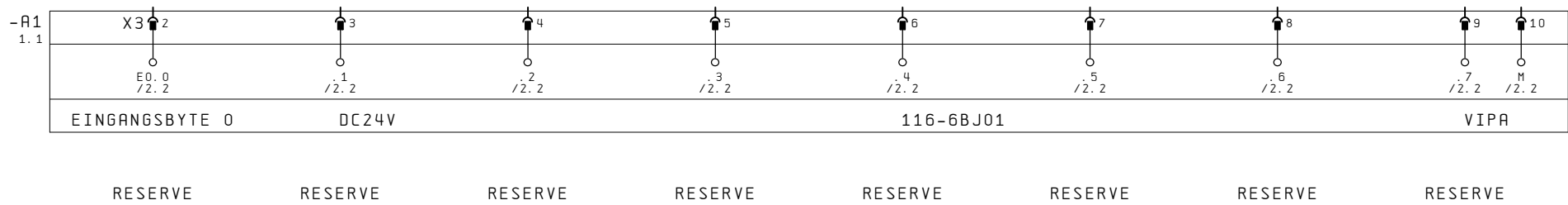




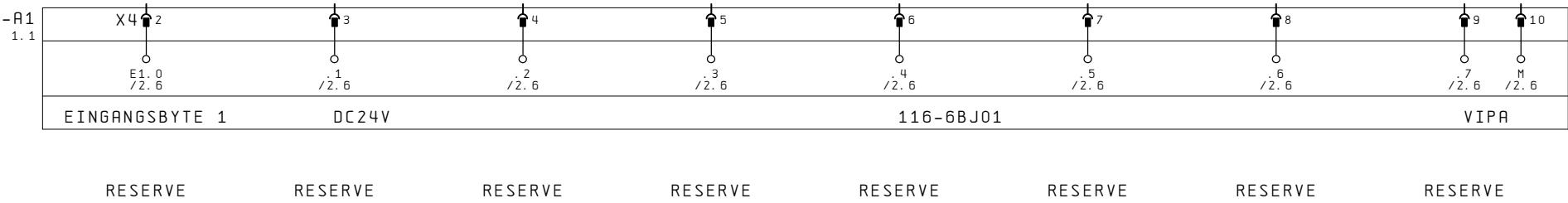
CPU 116  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48



0	1	2	3	4	5	6	7	8	9
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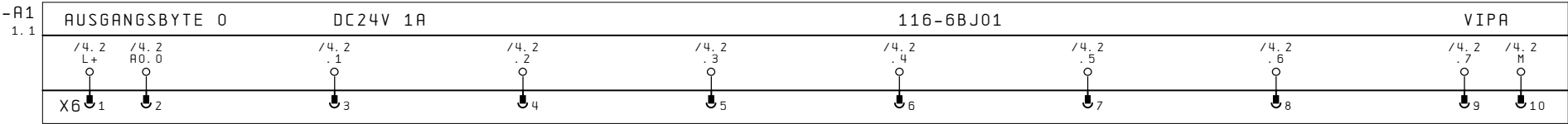


0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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RESERVE

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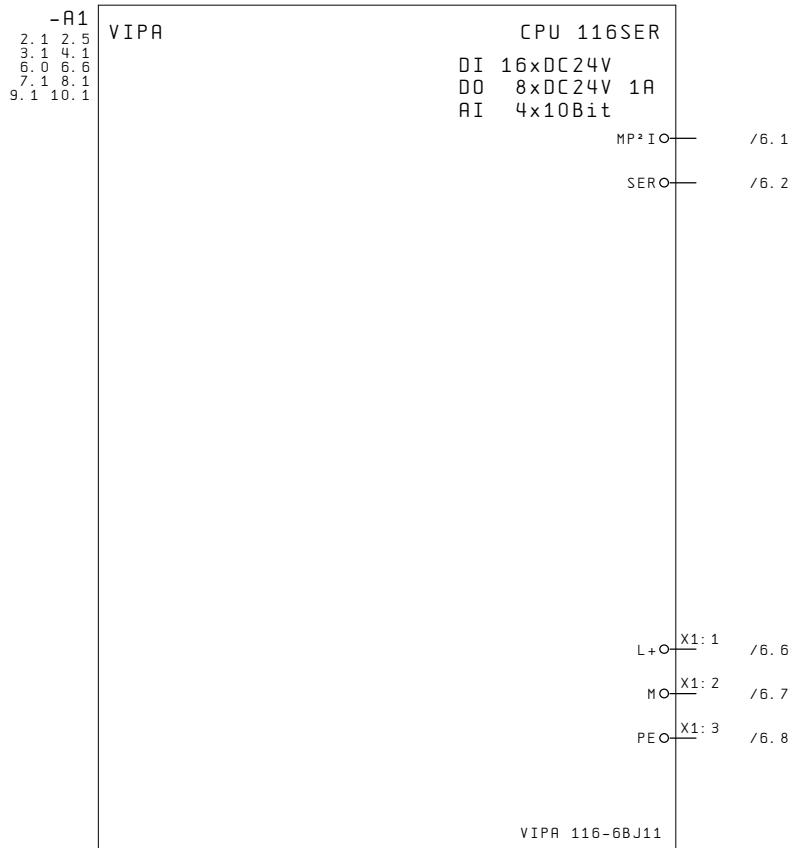
RESERVE

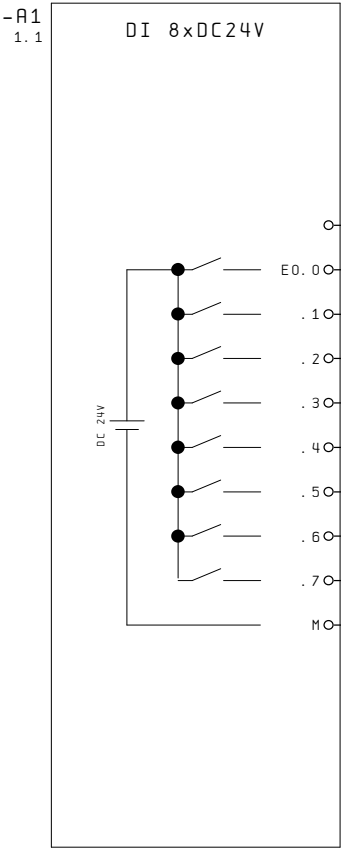
RESERVE

RESERVE

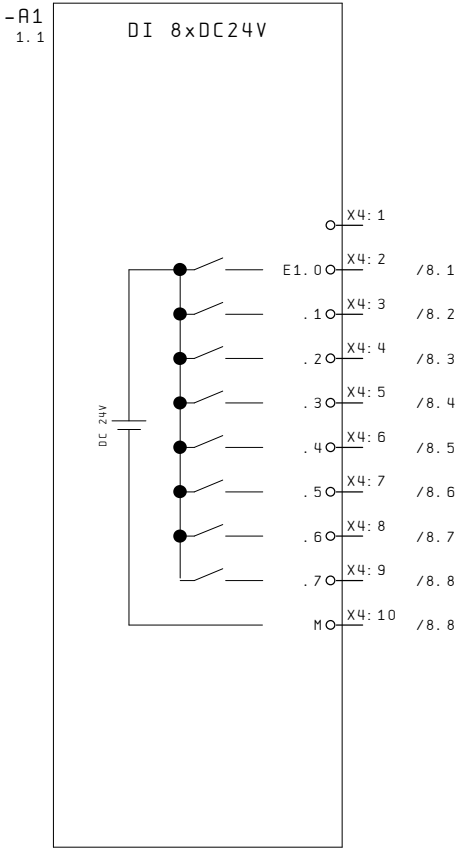
RESERVE



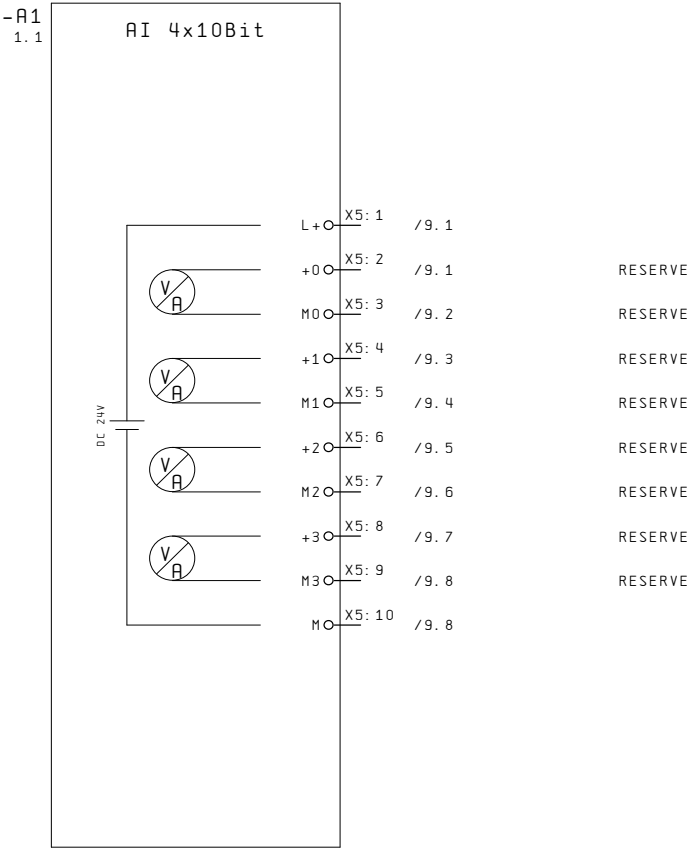


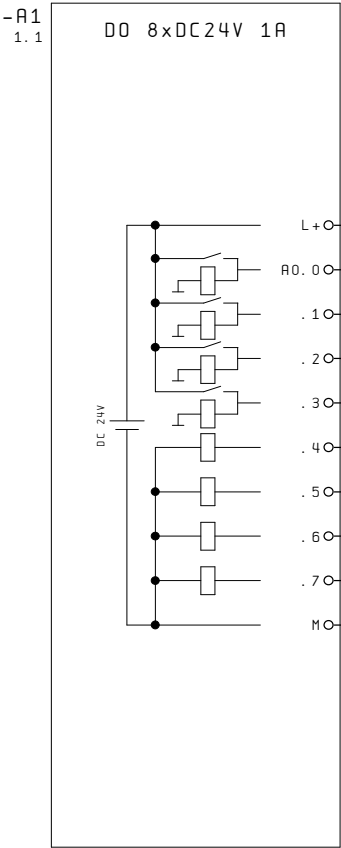


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RESERVE



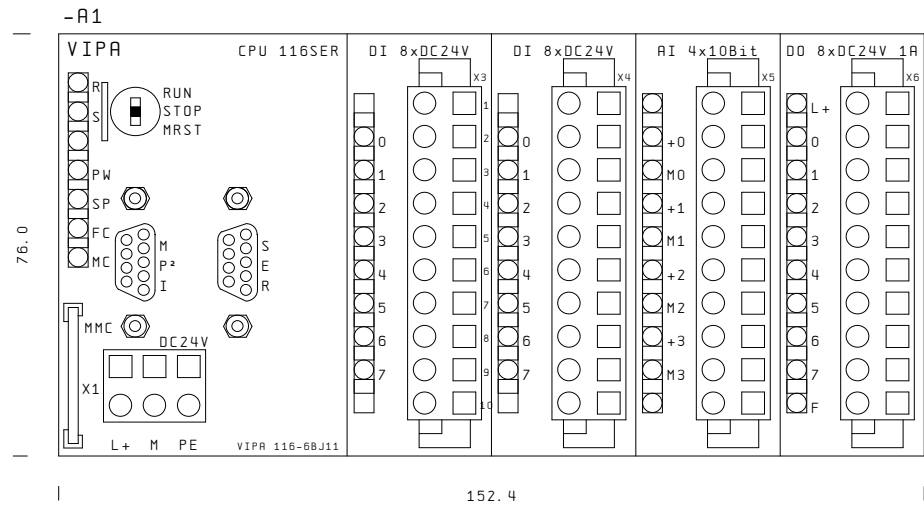
RESERVE  
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RESERVE  
RESERVE



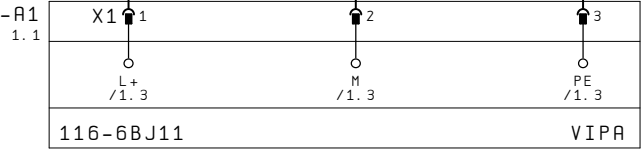
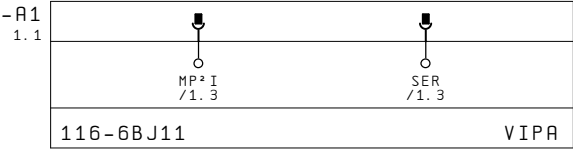


L+O	X6: 1	/10. 1	
A0. 0O	X6: 2	/10. 1	RESERVE
. 1O	X6: 3	/10. 2	RESERVE
. 2O	X6: 4	/10. 3	RESERVE
. 3O	X6: 5	/10. 4	RESERVE
. 4O	X6: 6	/10. 5	RESERVE
. 5O	X6: 7	/10. 6	RESERVE
. 6O	X6: 8	/10. 7	RESERVE
. 7O	X6: 9	/10. 8	RESERVE
M0	X6: 10	/10. 8	

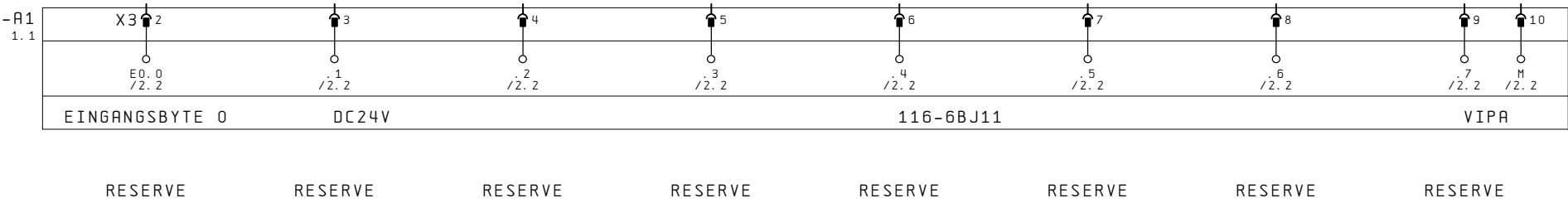
			Datum	12.07.03	Produktmakros für System 100V				SPS-Übersicht Ausgänge, CPU 116SER DC24V, 116-6BJ11	VIPA100V		=SYSTEM100V		
			Bearb.	ZBW								+116_6BJ11		
			Geänd.											
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	4	
													10 B1.	



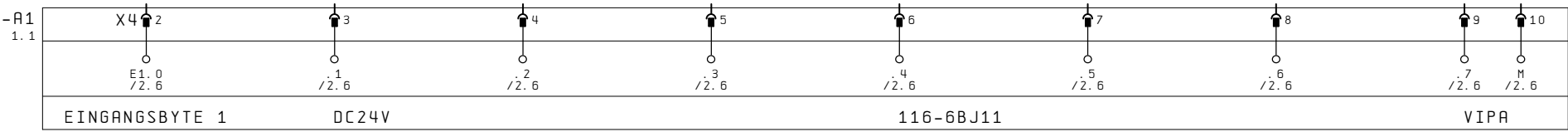
CPU 116SER  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48



0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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RESERVE

RESERVE

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RESERVE

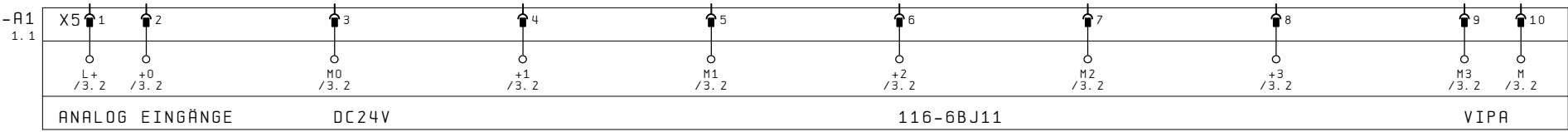
RESERVE

RESERVE

RESERVE



0	1	2	3	4	5	6	7	8	9
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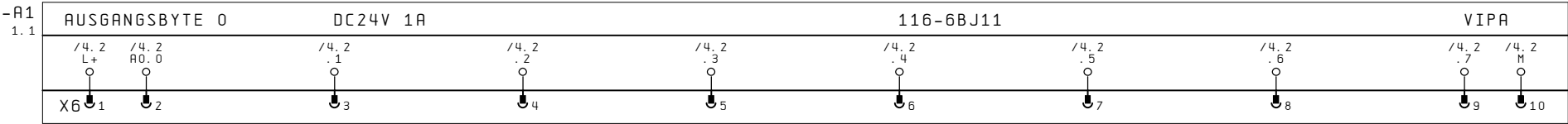
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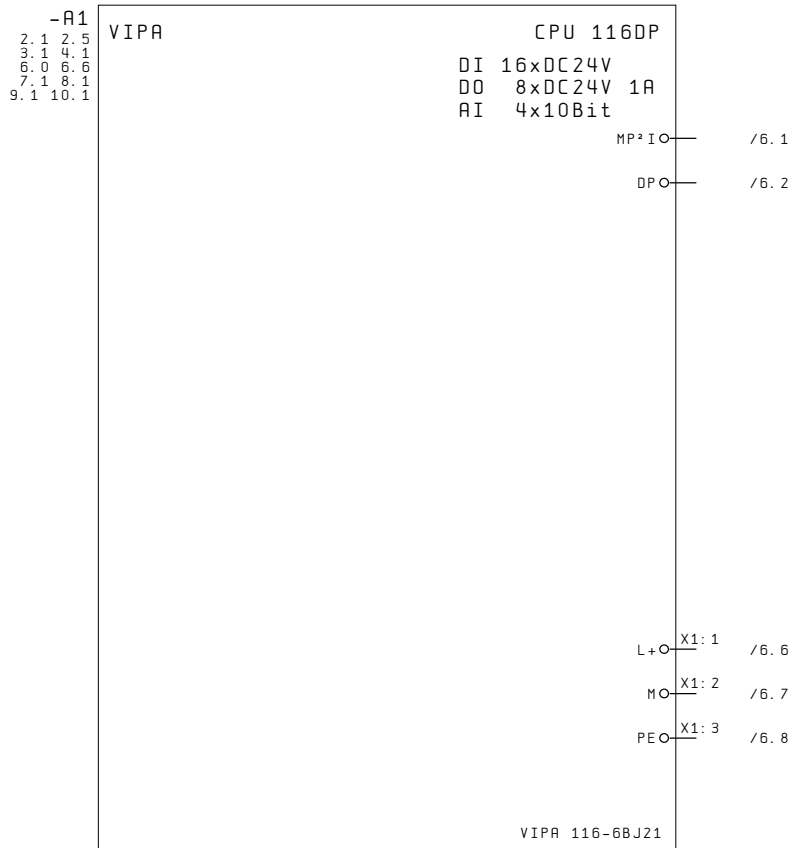
RESERVE

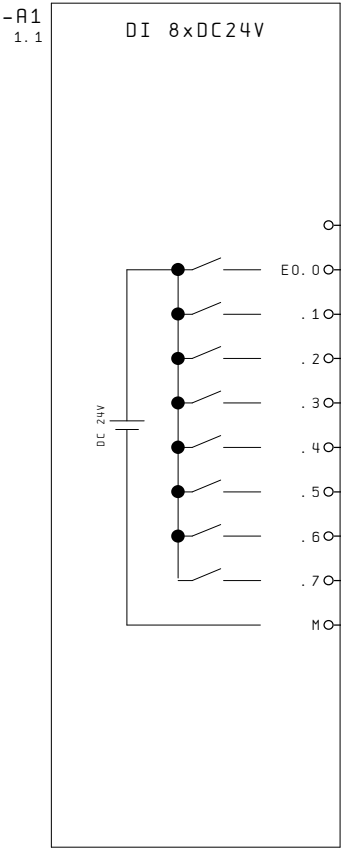
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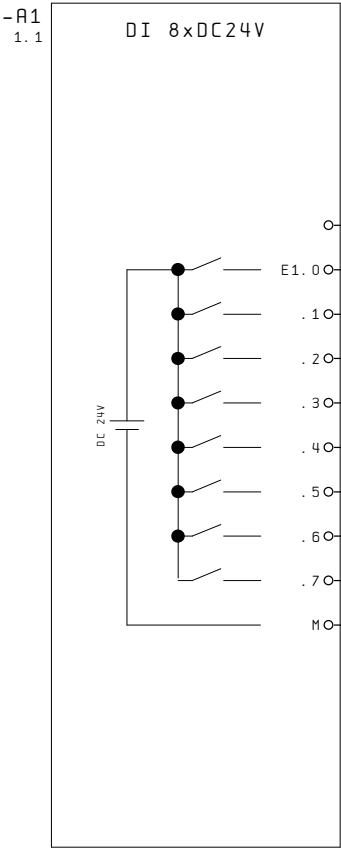
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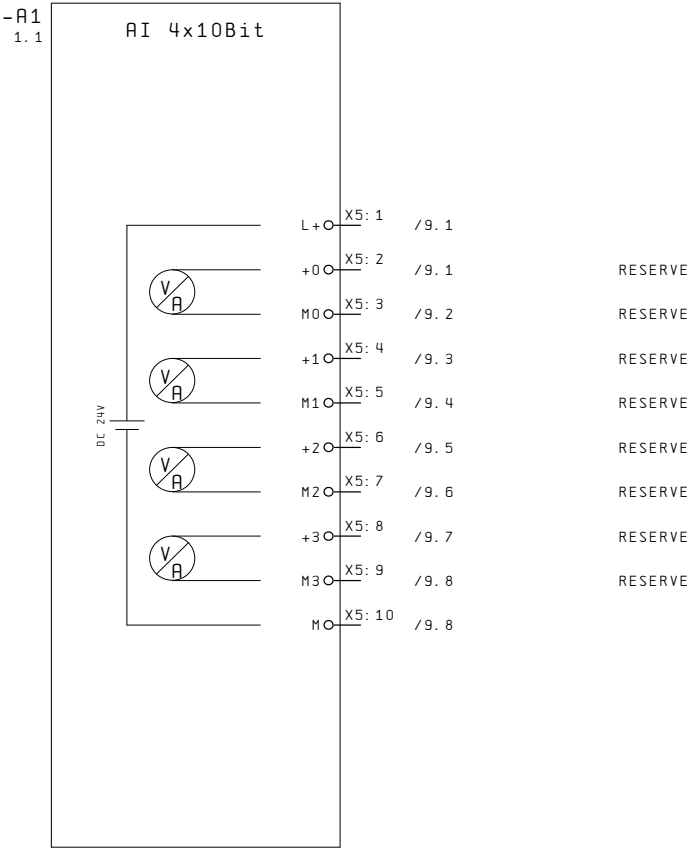
RESERVE

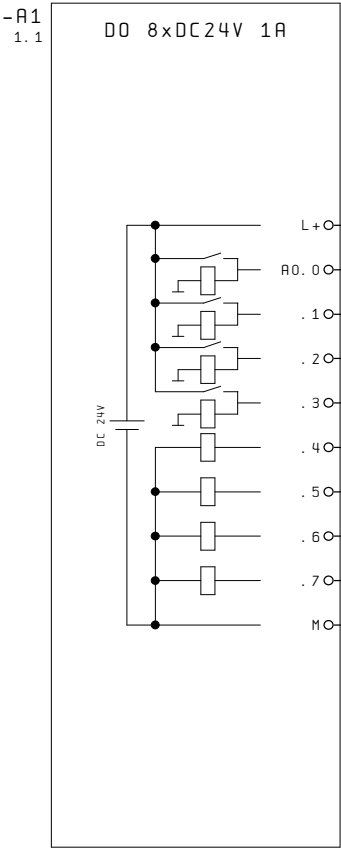
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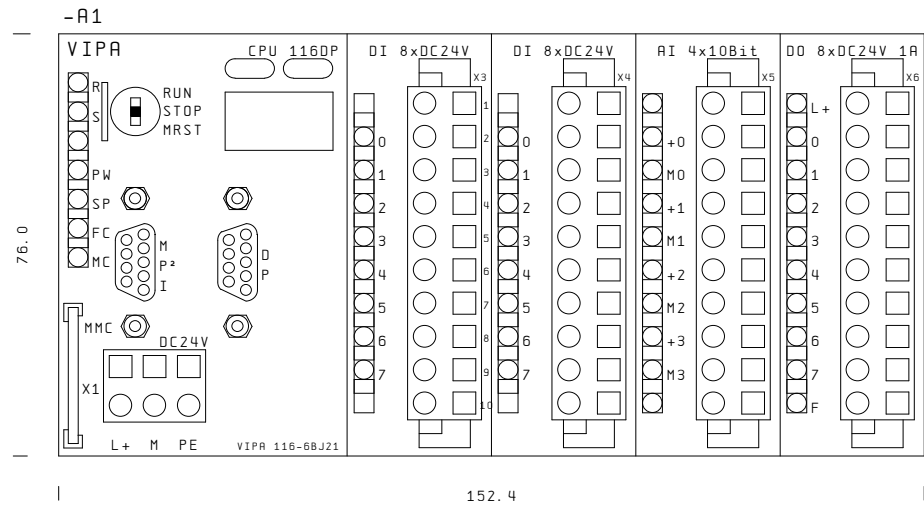
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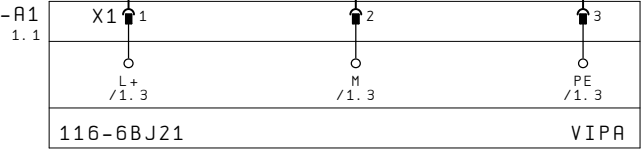
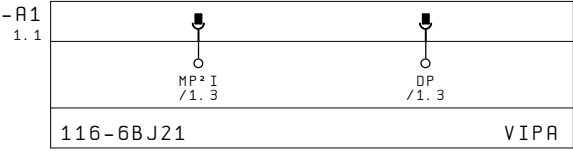


			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Ausgänge, CPU 116DP DC24V, 116-6BJ21 (Vorläufig nicht verfügbar)	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+116_6BJ21	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	4
											10 B1.	



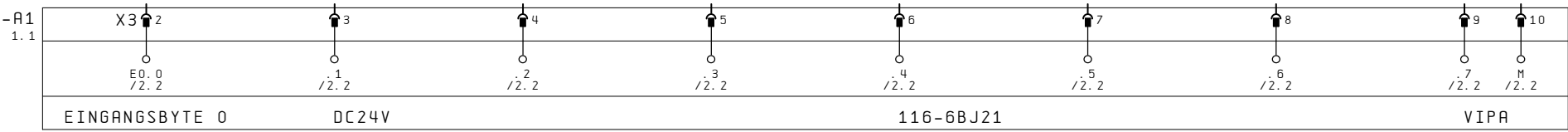
CPU 116DP  
Arbeitsspeicher 16kB  
Ladespeicher 24kB  
mit Steckplatz für Speicherkarte  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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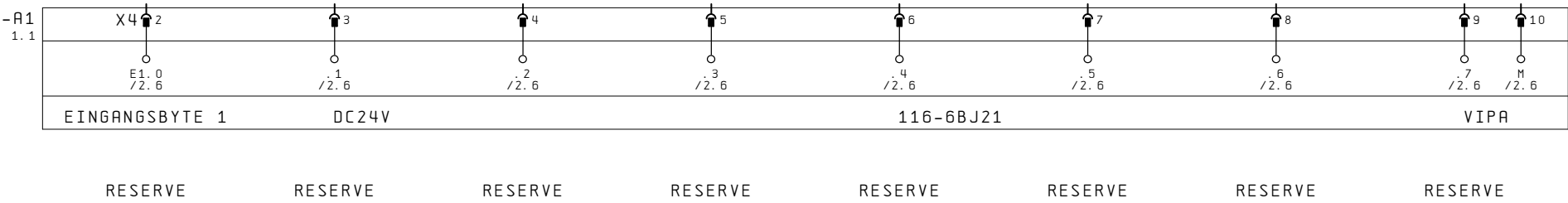
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/2.6.5  
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/2.6.7  
/2.6M  
/2.6

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DC24V

116-6BJ21

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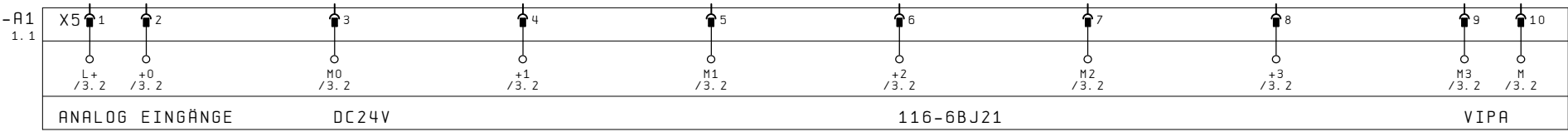
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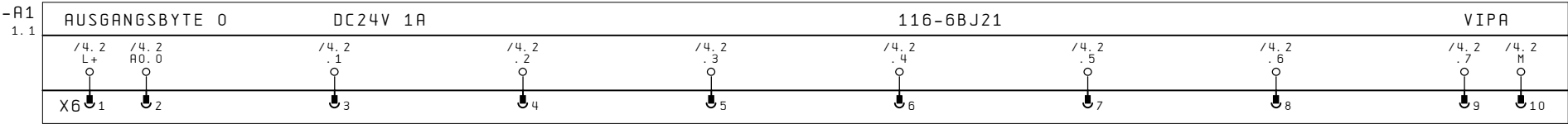
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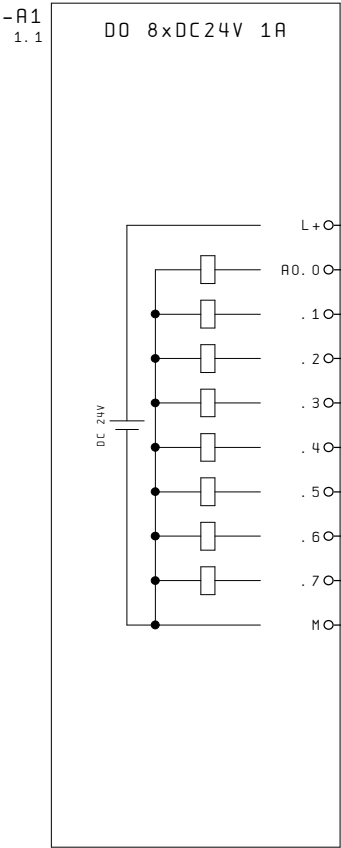
RESERVE

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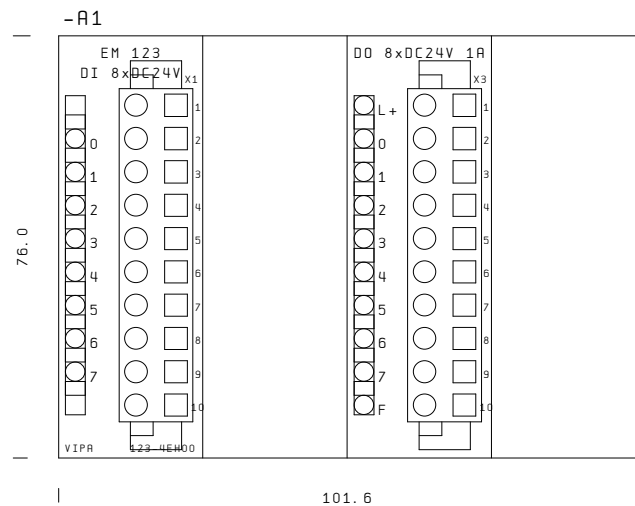
RESERVE

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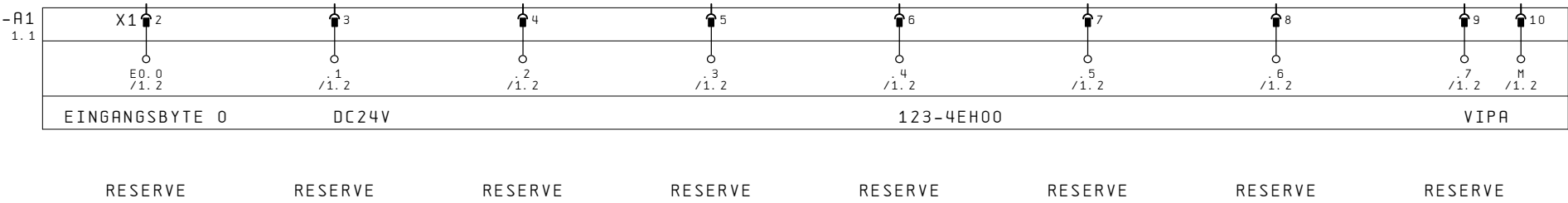


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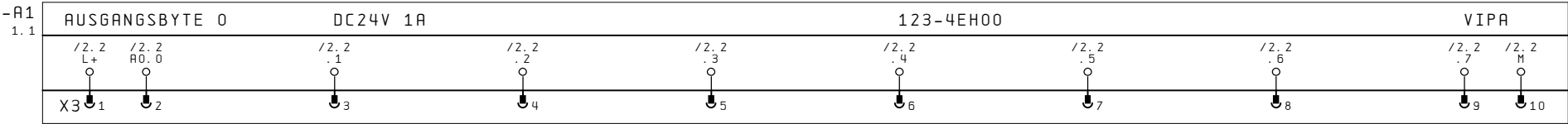
EM 123  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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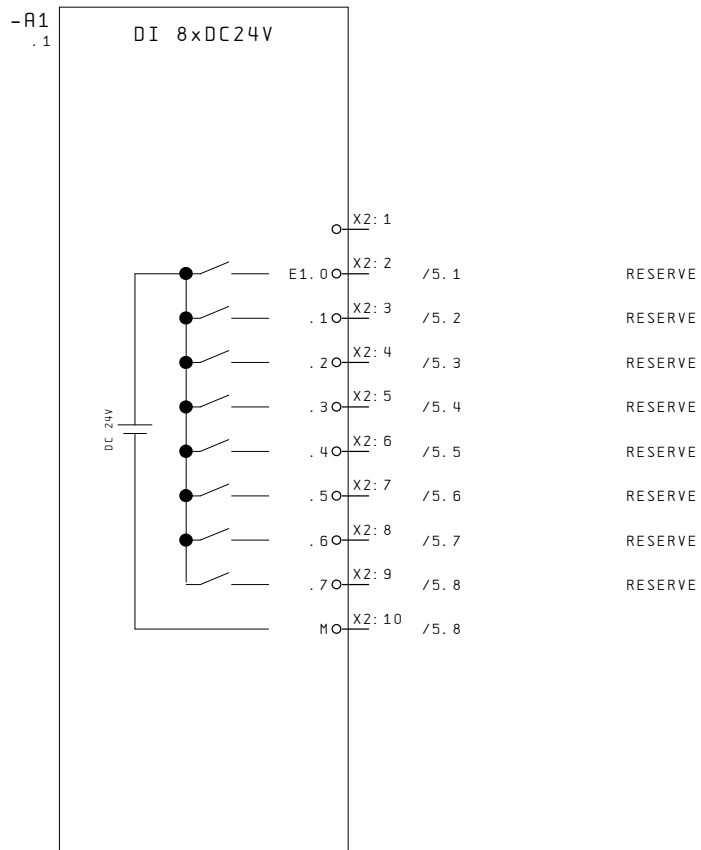
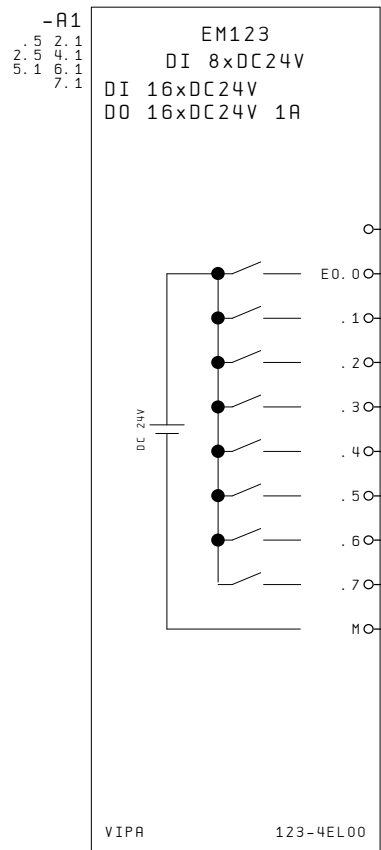
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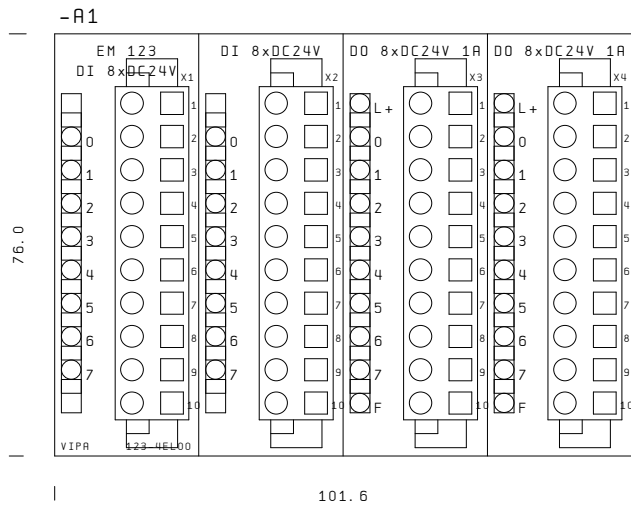
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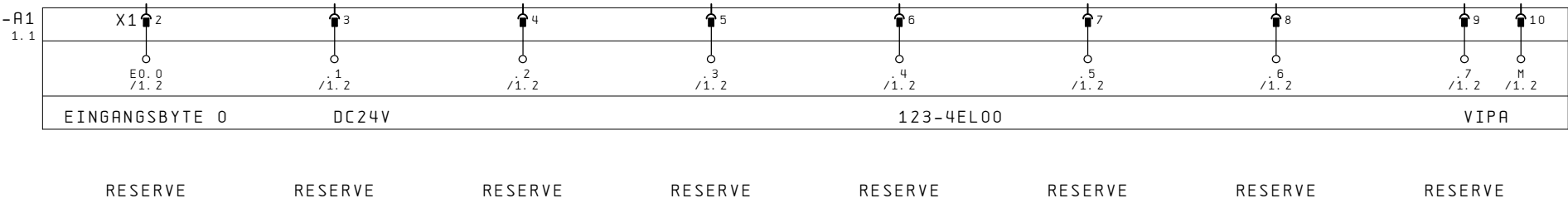




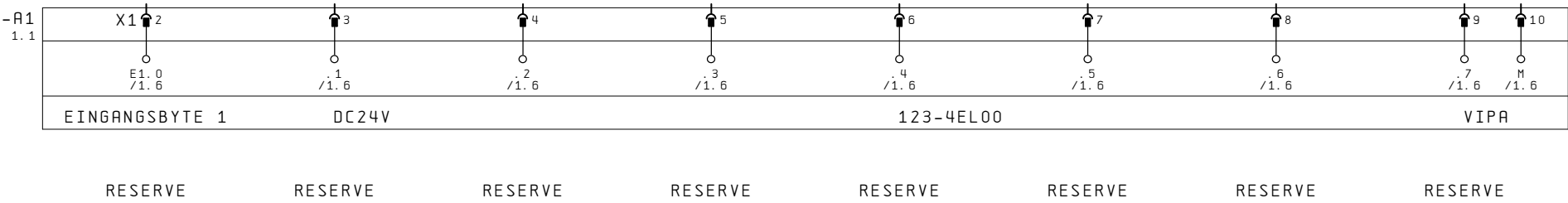


EM 123  
Abmessungen: (BxHxT) 101,6 x 76 x 48

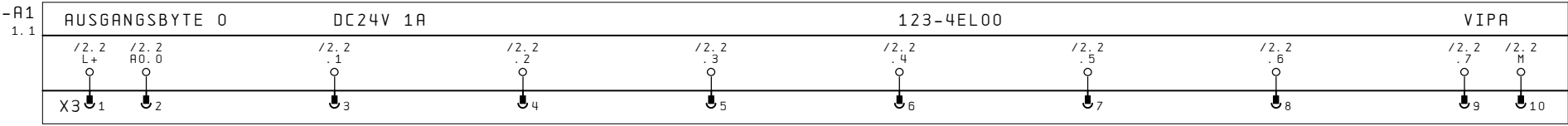
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0	1	2	3	4	5	6	7	8	9
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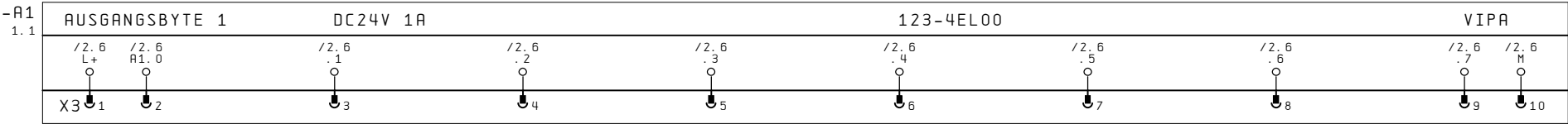
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0	1	2	3	4	5	6	7	8	9
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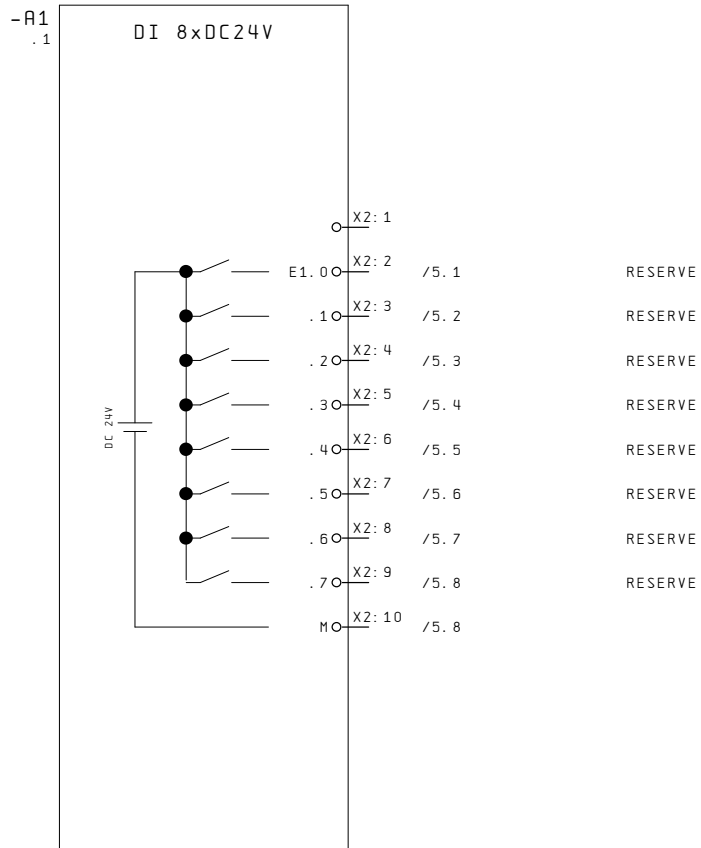
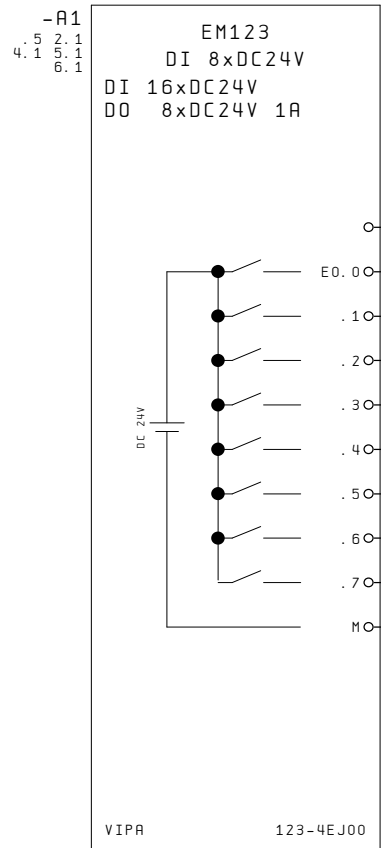
RESERVE

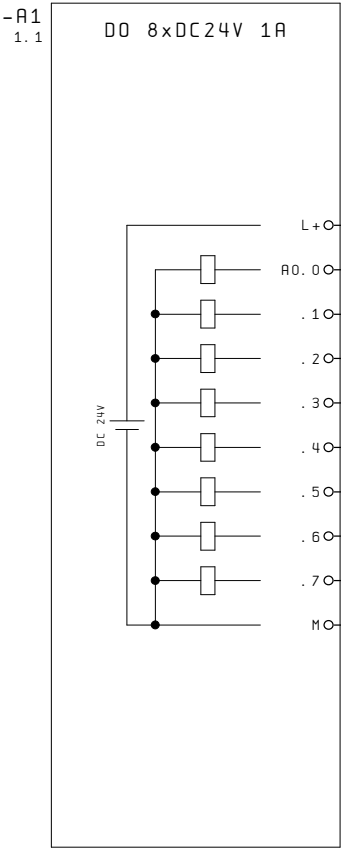
RESERVE

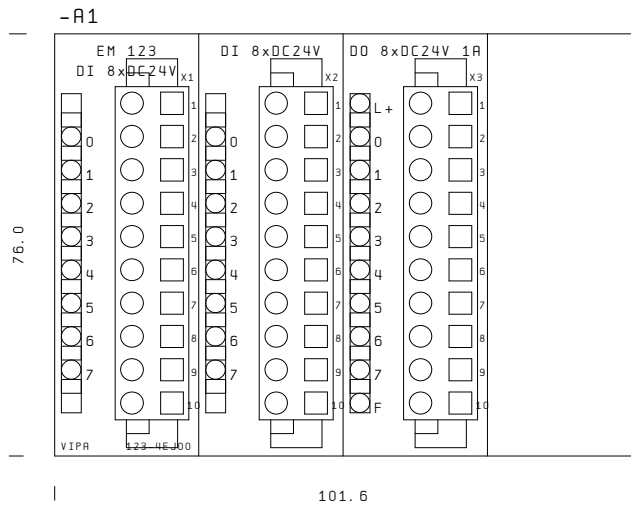
RESERVE

			Datum	14.07.03	Produktmakros für System 100V			Ausgangsbyte 1, EM 123 DC24V, 123-4EL00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+123_4EL00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	7
											7 B1.	



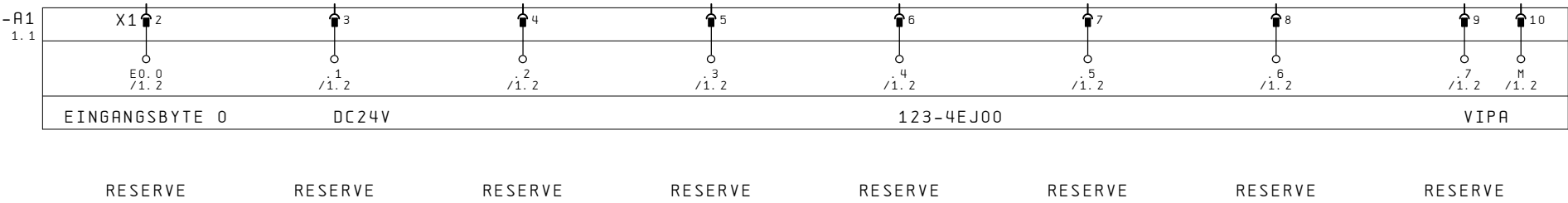


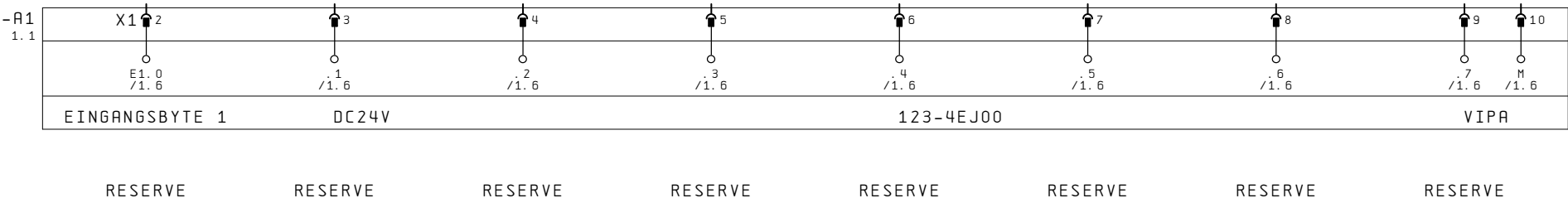




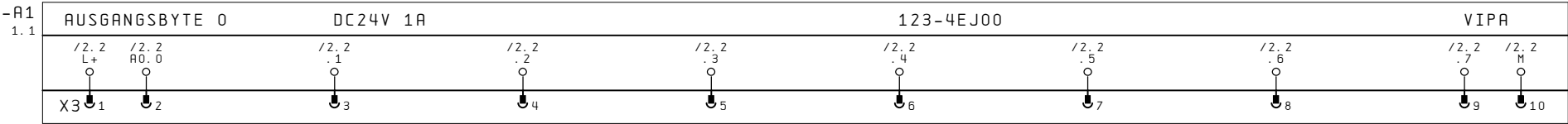
EM 123  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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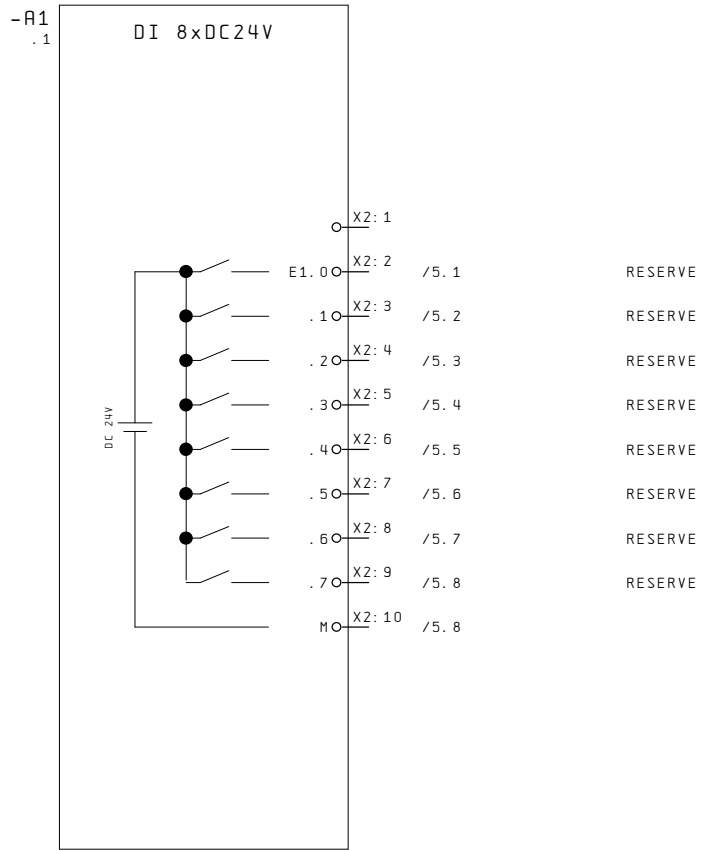
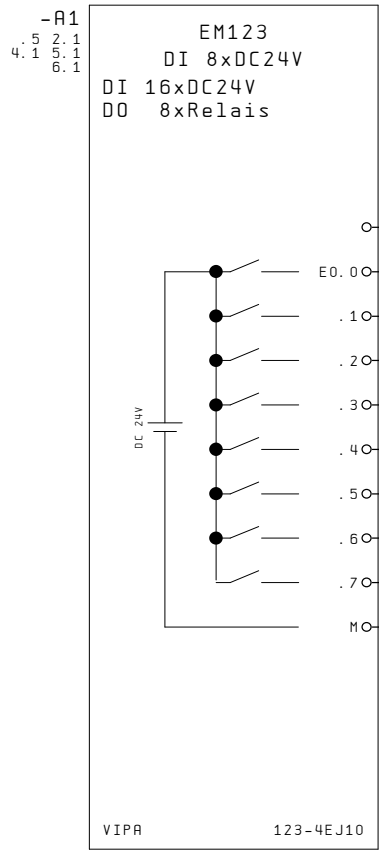
RESERVE

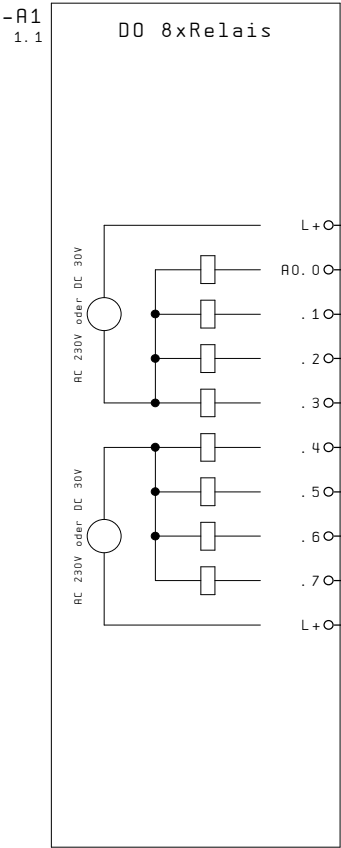
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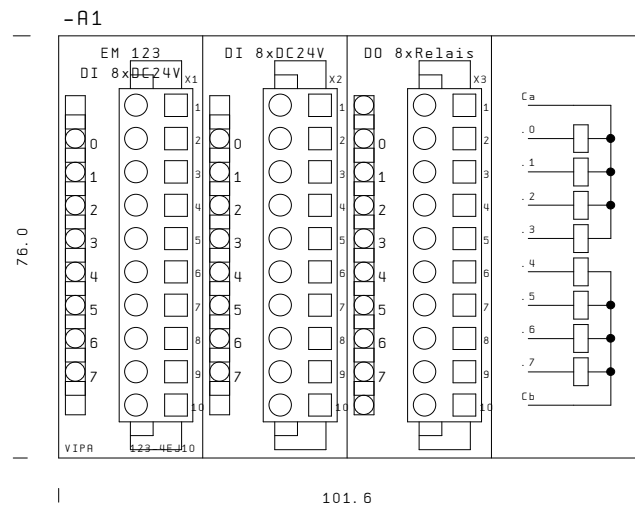
RESERVE





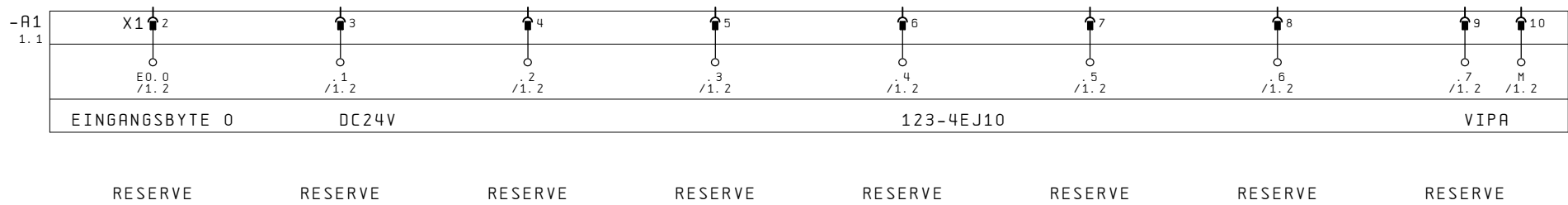
L+O	X5: 1	/6. 1	
A0. 0O	X5: 2	/6. 1	RESERVE
. 1O	X5: 3	/6. 2	RESERVE
. 2O	X5: 4	/6. 3	RESERVE
. 3O	X5: 5	/6. 4	RESERVE
. 4O	X5: 6	/6. 5	RESERVE
. 5O	X5: 7	/6. 6	RESERVE
. 6O	X5: 8	/6. 7	RESERVE
. 7O	X5: 9	/6. 8	RESERVE
L+O	X5: 10	/6. 8	



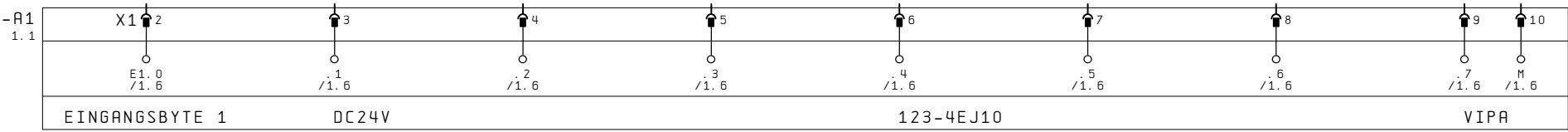


EM 123  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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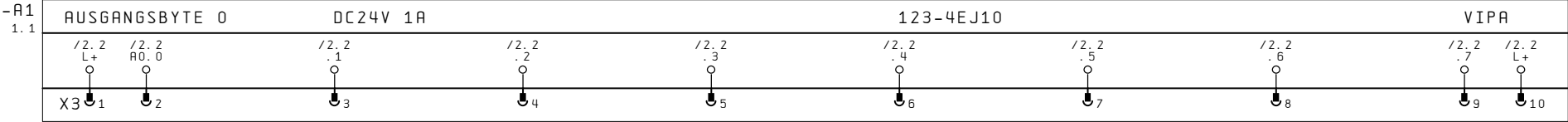
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0	1	2	3	4	5	6	7	8	9
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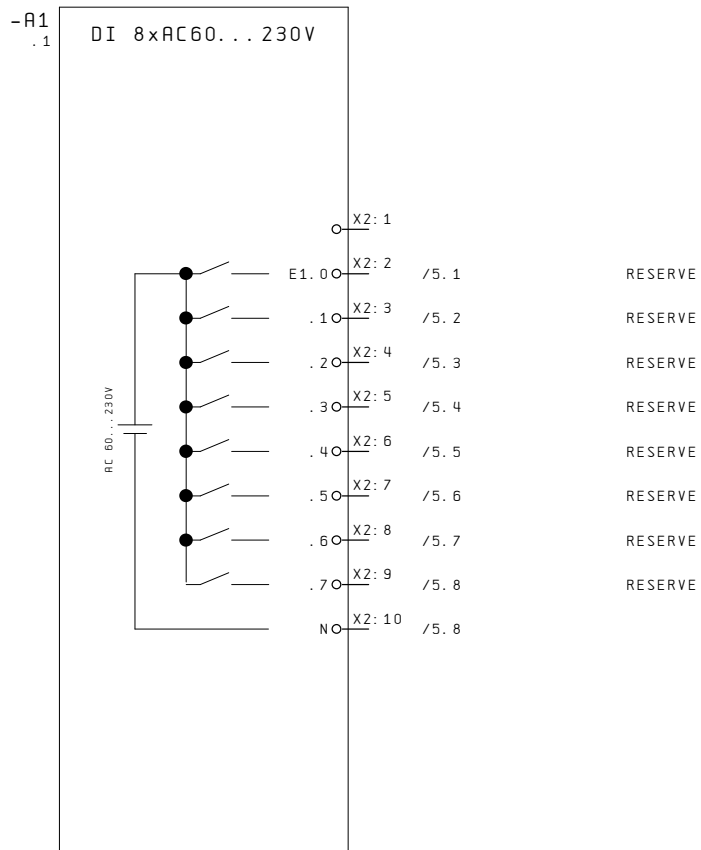
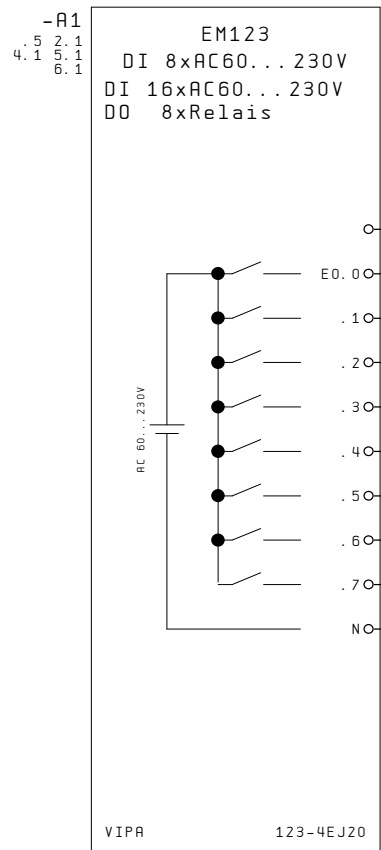
RESERVE

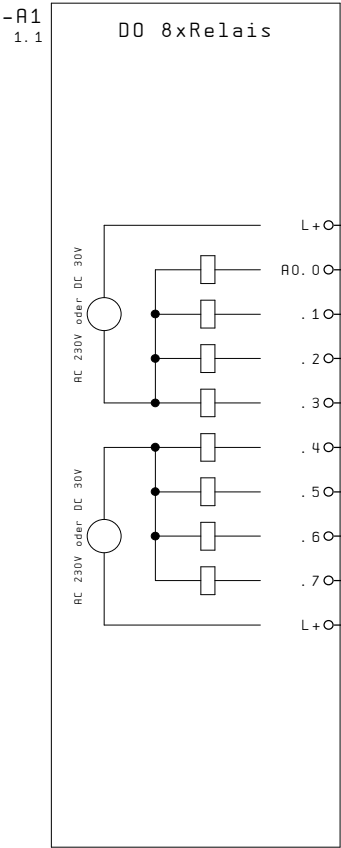
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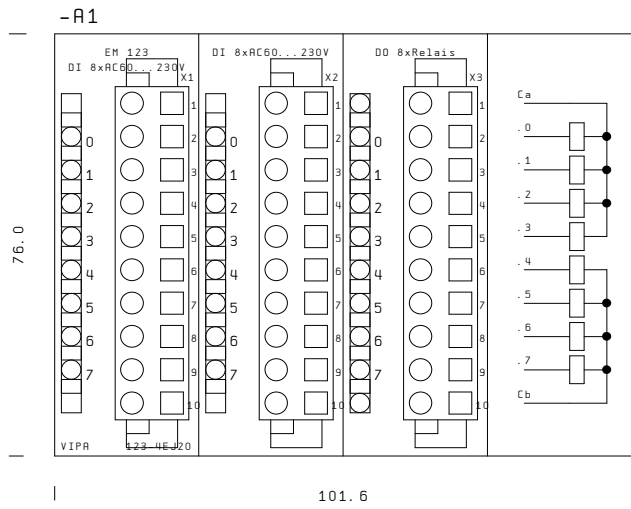
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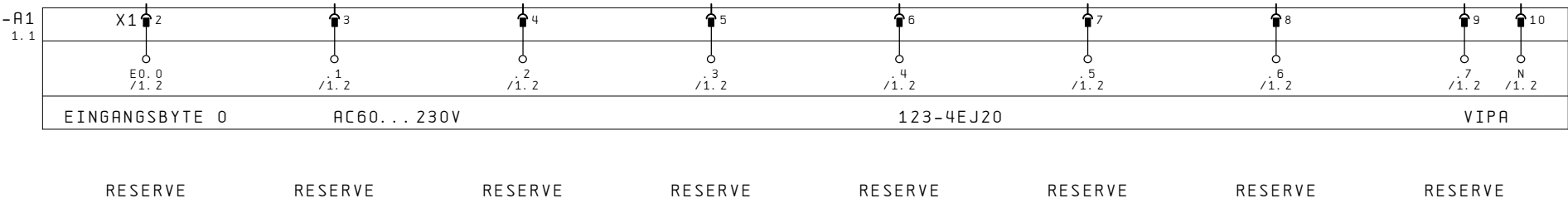
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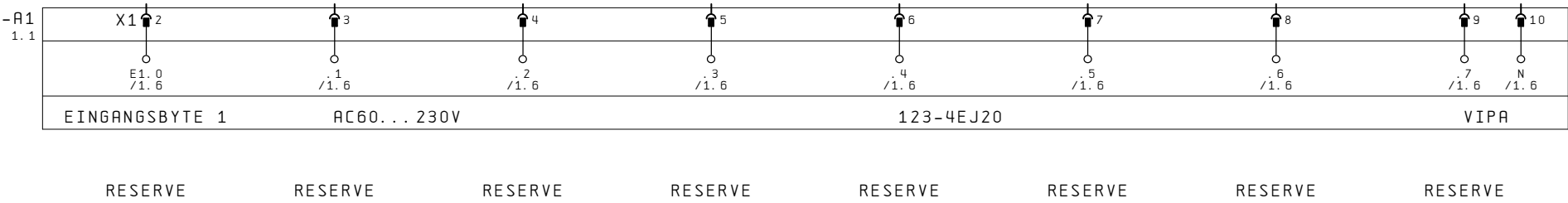
EM 123  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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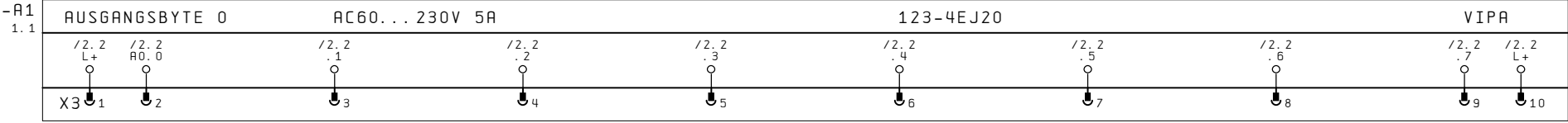




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0	1	2	3	4	5	6	7	8	9
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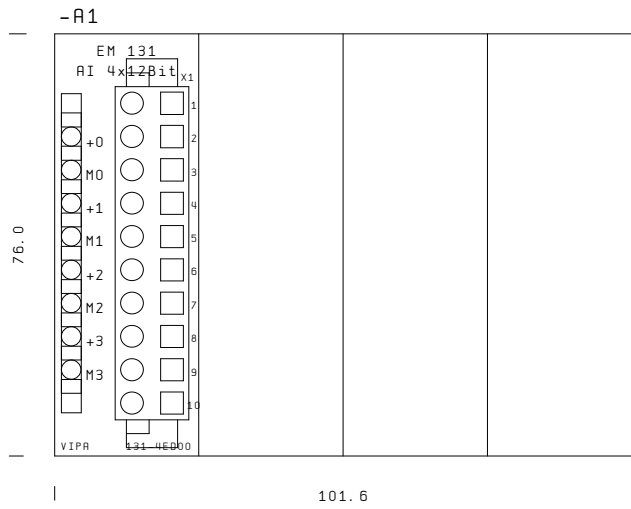
RESERVE

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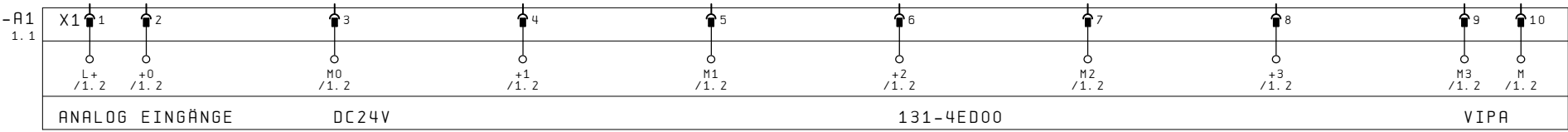
RESERVE





EM 131  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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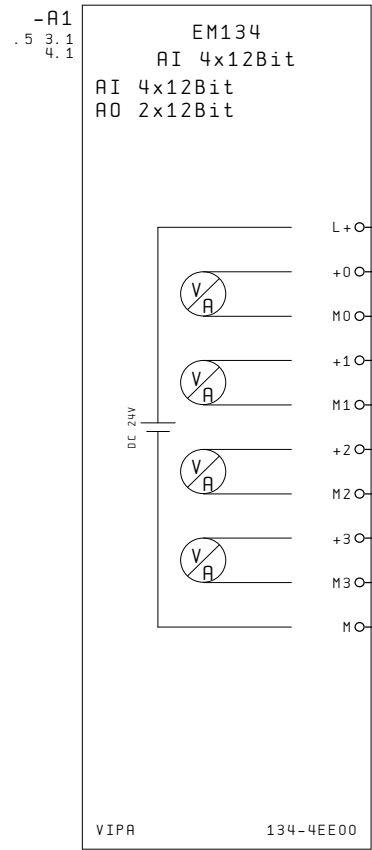
RESERVE

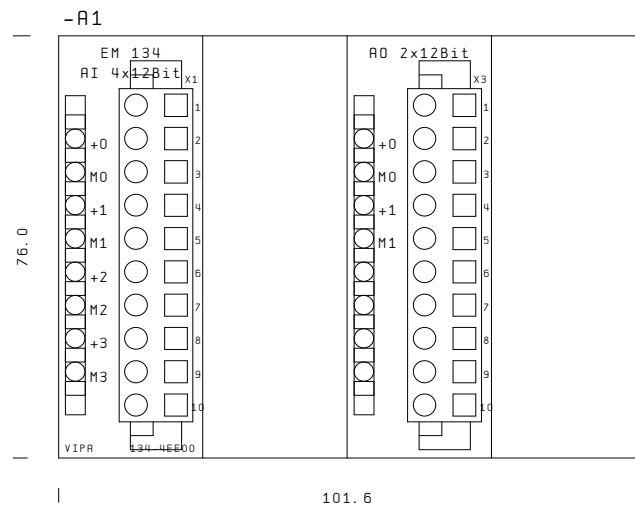
RESERVE

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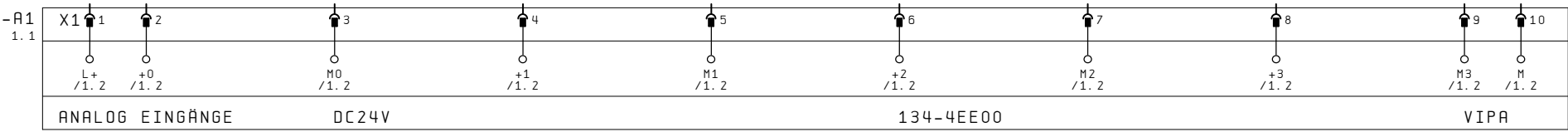
RESERVE





EM 134  
Abmessungen: (BxHxT) 101,6 x 76 x 48

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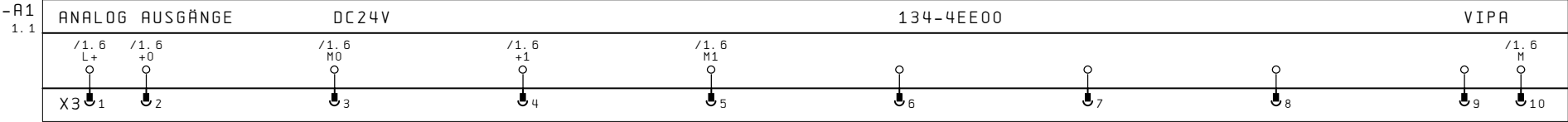
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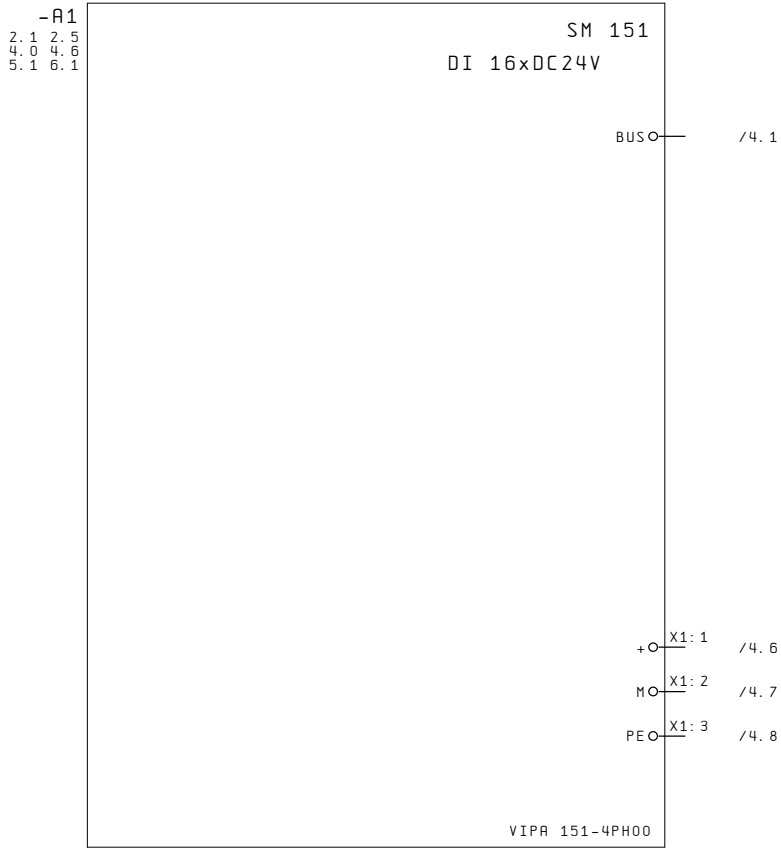
RESERVE

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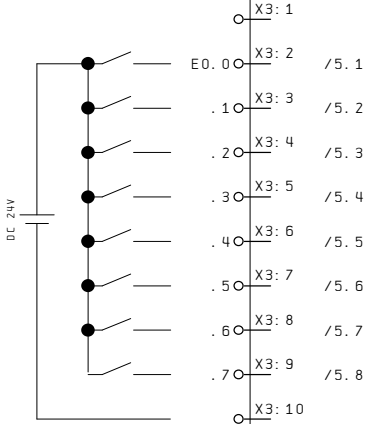
RESERVE

			Datum	14.07.03	Produktmakros für System 100V			Analog Ausgänge, EM 134 DC24V, 134-4EE00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+134_4EE00	
			Geänd.							System 100V		B1. 4
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.					4 B1.



-R1  
1.1

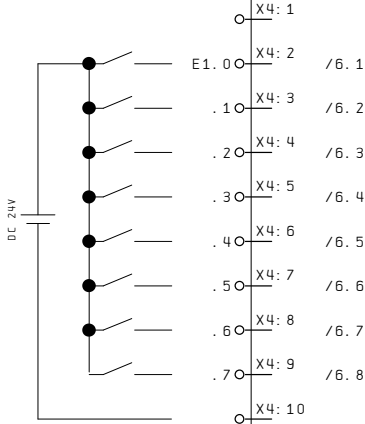
DI 8xDC24V



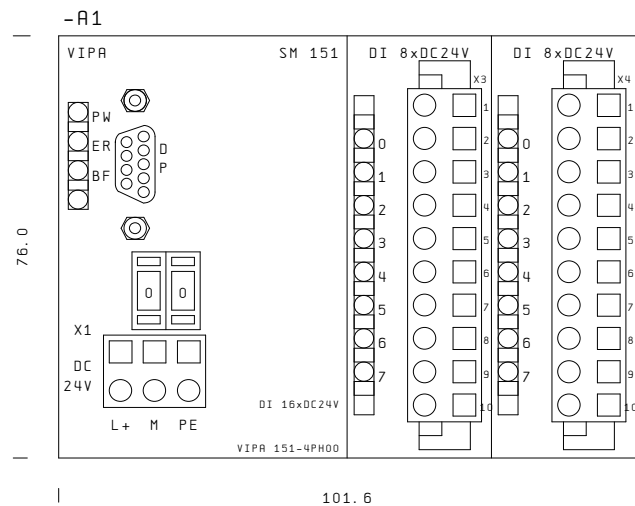
RESERVE  
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-R1  
1.1

DI 8xDC24V



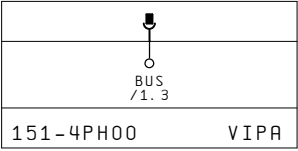
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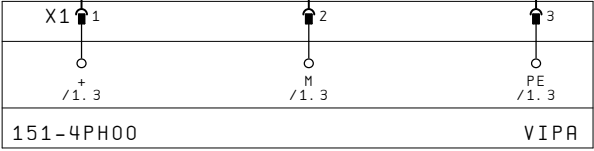
SM 151  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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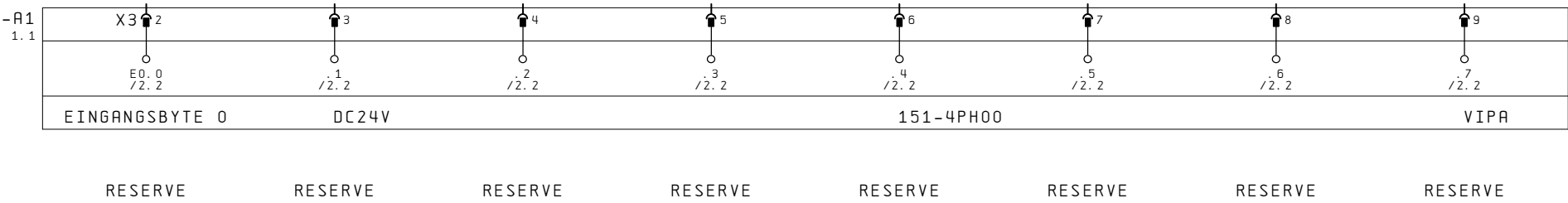
-R1  
1.1



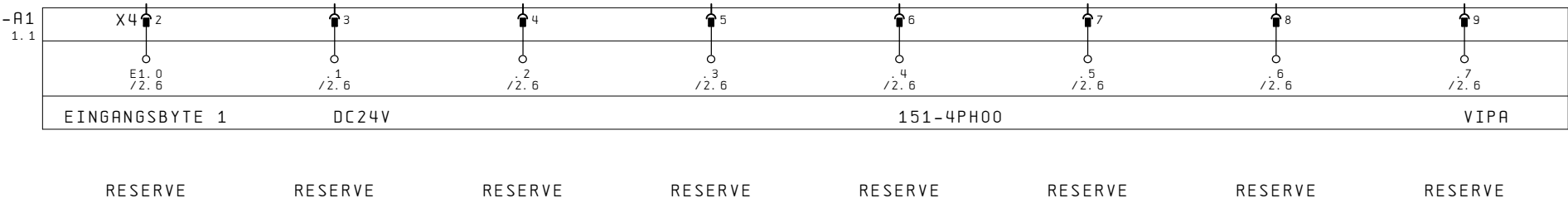
-R1  
1.1



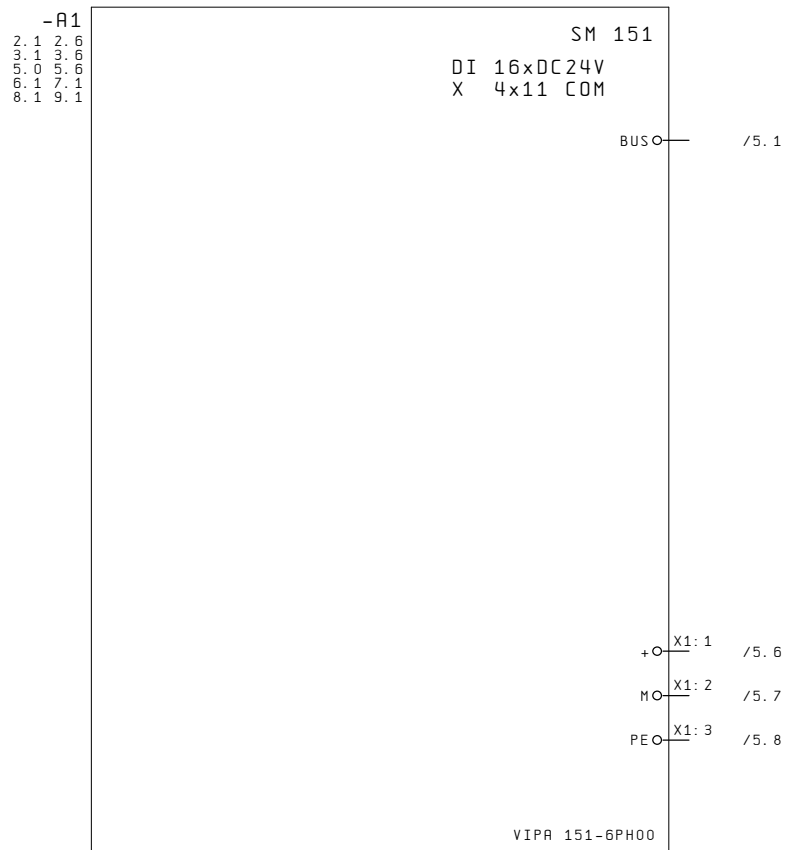
0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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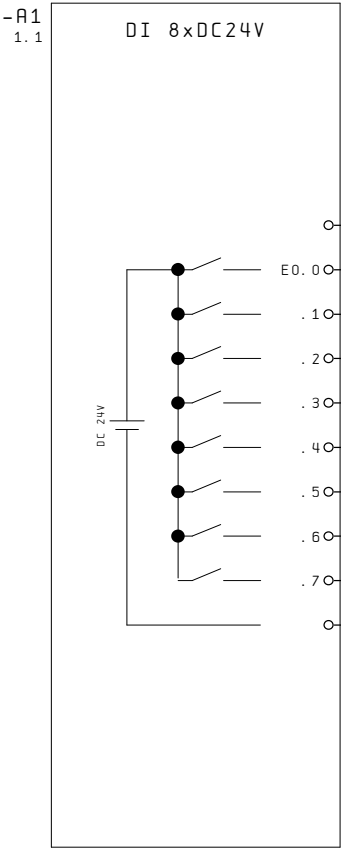


0	1	2	3	4	5	6	7	8	9
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+151_4PH00/6											2		
			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 151 DC24V, 151-6PH00		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+151_6PH00	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	1	
												9 B1.	





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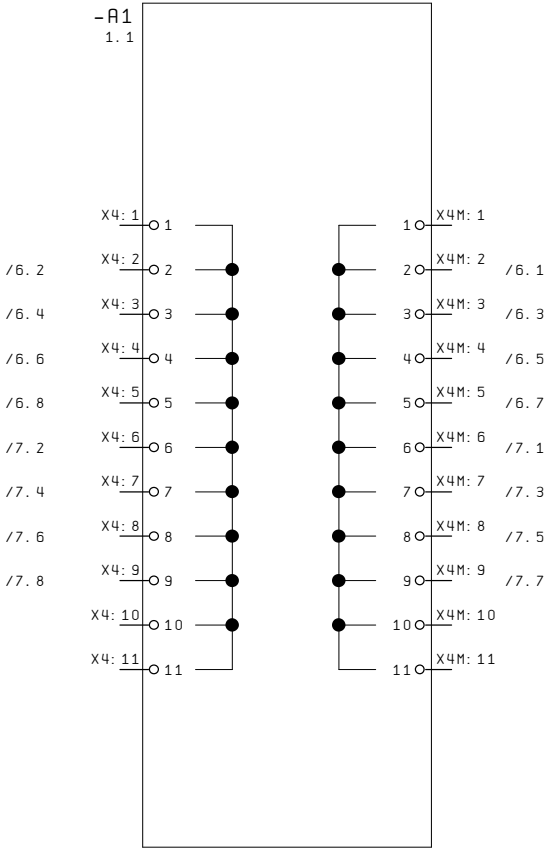
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/6. 2

/6. 4

/6. 6

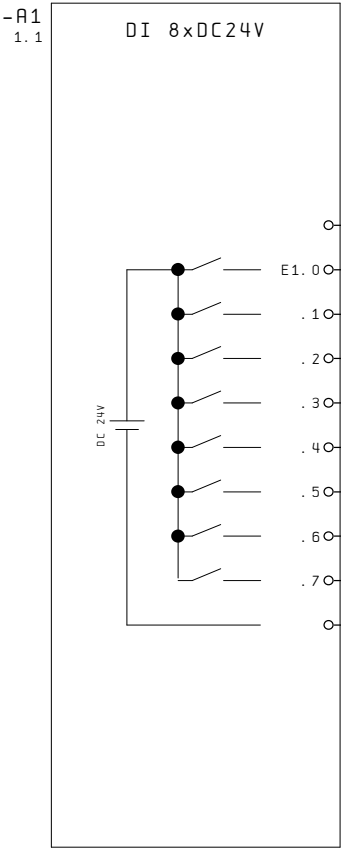
/6. 8

/7. 2

/7. 4

/7. 6

/7. 8



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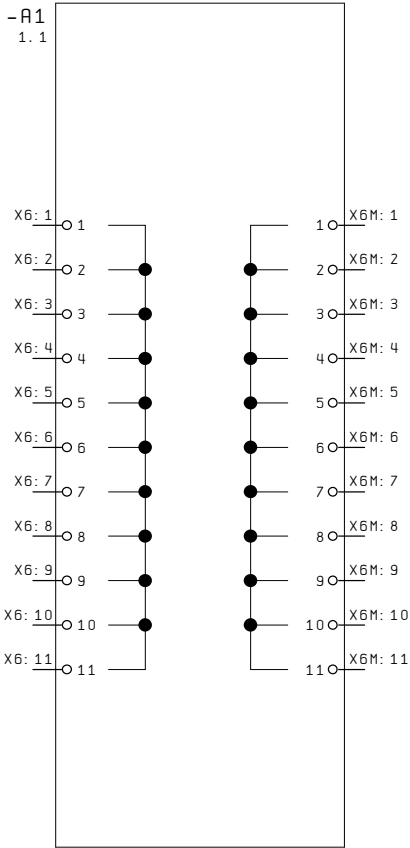
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/8.2

/8.4

/8.6

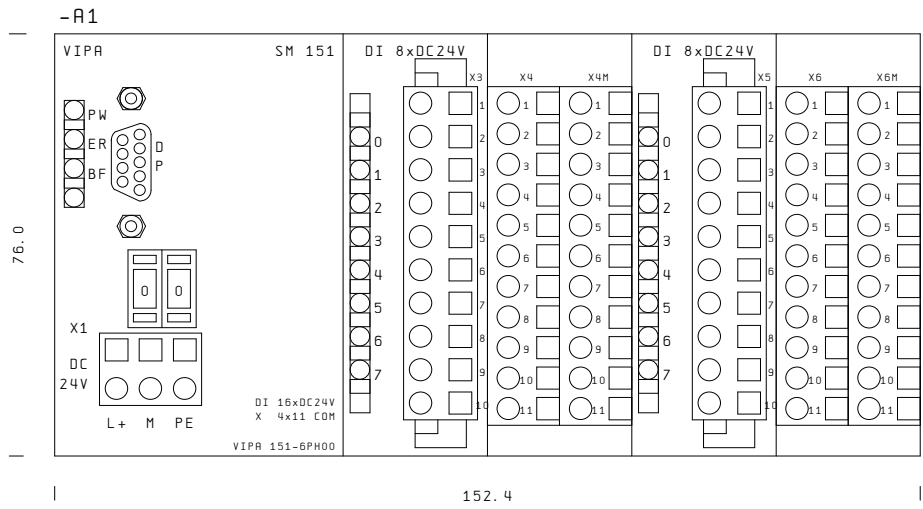
/8.8

/9.2

/9.4

/9.6

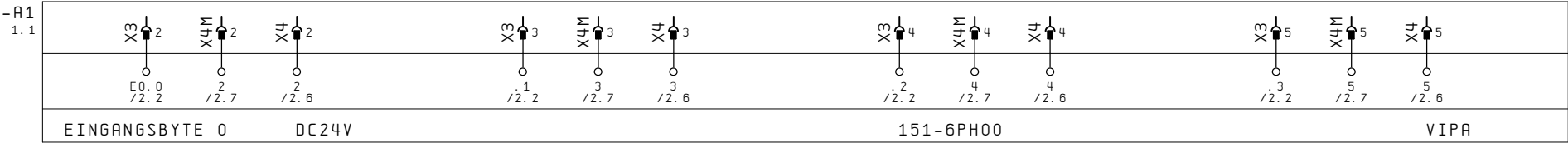
/9.8



SM 151  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48



0	1	2	3	4	5	6	7	8	9
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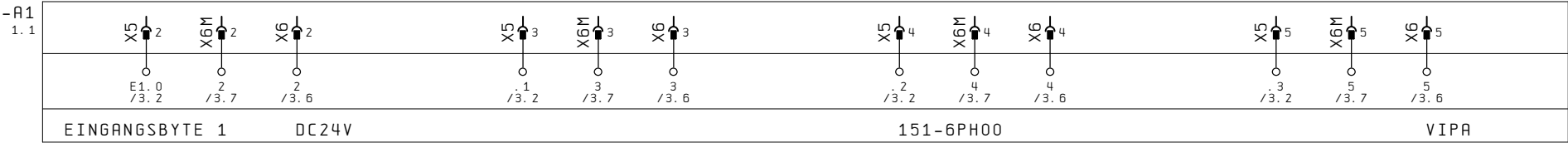
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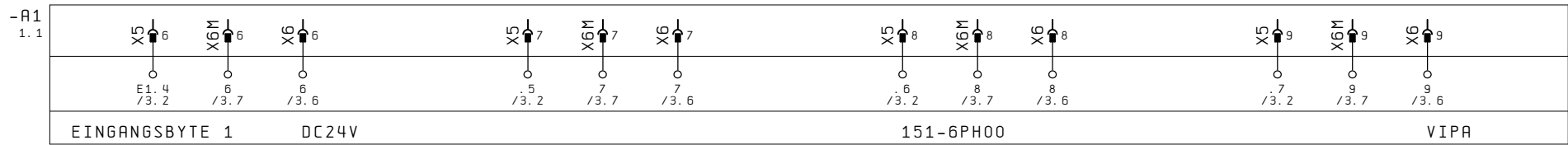
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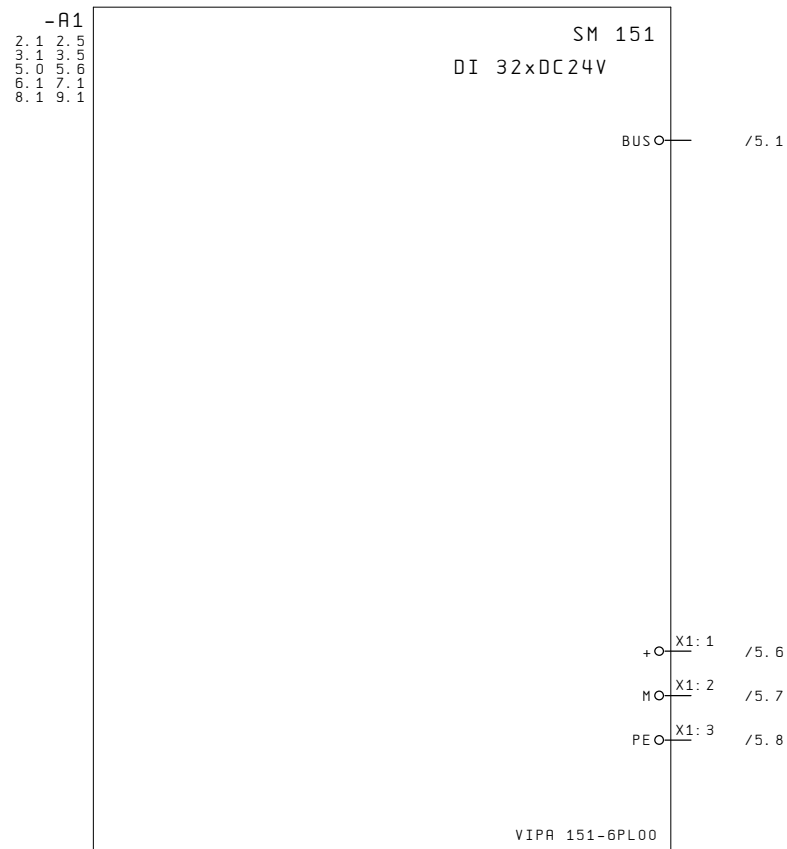
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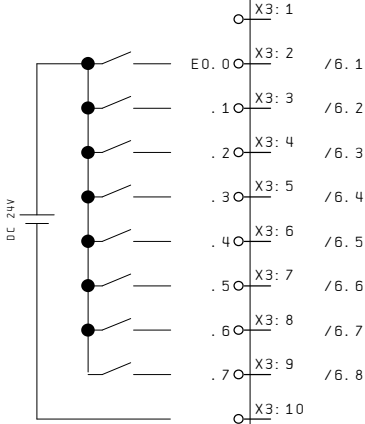
0	1	2	3	4	5	6	7	8	9
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+151_6PH00/9														2
			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 151 DC24V, 151-6PL00		VIPA100V		=SYSTEM100V		
			Bearb.	ZBW								+151_6PL00		
			Geänd.											
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	1	
												9 B1.		

-R1  
1.1

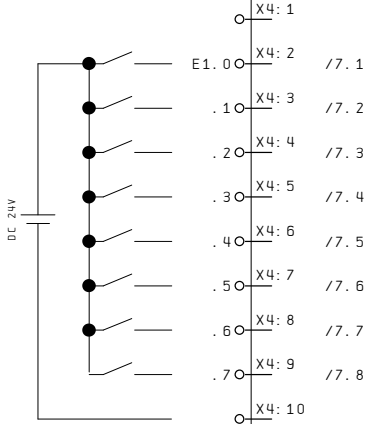
DI 8xDC24V



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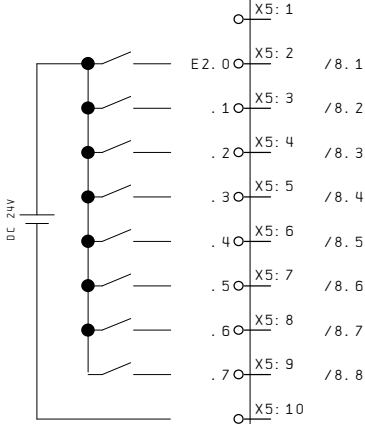
DI 8xDC24V



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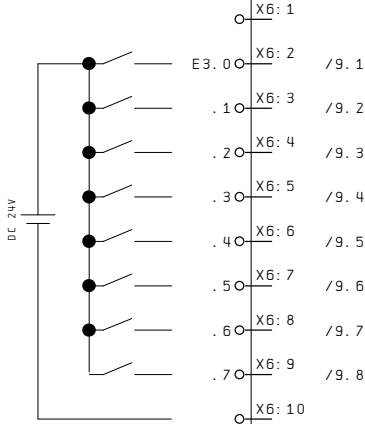
DI 8xDC24V



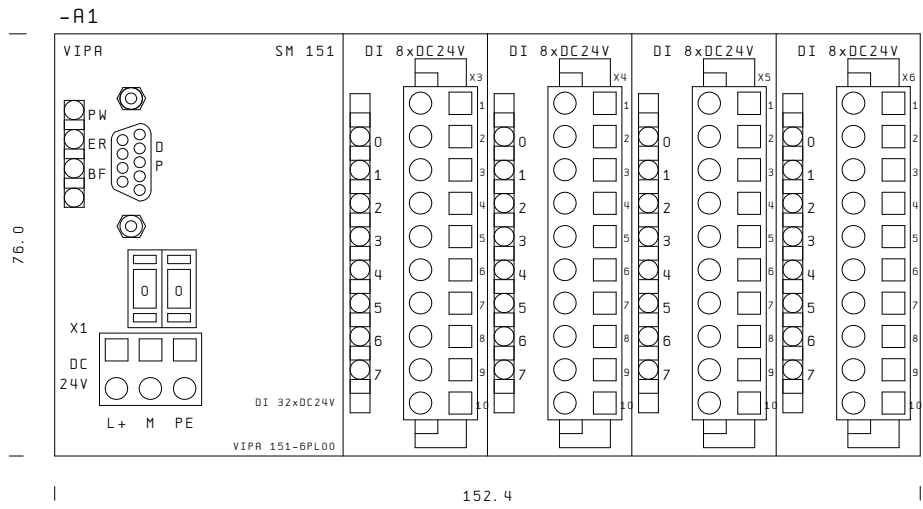
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DI 8xDC24V

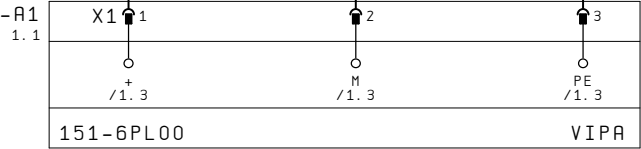
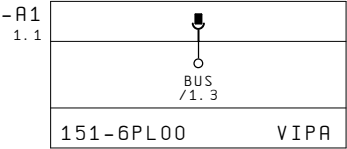


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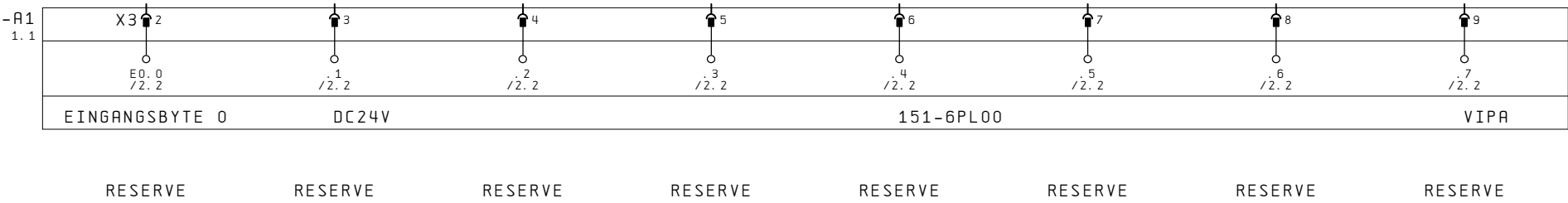
SM 151  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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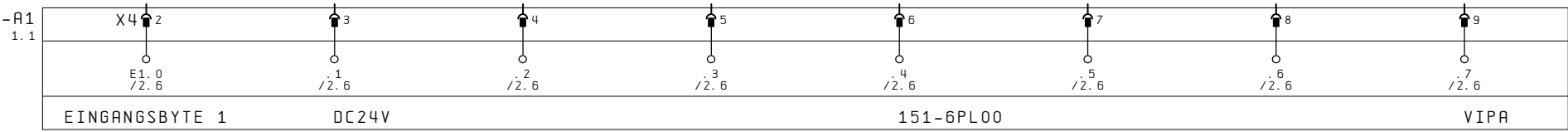


151-6PL00VIPA

0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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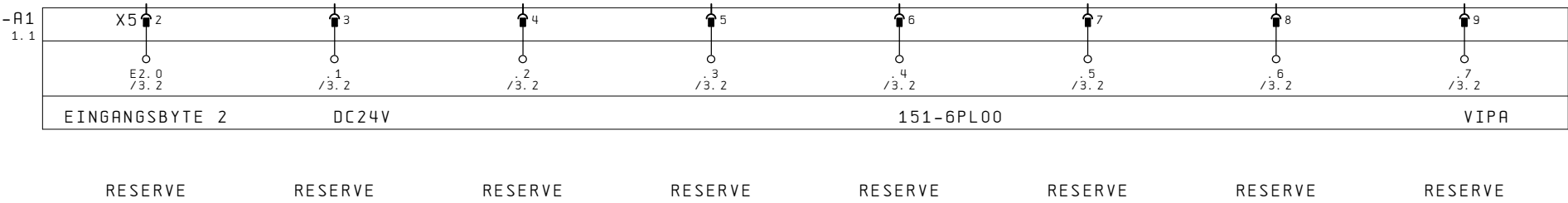
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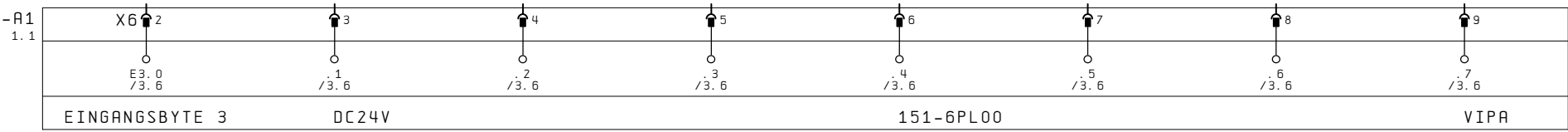
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0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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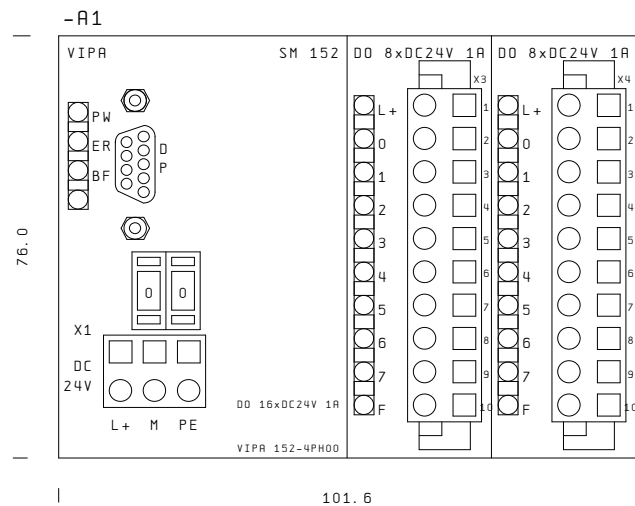
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			Datum	12.07.03	Produktmakros für System 100V		SPS-Übersicht Versorgung,	VIPA100V	=SYSTEM100V
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			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 152 DC24V, 152-4PH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+152_4PH00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	1
											6 B1	

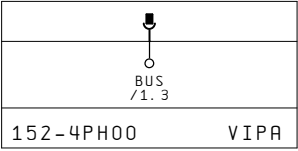




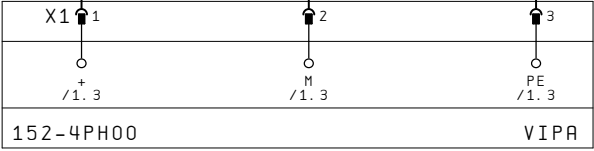
SM 152  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
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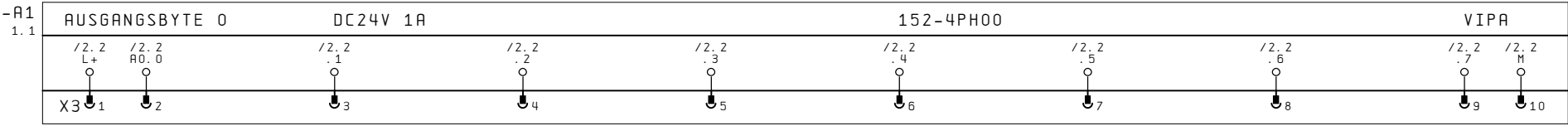


-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 152 DC24V, 152-4PH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+152_4PH00	
			Geänd.									
Änderung	Datum	Name	Form		Unspr.	Ers. f.	Ers. d.		System 100V		B1.	4
												6 B1.

0	1	2	3	4	5	6	7	8	9
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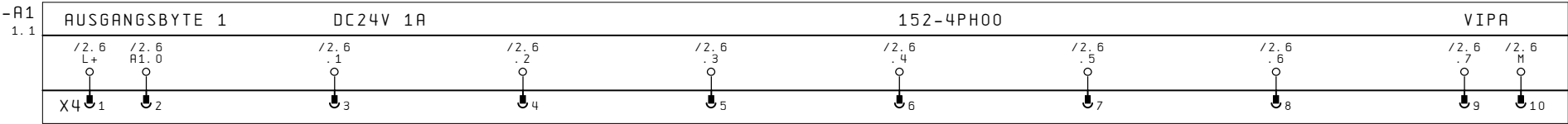
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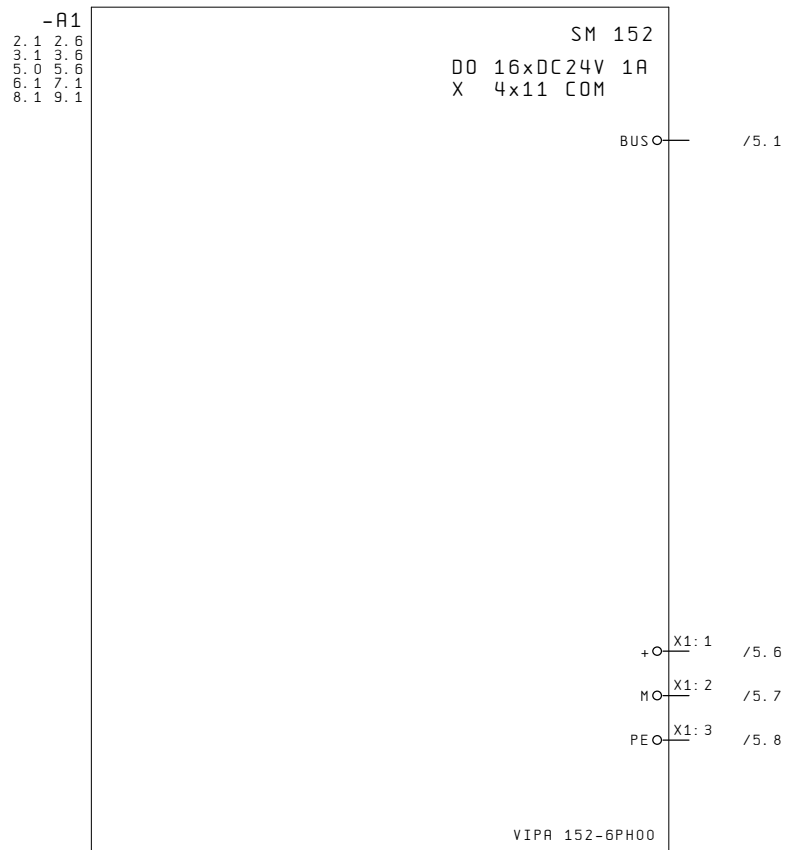
RESERVE

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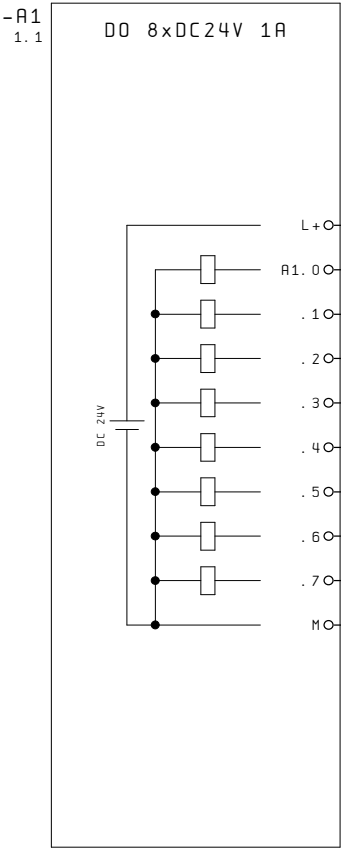
0	1	2	3	4	5	6	7	8	9
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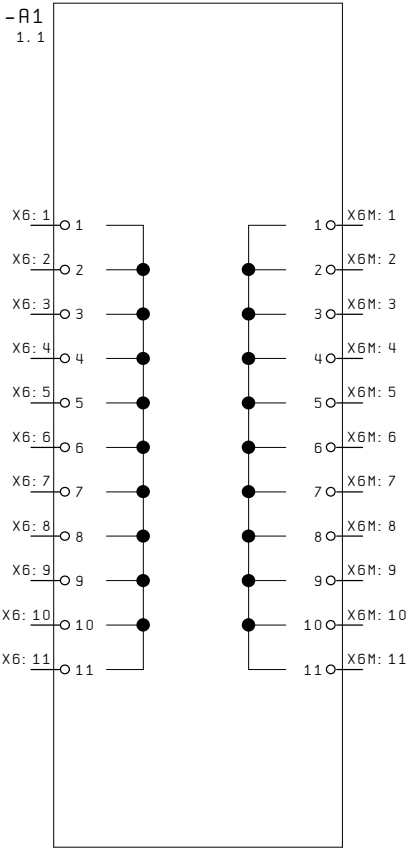
+152_4PH00/6											2		
			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung,		VIPA100V	=SYSTEM100V		
			Bearb.	ZBW				SM 152 DC24V,				+152_6PH00	
			Geänd.					152-6PH00				B1. 1	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		9 B1.	







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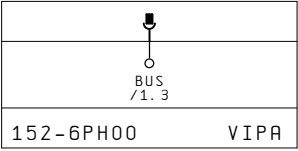


/8. 2  
/8. 4  
/8. 6  
/8. 8  
/9. 2  
/9. 4  
/9. 6  
/9. 8

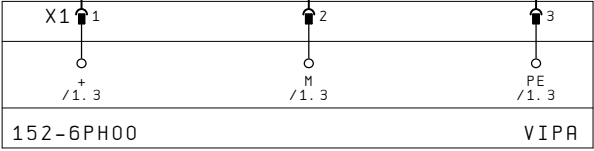


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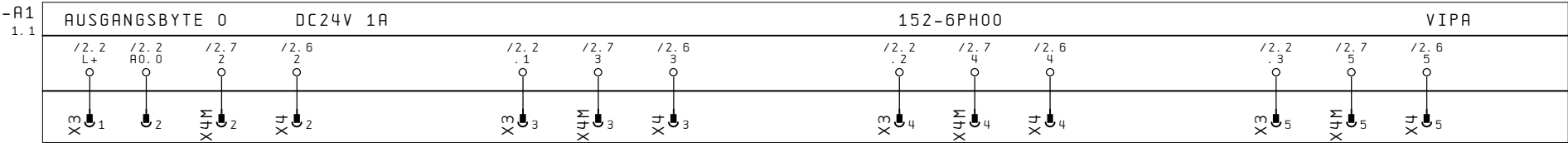
-R1  
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-R1  
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			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 152 DC24V, 152-6PH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+152_6PH00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	5
												9 B1.



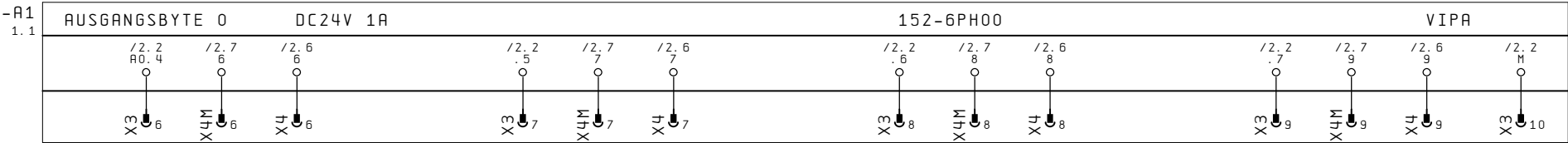
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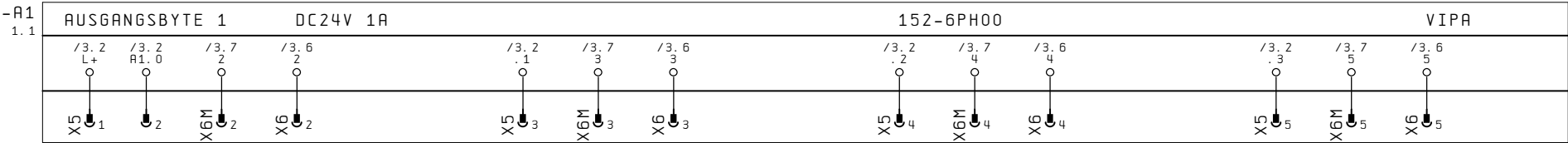


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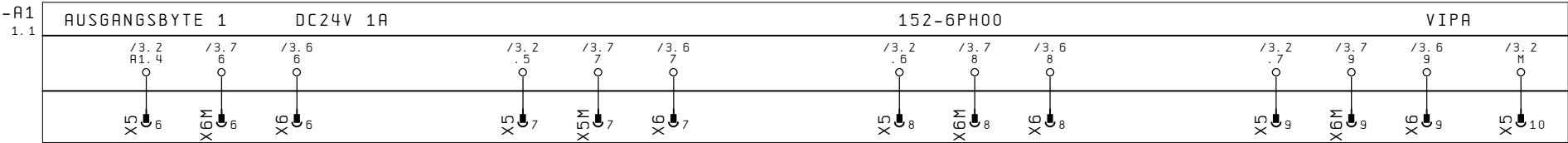
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0	1	2	3	4	5	6	7	8	9
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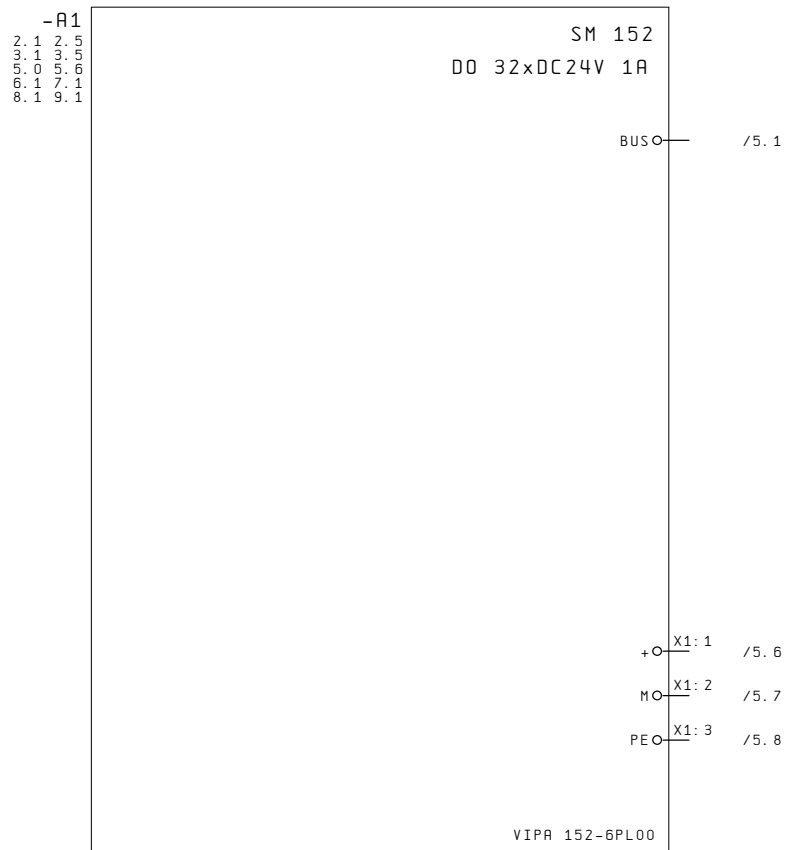
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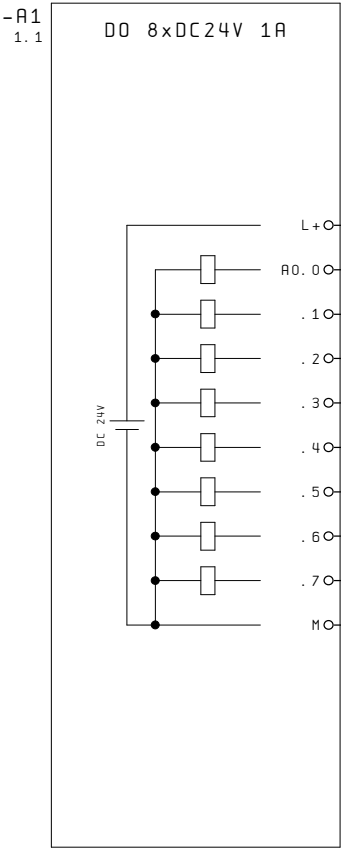
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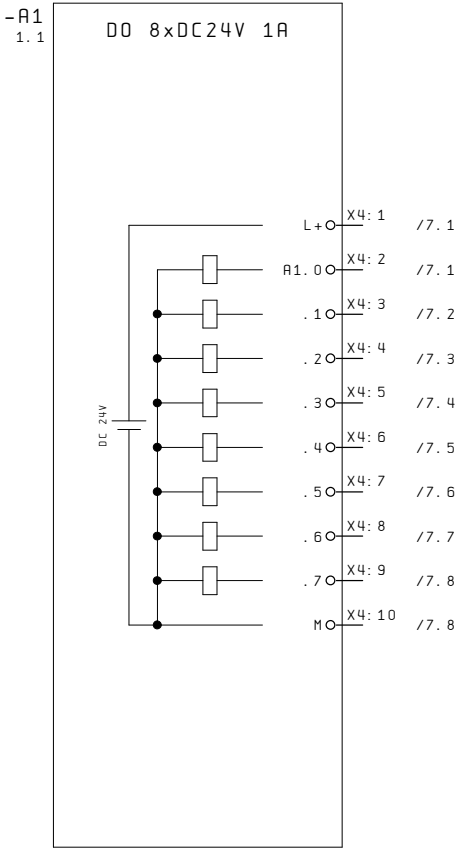
0	1	2	3	4	5	6	7	8	9
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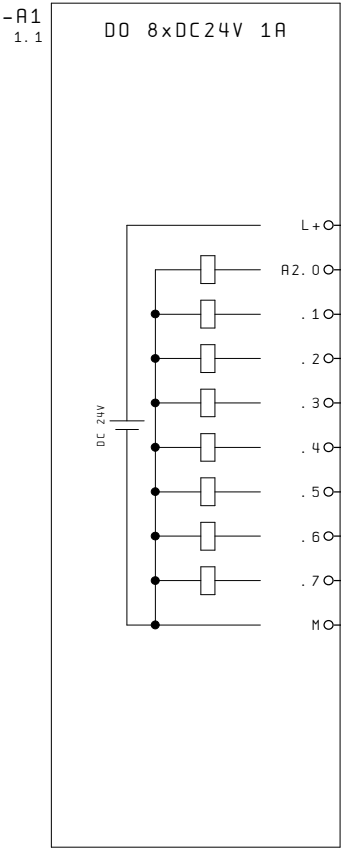
+152_6PH00/9													2
			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 152 DC24V, 152-6PL00		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+152_6PL00	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	1
													9 B1.



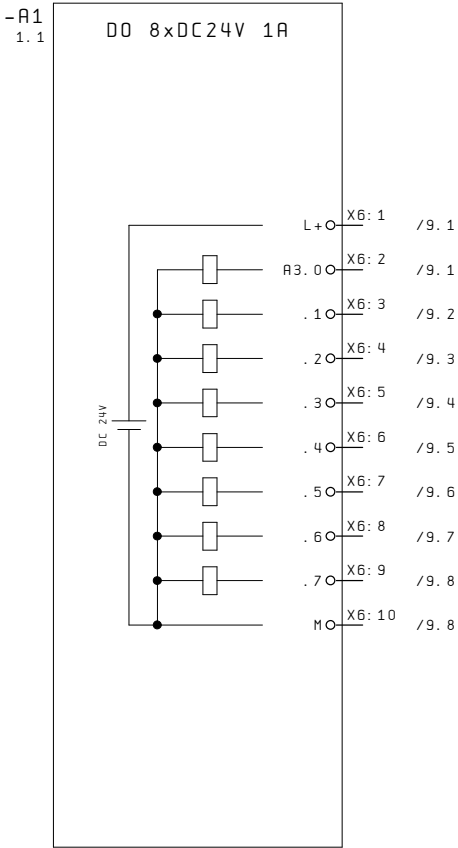
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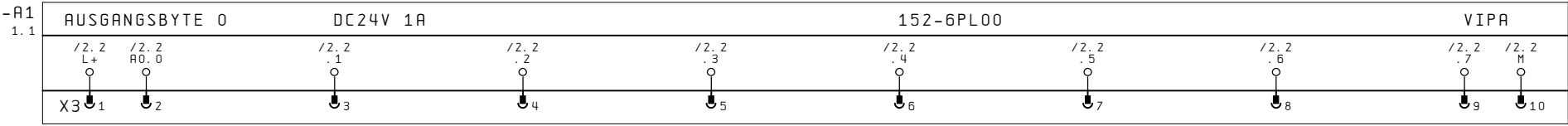


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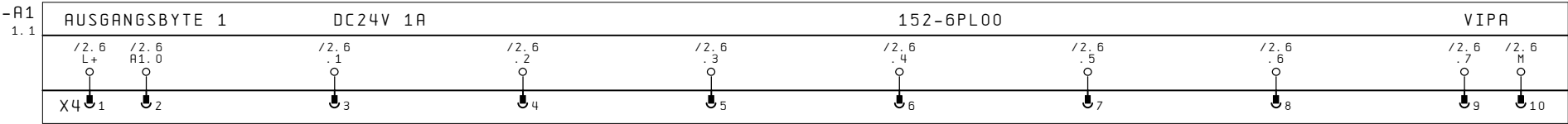
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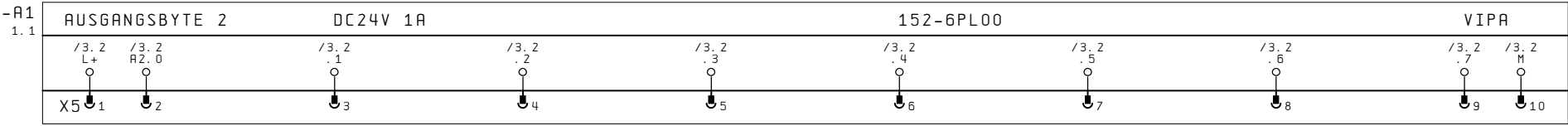
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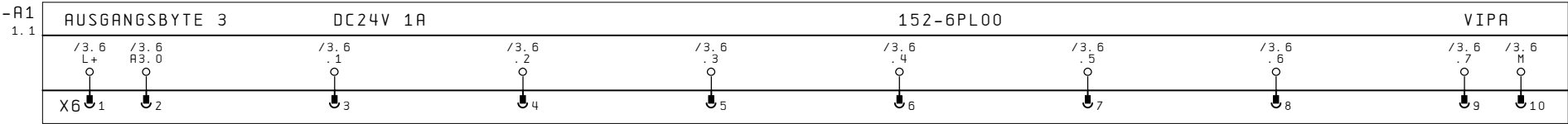
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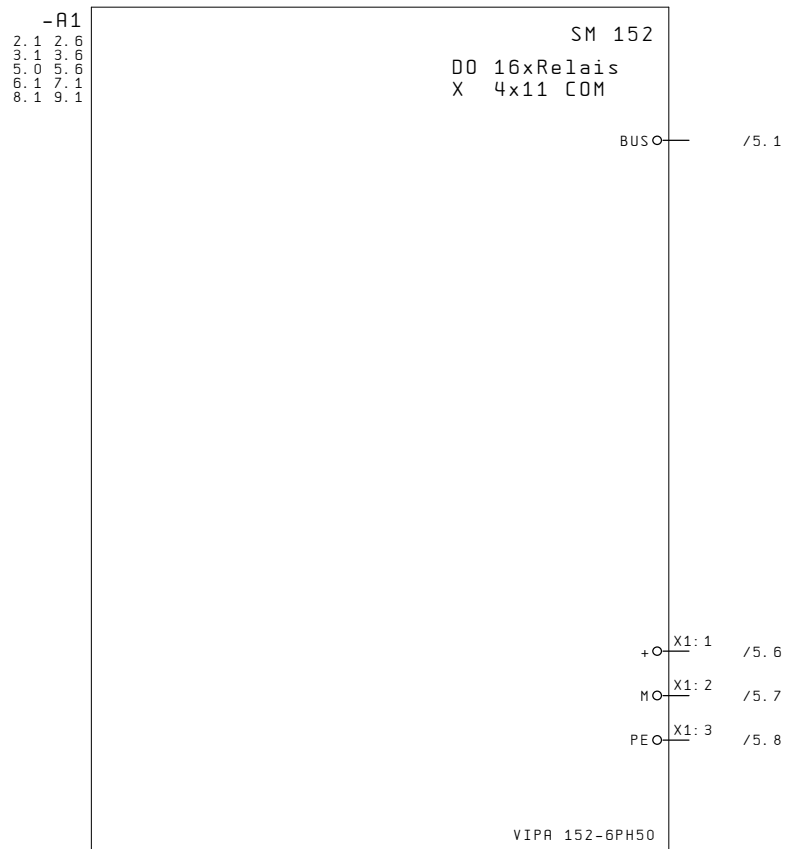
RESERVE

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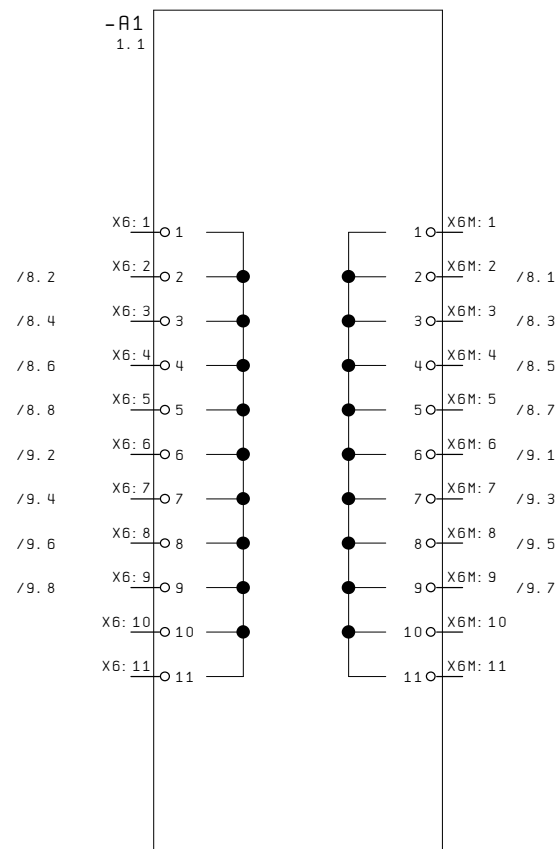
RESERVE

0	1	2	3	4	5	6	7	8	9
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			Datum	12.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 152 DC24, 152-6PH50	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+152_6PH50	
			Geänd.								B1.	1
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		9 B1.

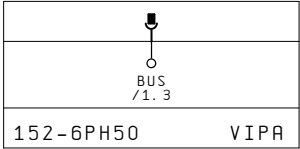




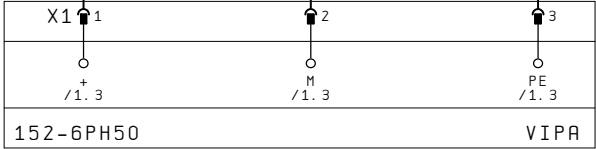


0	1	2	3	4	5	6	7	8	9
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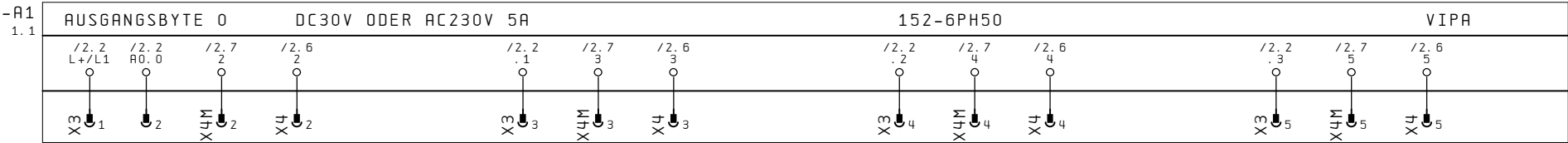
-R1  
1.1



-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 152 DC24, 152-6PH50	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+152_6PH50	
			Geänd.								B1.	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		9 B1.	



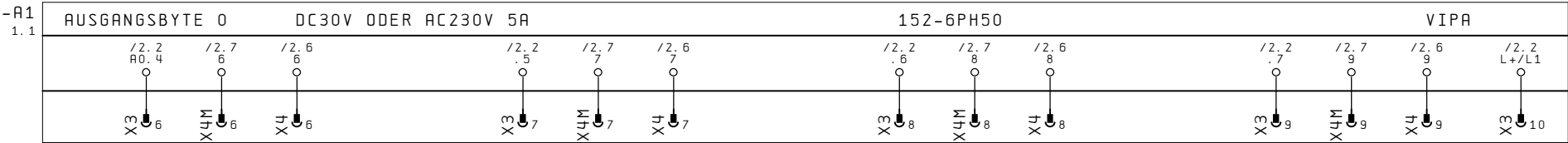
RESERVE

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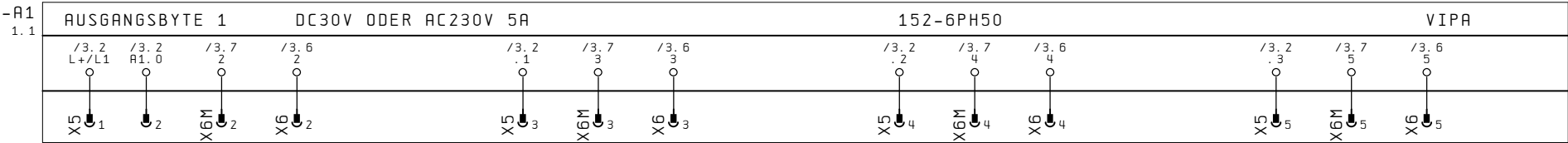
RESERVE

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	AUSGANGSBYTE 1	DC30V ODER AC230V 5A	152-6PH50	VIPA
-R1 1.1	 X5 6	 X6M 6	 X6 6	 X5 8
	RESERVE	RESERVE	RESERVE	RESERVE

-A1

2.1	2.6
5.0	5.6
6.0	7.0

SM 153

DIO 8xDC24V 1A  
X 2x11 COM

BUSO /5. 1

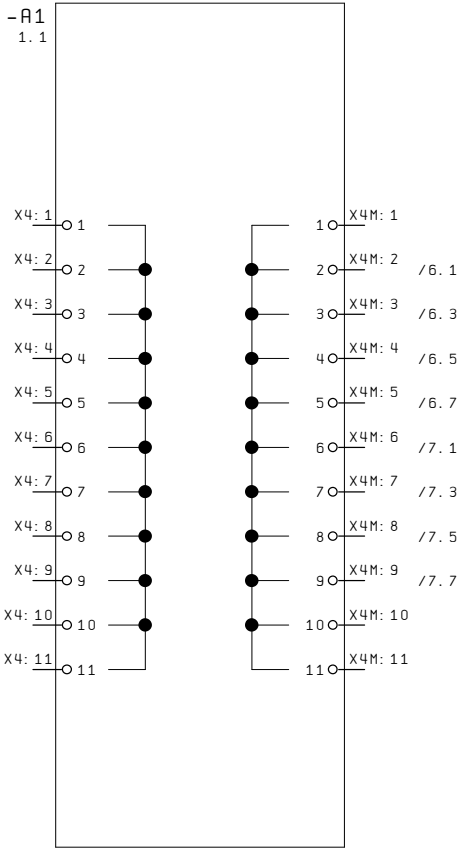
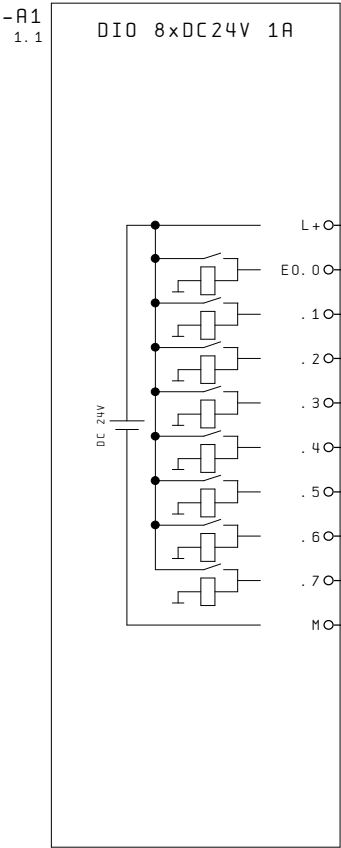
+X1: 1 /5. 6  
NO X1: 2 /5. 7  
PEO X1: 3 /5. 8

VIPA 153-4PF00

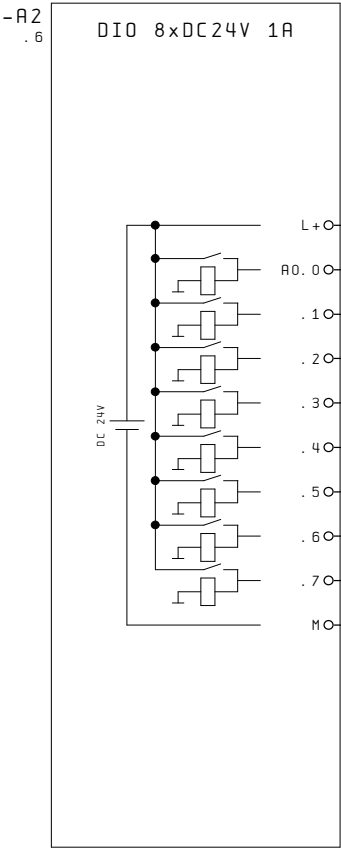
2

[illegible]

Variante 1: 8 Eingänge



Variante 2: 8 Ausgänge



RESERVE

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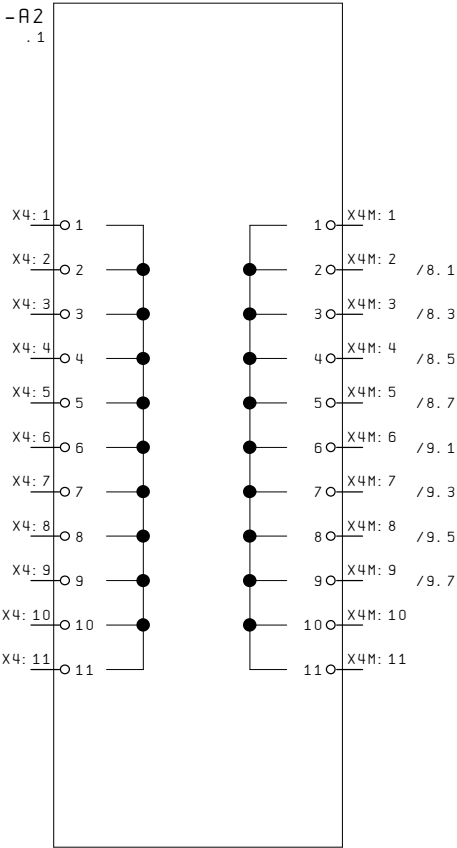
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/8. 2

/8. 4

/8. 6

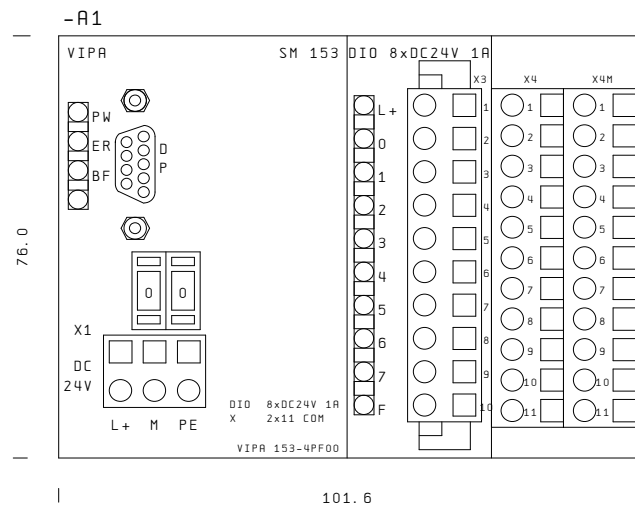
/8. 8

/9. 2

/9. 4

/9. 6

/9. 8

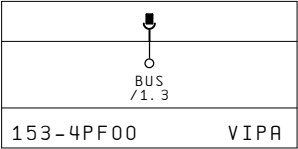


SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

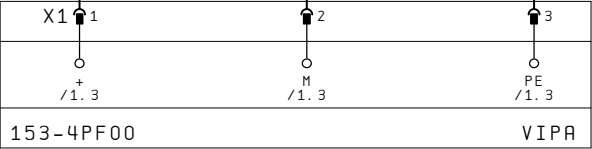
			Datum	07.05.05	Produktmakros für System 100V			Frontansicht, SM 153 DC24V, 153-4PF00	VIPR100V		=SYSTEM100V +153_4PF00	
			Bearb.	ZBW							B1.	4
			Geänd.									9 B1.
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		

0	1	2	3	4	5	6	7	8	9
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-R1  
1.1



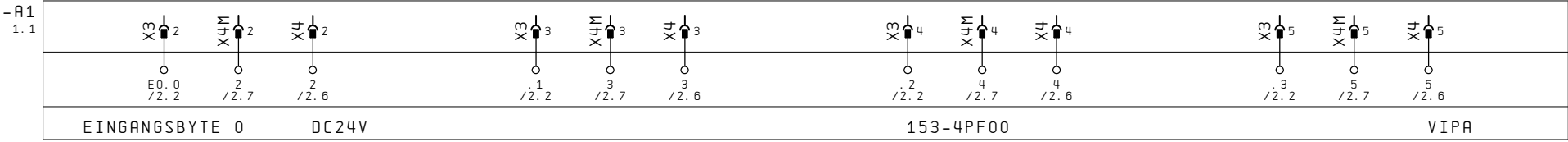
-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 153 DC24V, 153-4PF00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_4PF00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	5
												9 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge



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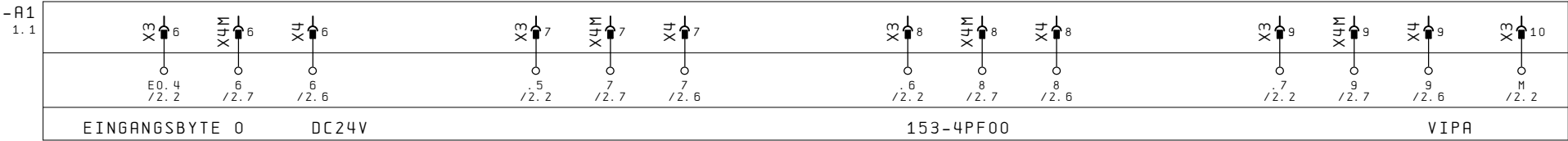
RESERVE

5				Datum	07.05.05	Produktmakros für System 100V		Eingangsbyte 0, SM 153 DC24V, 153-4PF00	VIPA100V		=SYSTEM100V +153_4PF00			
				Bearb.	ZBW								B1.	6
				Geänd.										9 B1.
Änderung	Datum	Name	Form			Urspr.	Ers. f.	Ers. d.			System 100V			



0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge



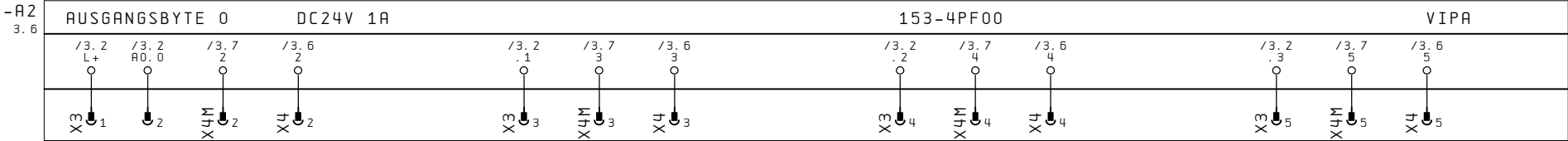
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Variante 2: 8 Ausgänge



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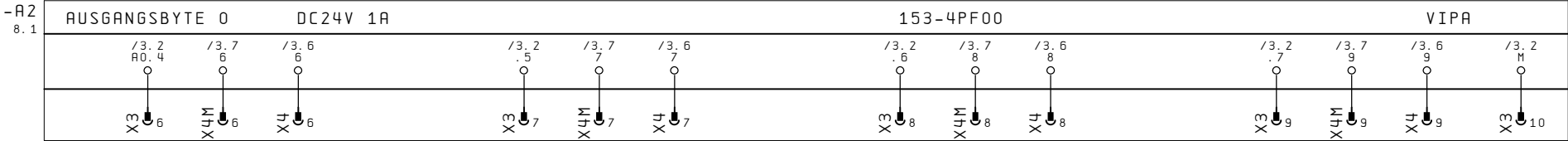
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0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Ausgänge



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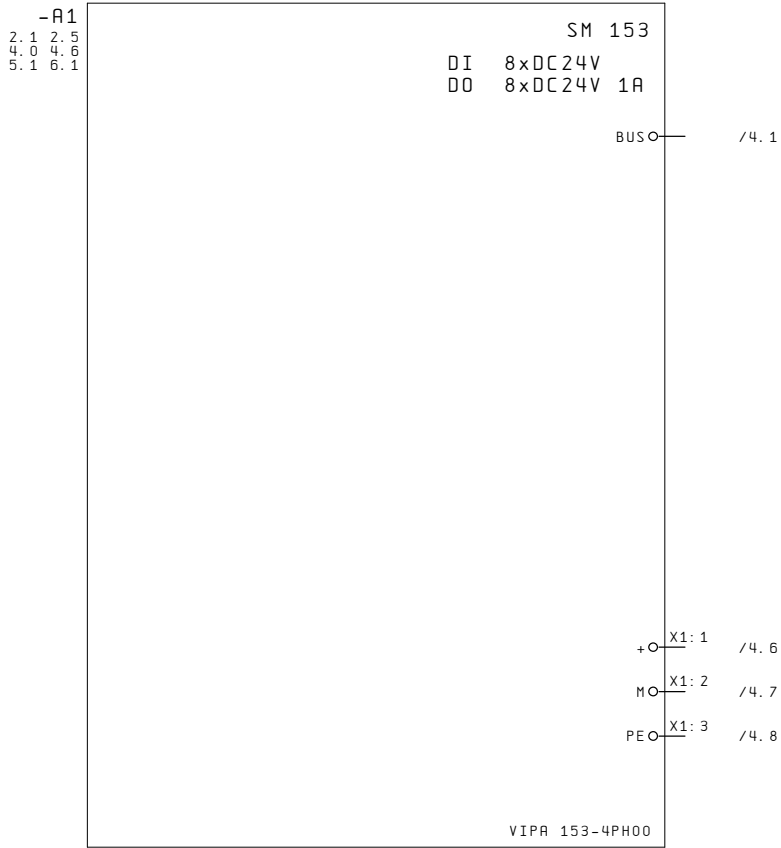
RESERVE

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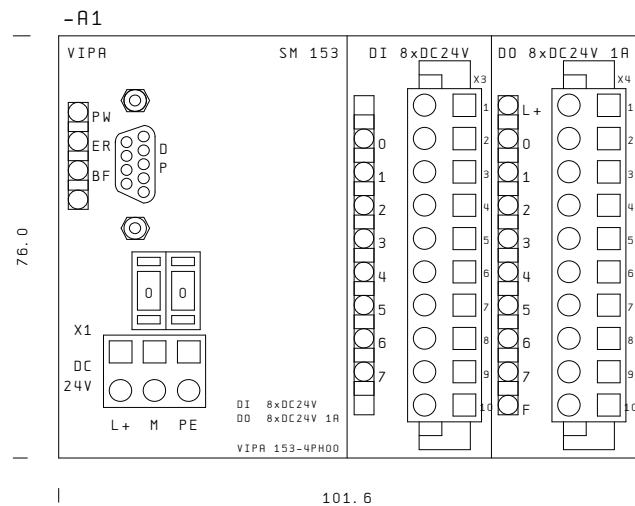
			Datum	07.05.05	Produktmakros für System 100V				Ausgangsbyte 0, SM 153 DC24V, 153-4PF00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+153_4PF00	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	9	
											9 B1.		

0	1	2	3	4	5	6	7	8	9
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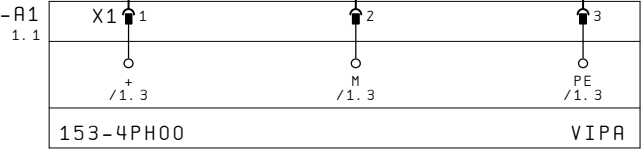
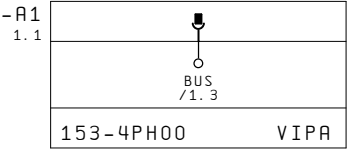
+153_4PF00/9												2			
			Datum	14.07.03	Produktmakros für System 100V				SPS-Übersicht Versorgung, SM 153 DC24V, 153-4PH00		VIPA100V		=SYSTEM100V		
			Bearb.	ZBW									+153_4PH00		
			Geänd.												
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.				System 100V		B1.	1	
														6 B1.	



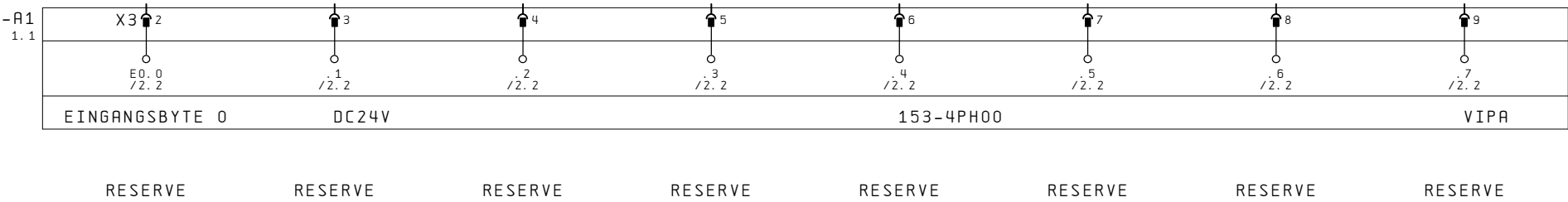


SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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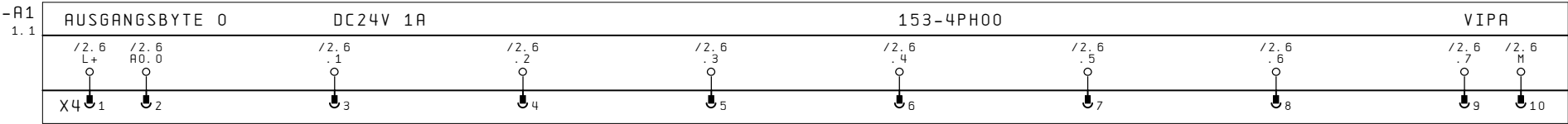


0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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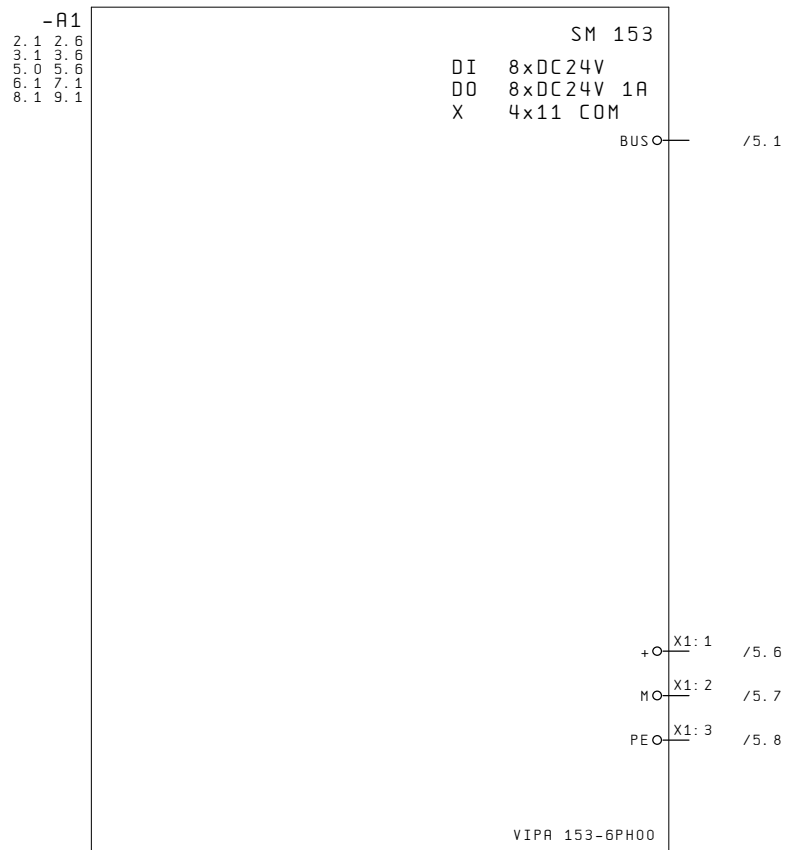
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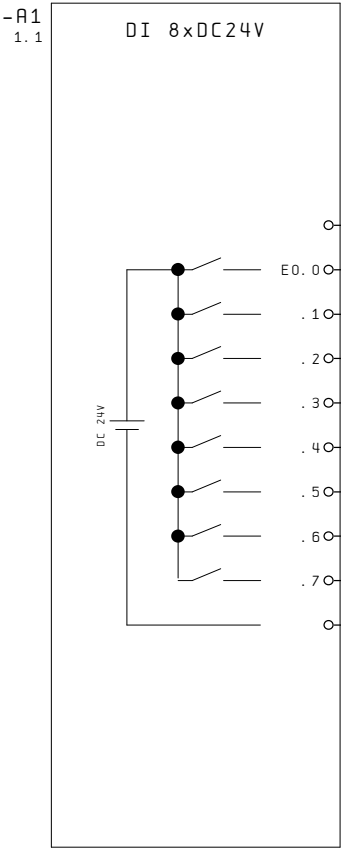
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0	1	2	3	4	5	6	7	8	9
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+153_4PH00/6												2
			Datum	14.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 153 DC24V, 153-6PH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_6PH00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V	B1.	1
											9 B1.	



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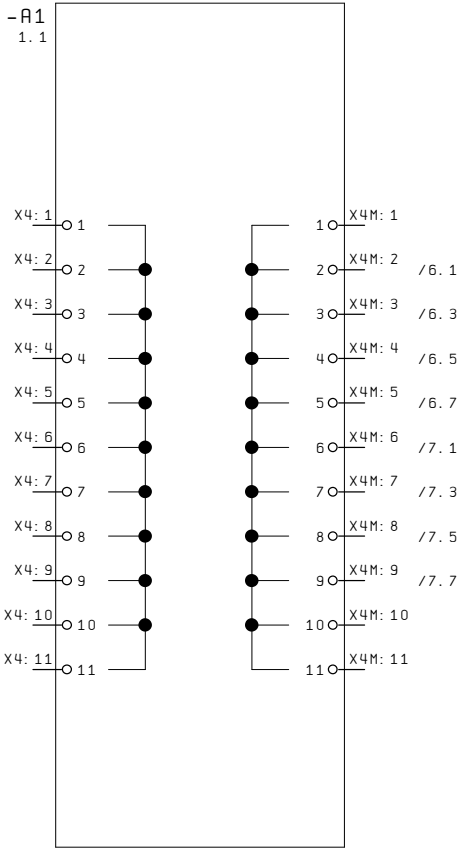
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/6. 2

/6. 4

/6. 6

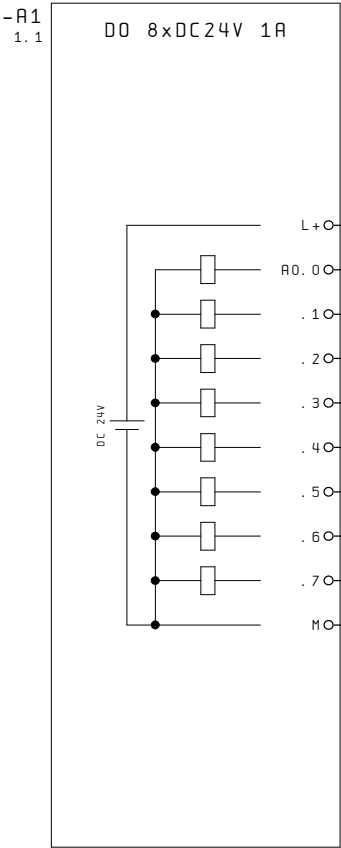
/6. 8

/7. 2

/7. 4

/7. 6

/7. 8



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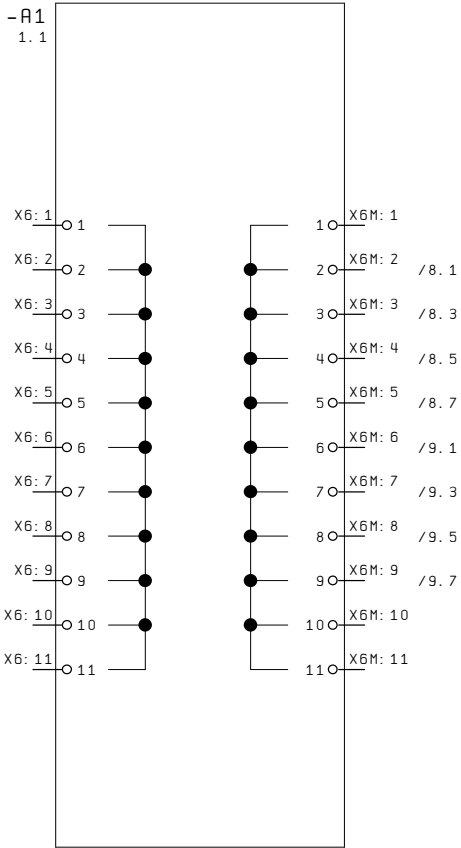
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/8. 2

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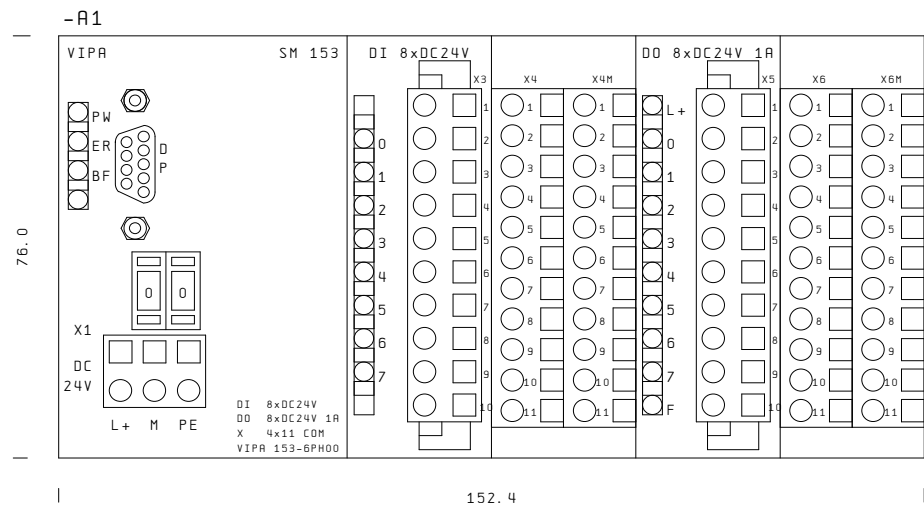
/8. 8

/9. 2

/9. 4

/9. 6

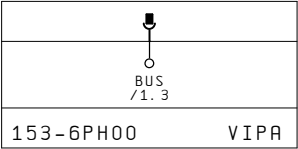
/9. 8



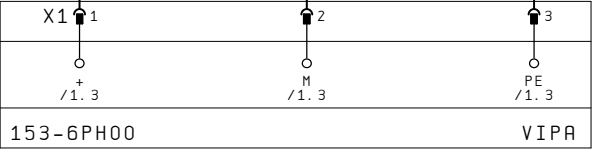
SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
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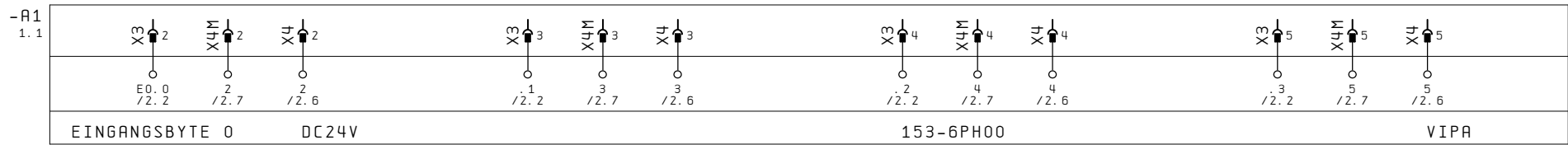


-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 153 DC24V, 153-6PH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_6PH00	
			Geänd.								B1.	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		9 B1.	

0	1	2	3	4	5	6	7	8	9
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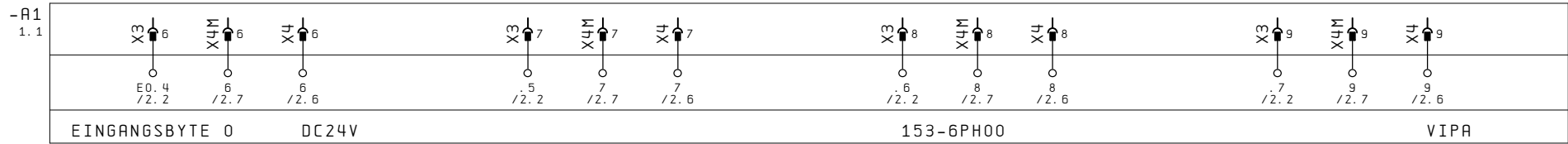
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0	1	2	3	4	5	6	7	8	9
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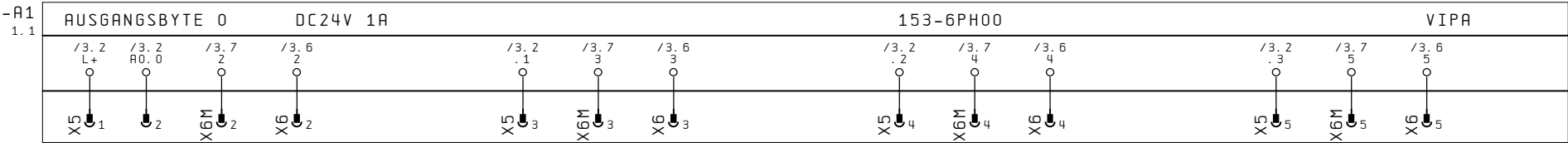
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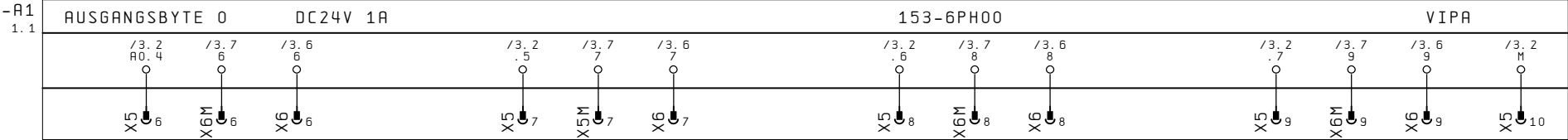
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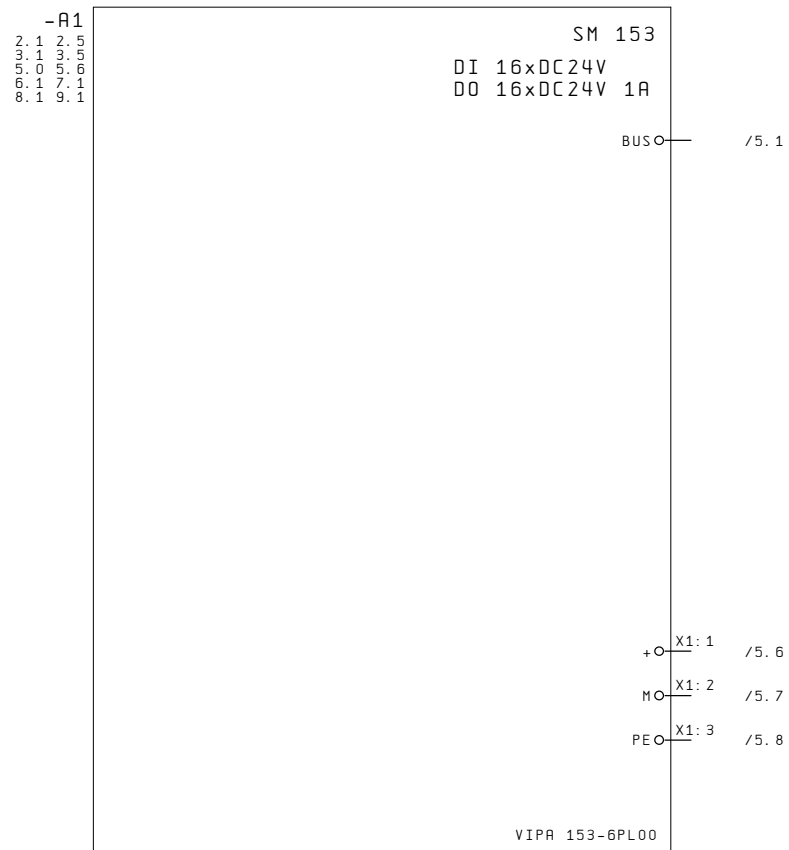
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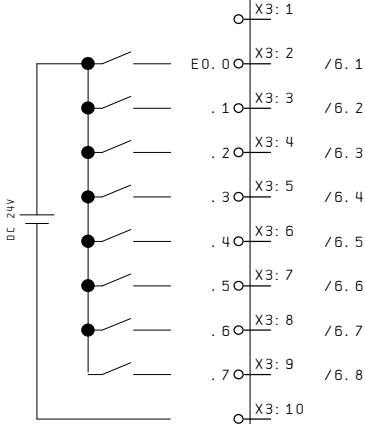
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+153_6PH00/9															2
			Datum	14.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 153 DC24V, 153-6PL00	VIPA100V		=SYSTEM100V				
			Bearb.	ZBW							+153_6PL00				
			Geänd.										B1.	1	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V				9 B1.		

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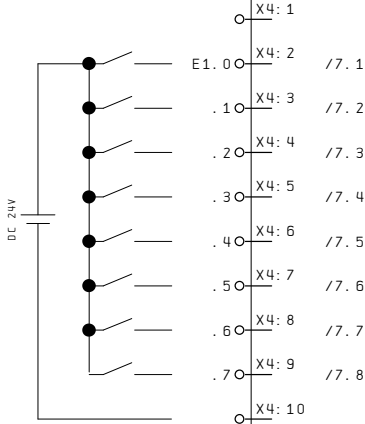
DI 8xDC24V



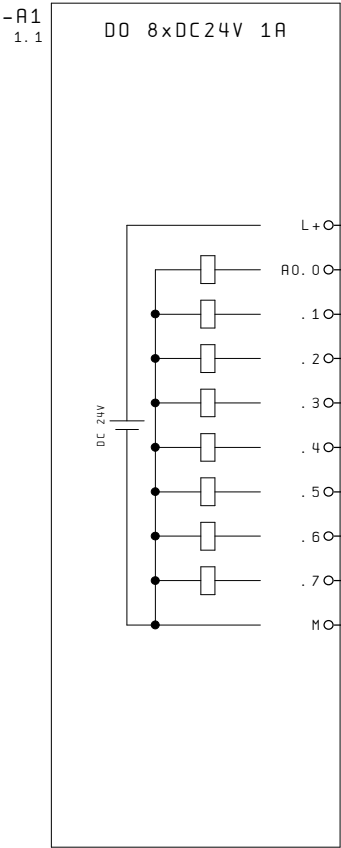
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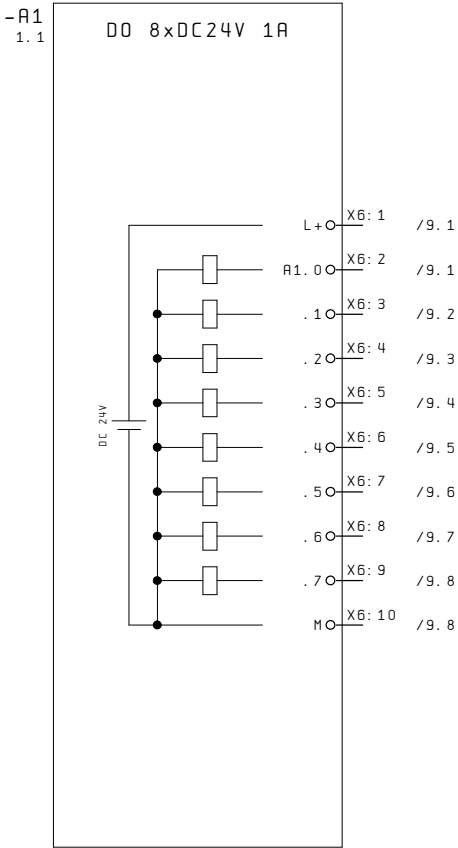
DI 8xDC24V



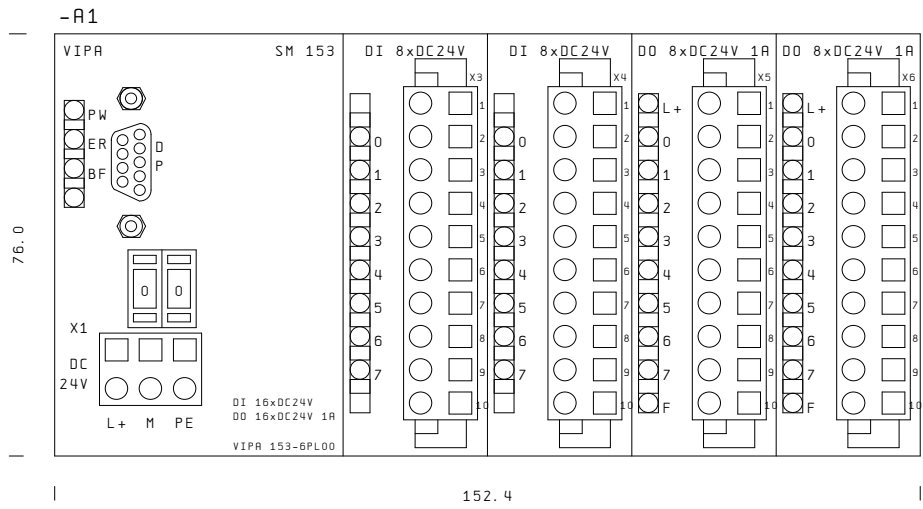
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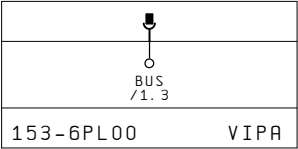
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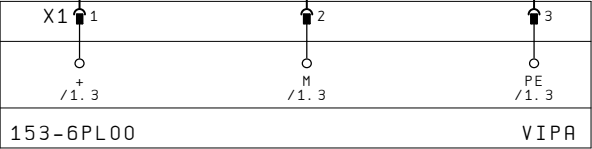
SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
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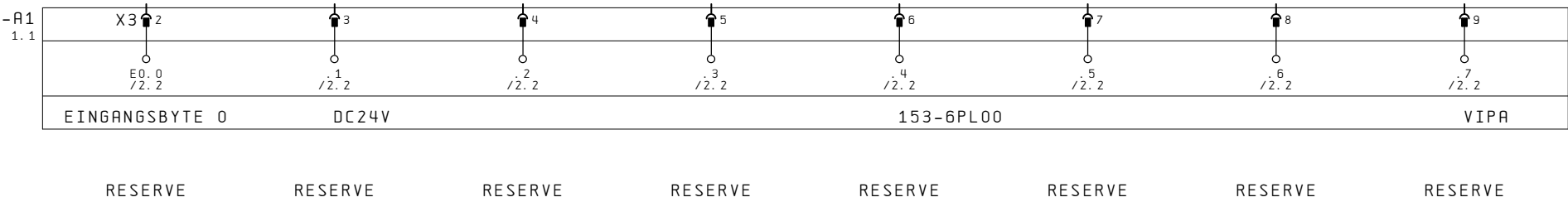


-R1  
1.1



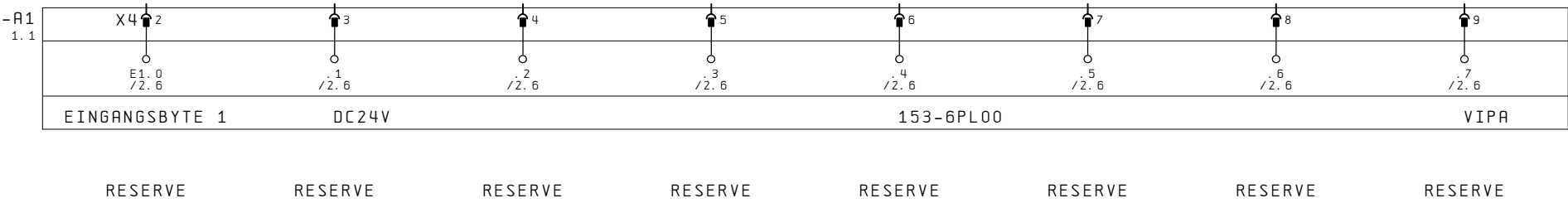
			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 153 DC24V, 153-6PL00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_6PL00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	5
												9 B1.

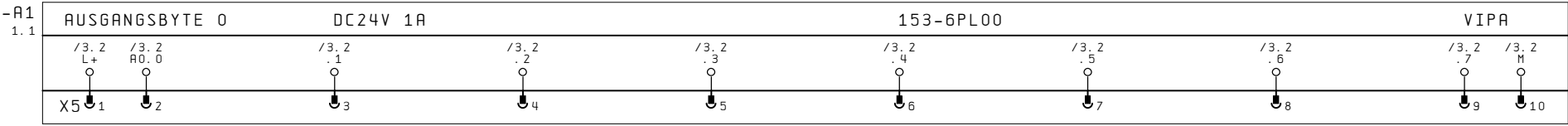
0	1	2	3	4	5	6	7	8	9
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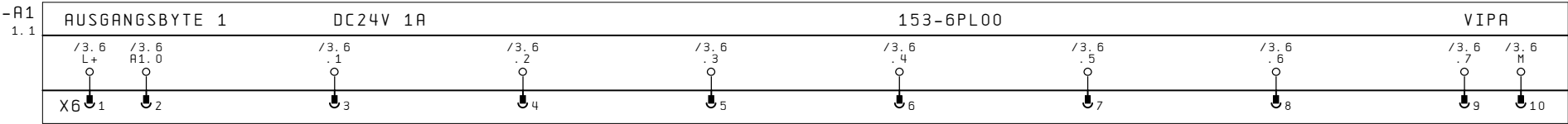
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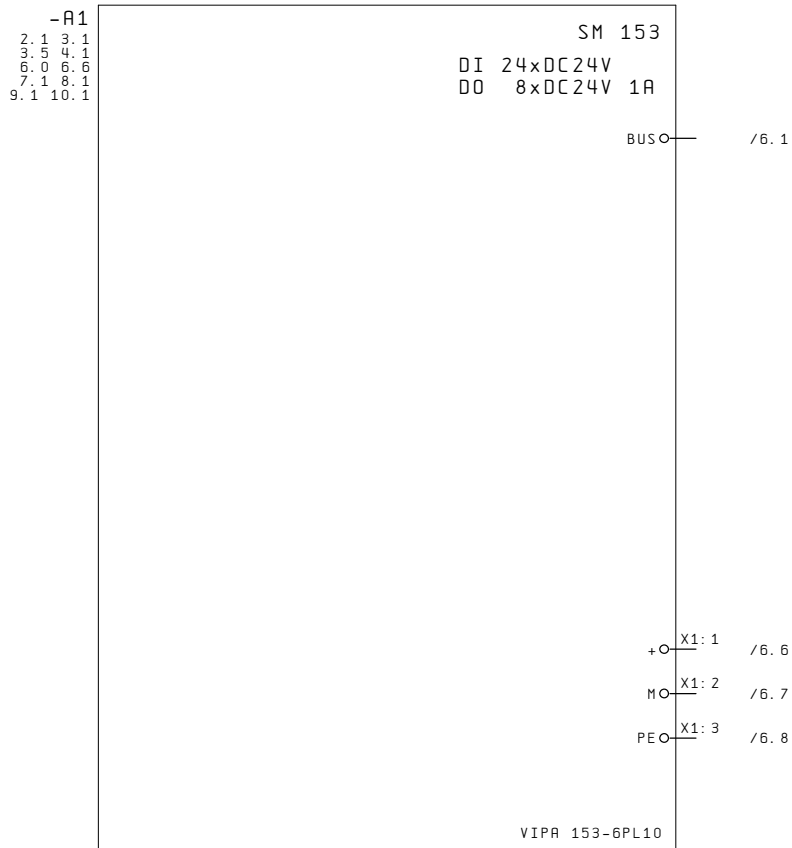
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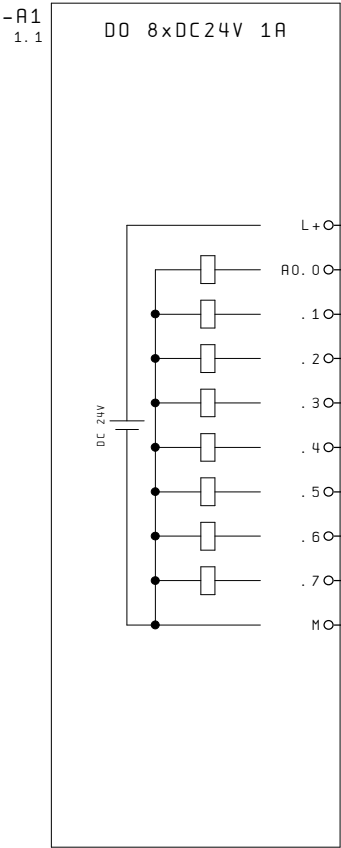
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0	1	2	3	4	5	6	7	8	9
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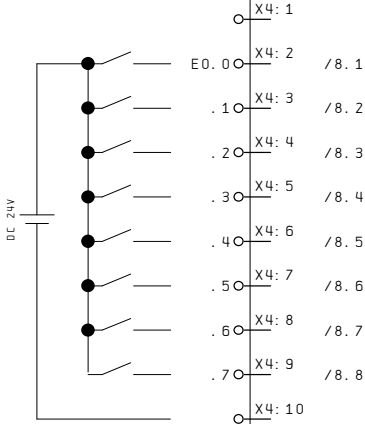
+153_6PL00/9											2		
			Datum	14.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung,		VIPA100V	=SYSTEM100V		
			Bearb.	ZBW				SM 153 DC24V,				+153_6PL10	
			Geänd.					153-6PL10				B1. 1	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		10 B1.	



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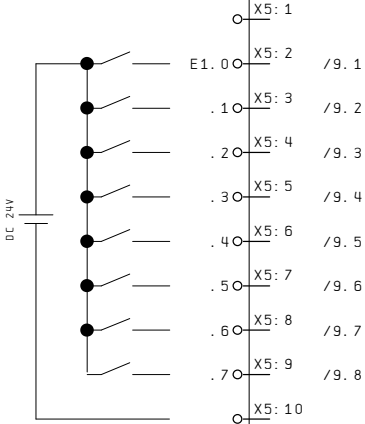
DI 8xDC24V



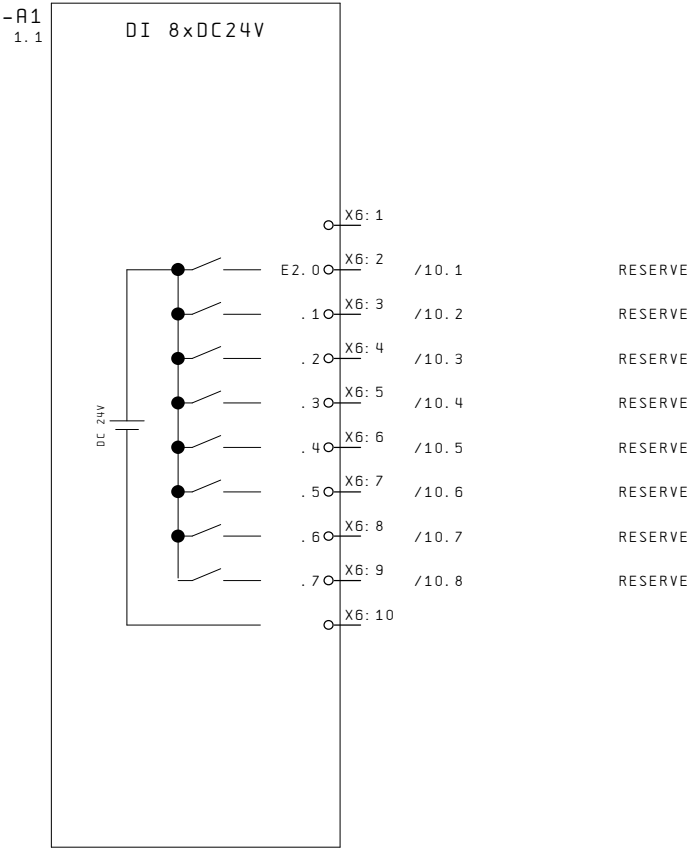
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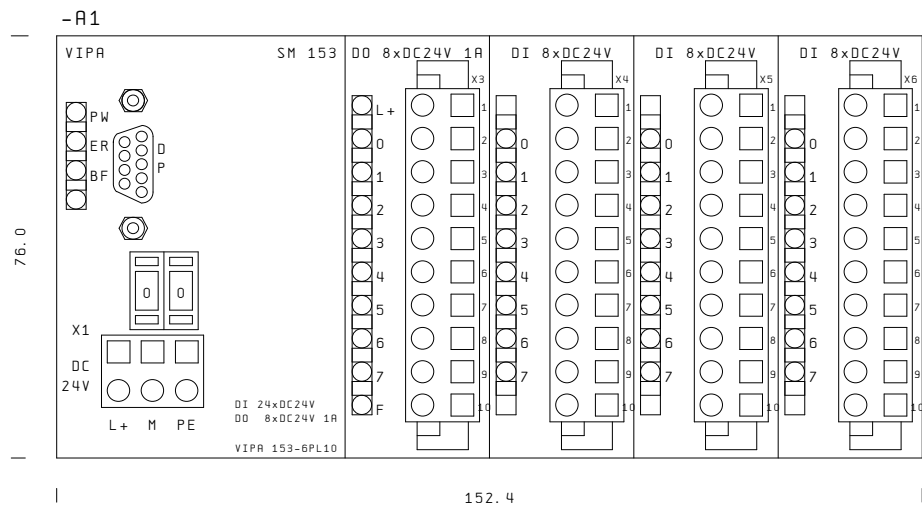
-R1  
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DI 8xDC24V



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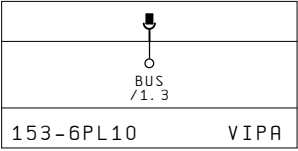


SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48

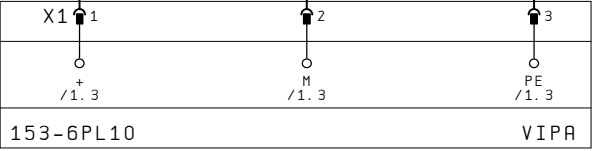


0	1	2	3	4	5	6	7	8	9
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-R1  
1.1

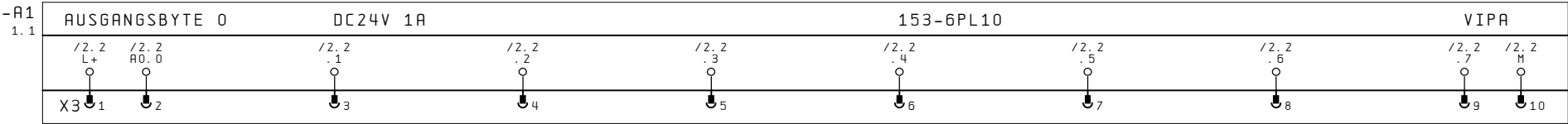


-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 153 DC24V, 153-6PL10	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_6PL10	
			Geänd.								B1. 6	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		10 B1.	

0	1	2	3	4	5	6	7	8	9
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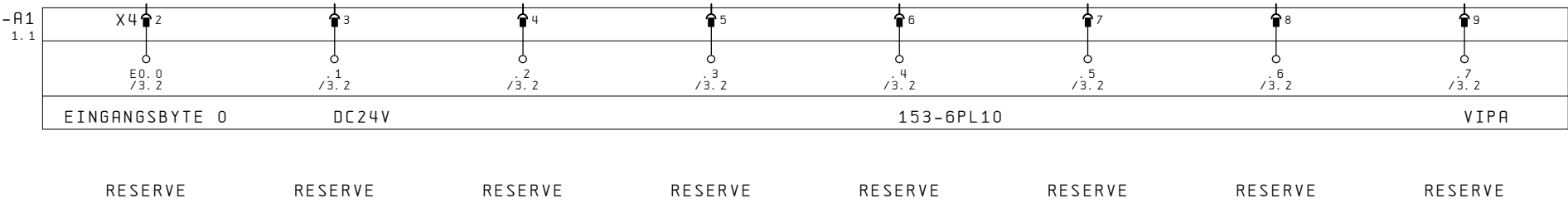
RESERVE

RESERVE

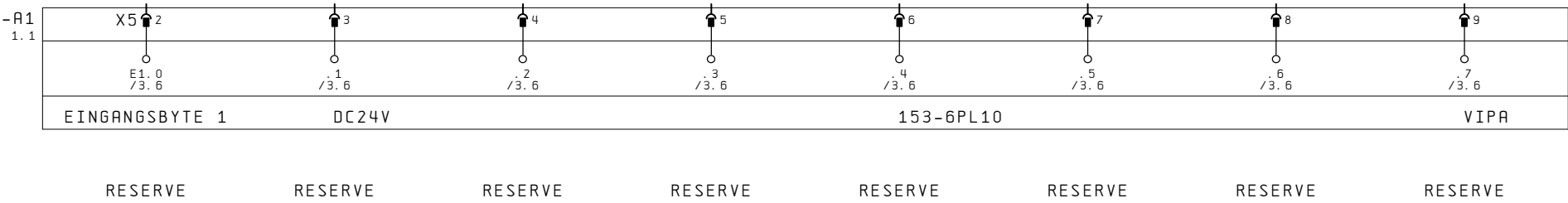
RESERVE

RESERVE

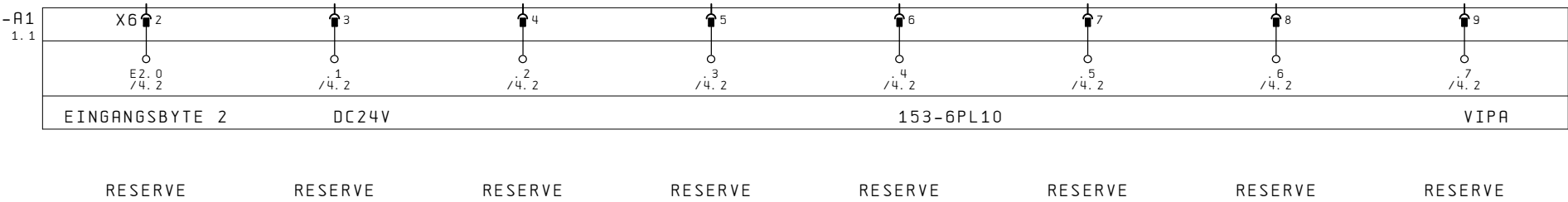
0	1	2	3	4	5	6	7	8	9
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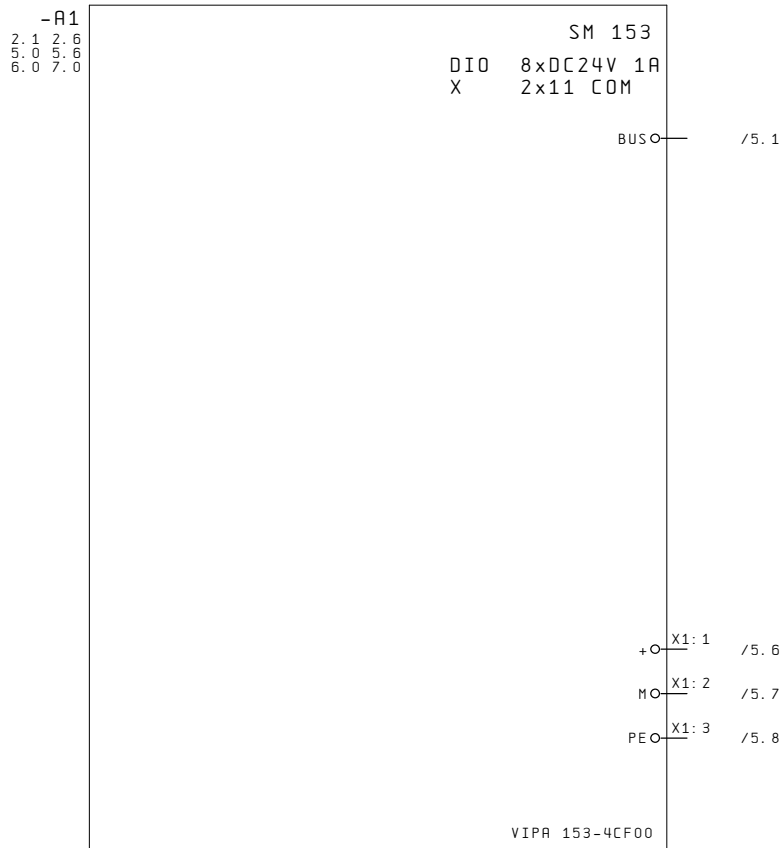
0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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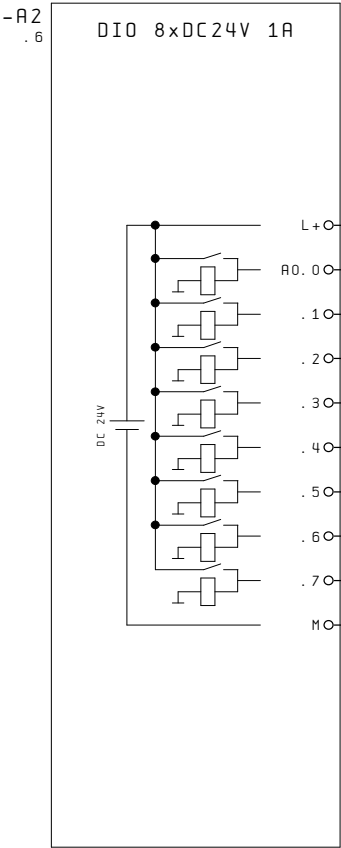
0	1	2	3	4	5	6	7	8	9
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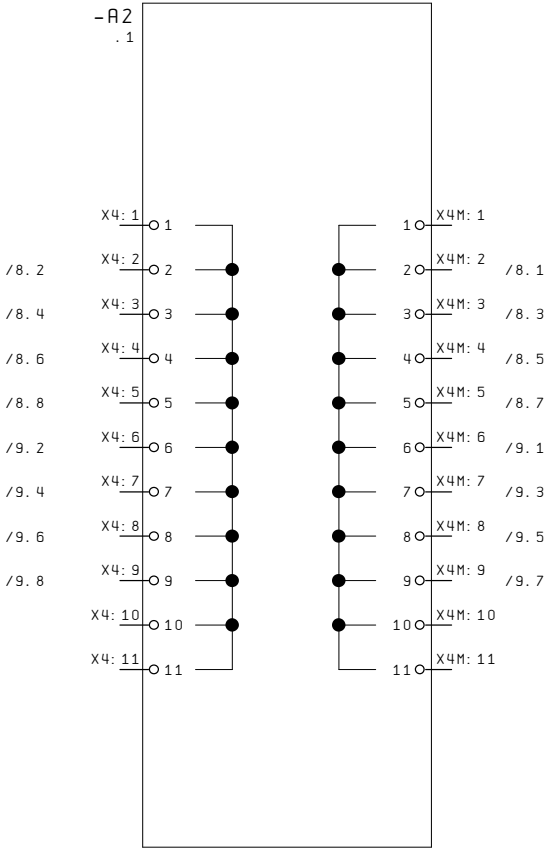
+153_6PL10/10											2		
			Datum	14.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 153 DC24V, 153-4CF00		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+153_4CF00	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	1	
												9 B1.	



Variante 2: 8 Ausgänge

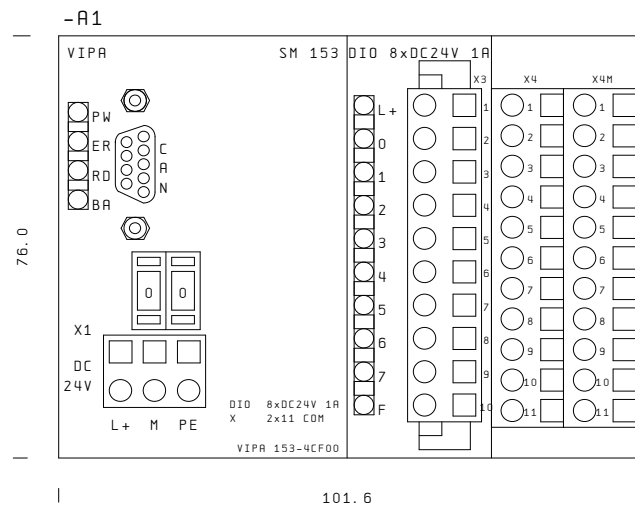


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RESERVE  
RESERVE  
RESERVE



/8. 2  
/8. 4  
/8. 6  
/8. 8  
/9. 2  
/9. 4  
/9. 6  
/9. 8

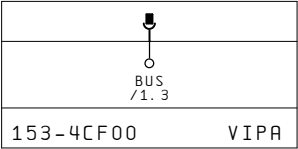




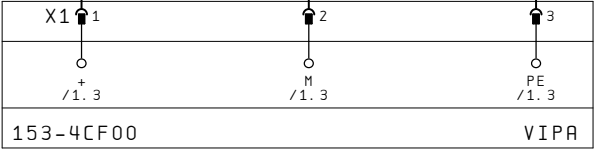
SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
1.1



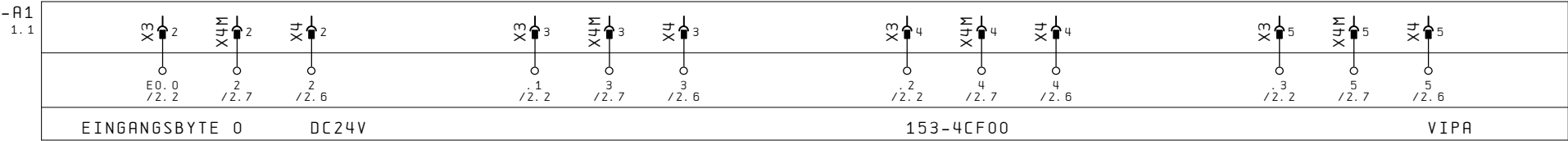
-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 153 DC24V, 153-4CF00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_4CF00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	5
												9 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge



RESERVE

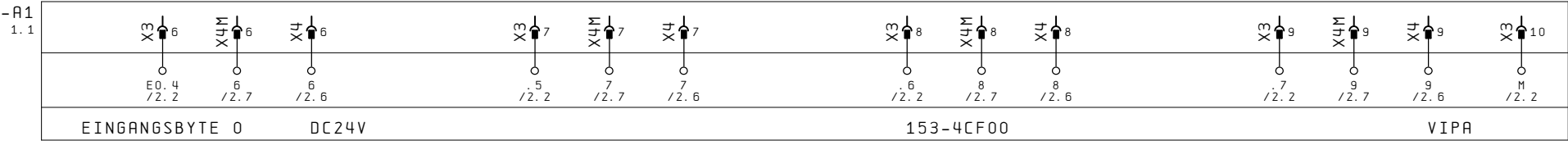
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge



RESERVE

RESERVE

RESERVE

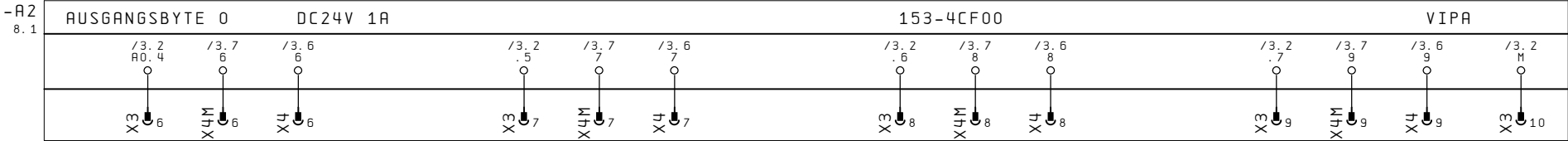
RESERVE

[illegible]

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Ausgänge



RESERVE

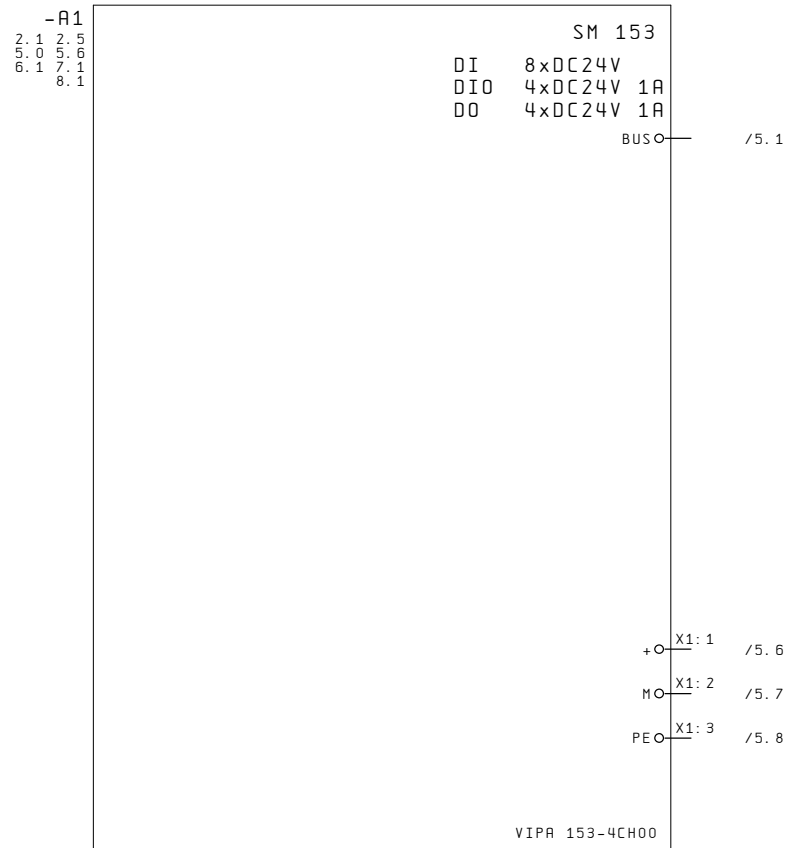
RESERVE

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			Datum	14.07.03	Produktmakros für System 100V				Ausgangsbyte 0, SM 153 DC24V, 153-4CF00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+153_4CF00	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	9	
											9 B1.		

0	1	2	3	4	5	6	7	8	9
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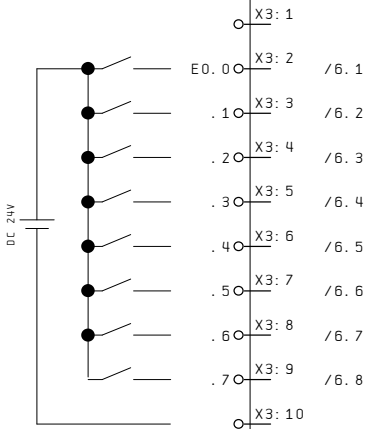


+153_4CF00/9													2
			Datum	14.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 153 DC24V, 153-4CH00		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+153_4CH00	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	1
												10 B1.	

Variante 1: 12 Eingänge und 4 Ausgänge

-A1  
1.1

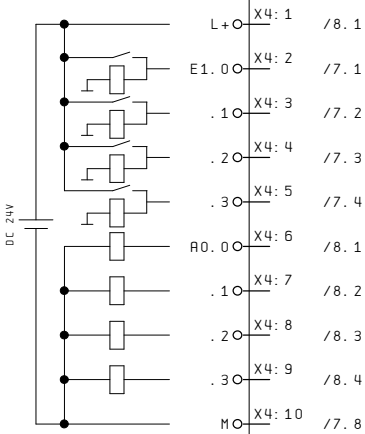
DI 8xDC24V



RESERVE  
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RESERVE  
RESERVE

-A1  
1.1

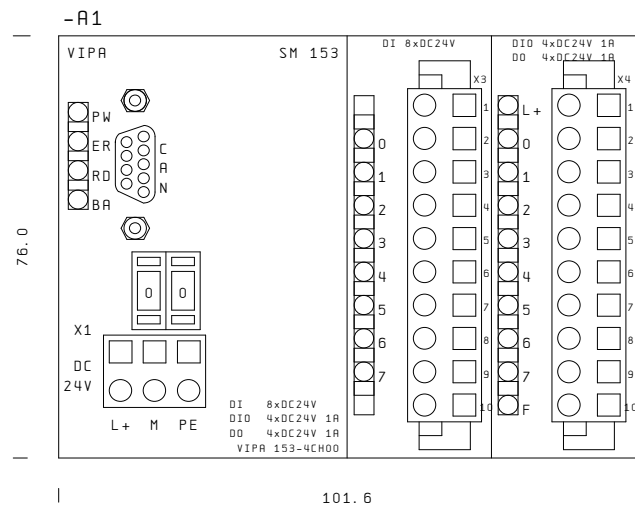
DIO 4xDC24V 1A  
DO 4xDC24V 1A



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RESERVE



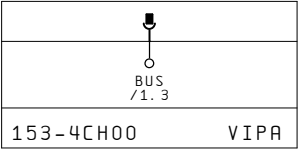




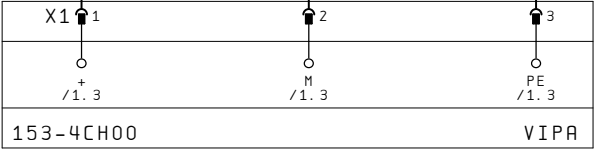
SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
1.1



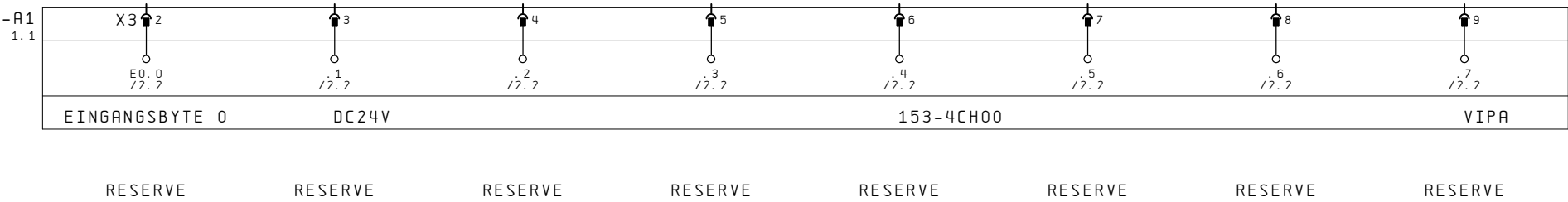
-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 153 DC24V, 153-4CH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_4CH00	
			Geänd.								B1. 5	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		10 B1.	

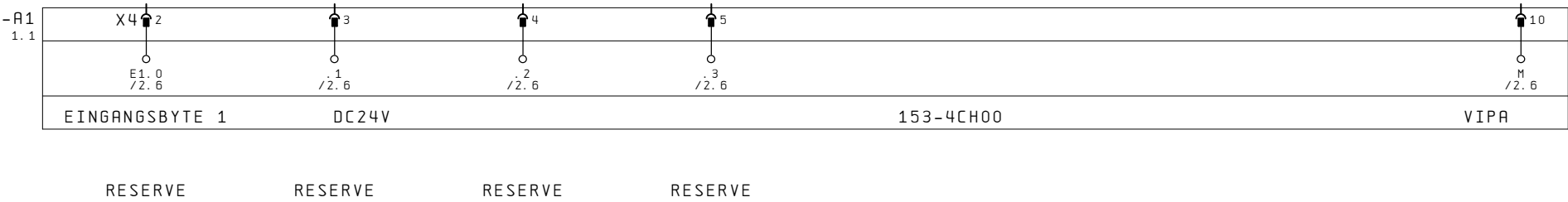
0	1	2	3	4	5	6	7	8	9
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Variante 1: 12 Eingänge und 4 Ausgänge



0	1	2	3	4	5	6	7	8	9
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Variante 1: 12 Eingänge und 4 Ausgänge



0	1	2	3	4	5	6	7	8	9
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Variante 1: 12 Eingänge und 4 Ausgänge

-A1 1.1	AUSGANGSBYTE 0		DC24V 1A		153-4CH00			VIPA	
	/2.6 L+	/2.6 AO.0	/2.6 .1	/2.6 .2	/2.6 .3				
	X4 1	6	7	8	9				

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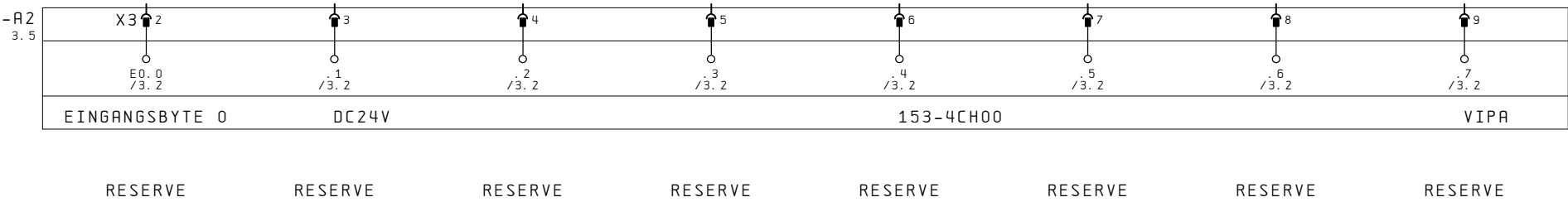
RESERVE

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7													9	
			Datum	07.05.05	Produktmakros für System 100V			Ausgangsbyte 0, SM 153 DC24V, 153-4CH00		VIPA100V		=SYSTEM100V +153_4CH00		
			Bearb.	ZBW										
			Geänd.											
Änderung	Datum	Name	Form		Unspr.	Ers. f.	Ers. d.			System 100V		B1.	8	
													10 B1.	

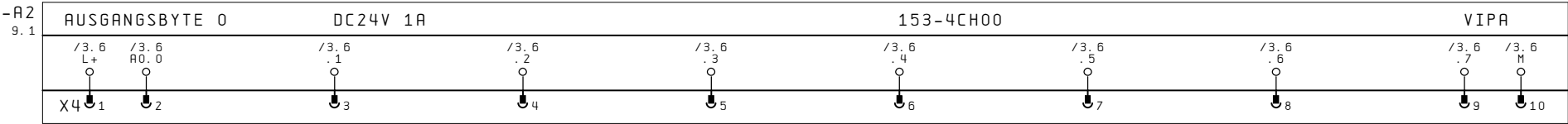
0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge und 8 Ausgänge



			Datum	07.05.05	Produktmakros für System 100V			Eingangsbyte 0, SM 153 DC24V, 153-4CH00	VIPA100V		=SYSTEM100V +153_4CH00		
			Bearb.	ZBW								B1.	9
			Geänd.										10 B1.
Änderung	Datum	Name	Form		Unspr.	Ers. f.	Ers. d.			System 100V			

0	1	2	3	4	5	6	7	8	9
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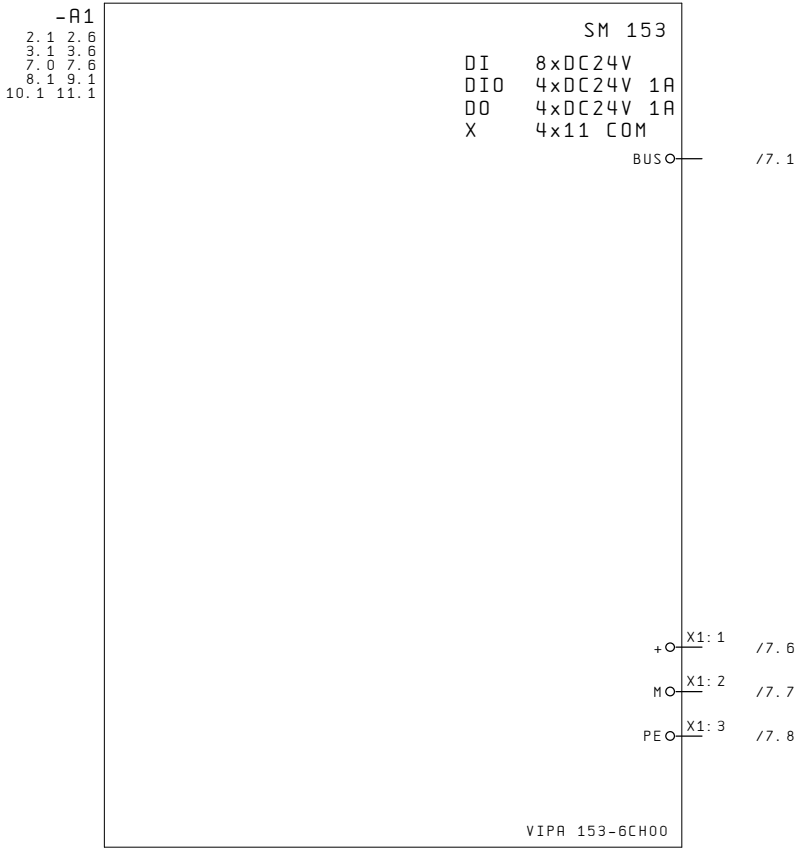
RESERVE

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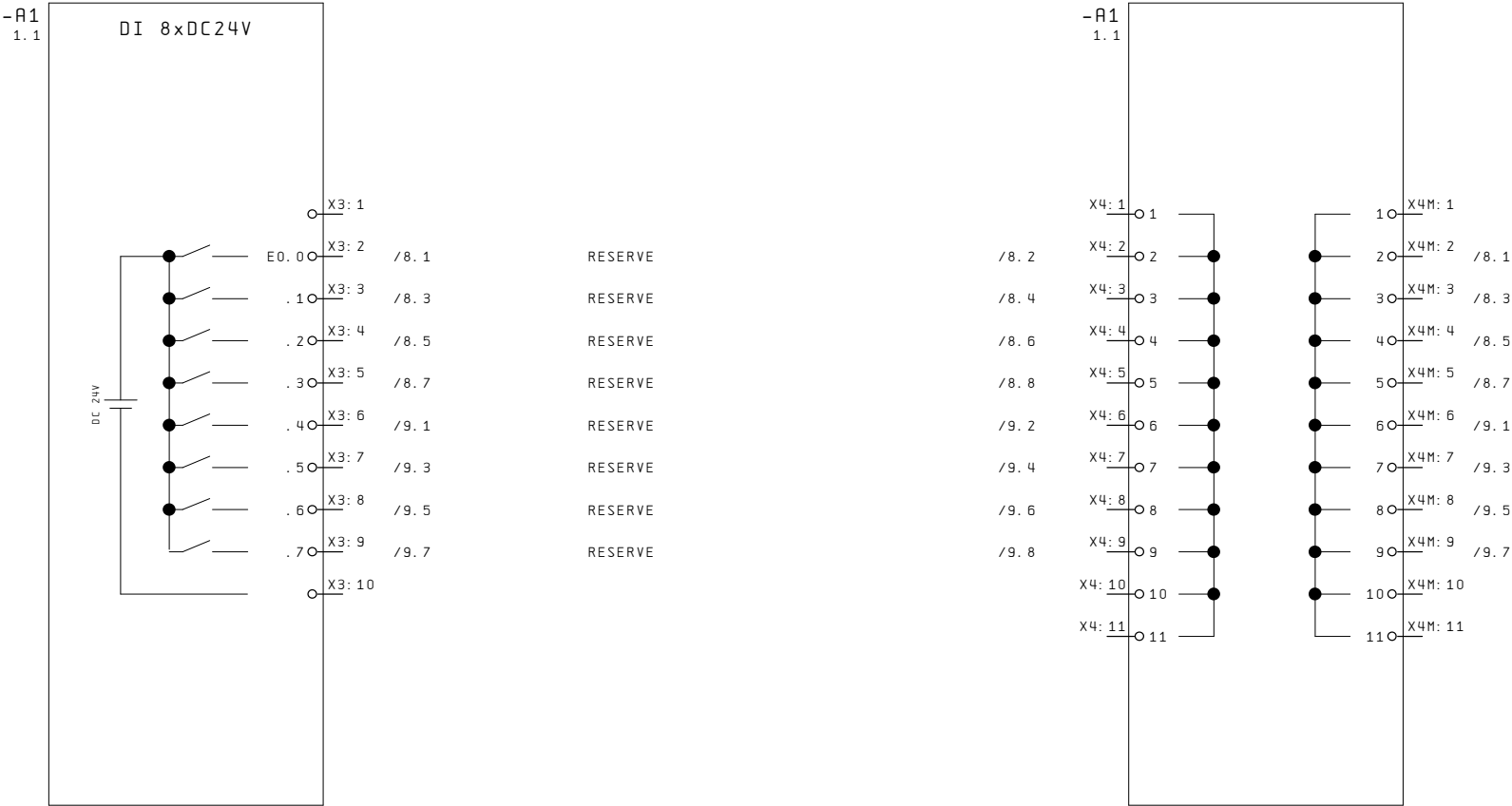


0	1	2	3	4	5	6	7	8	9
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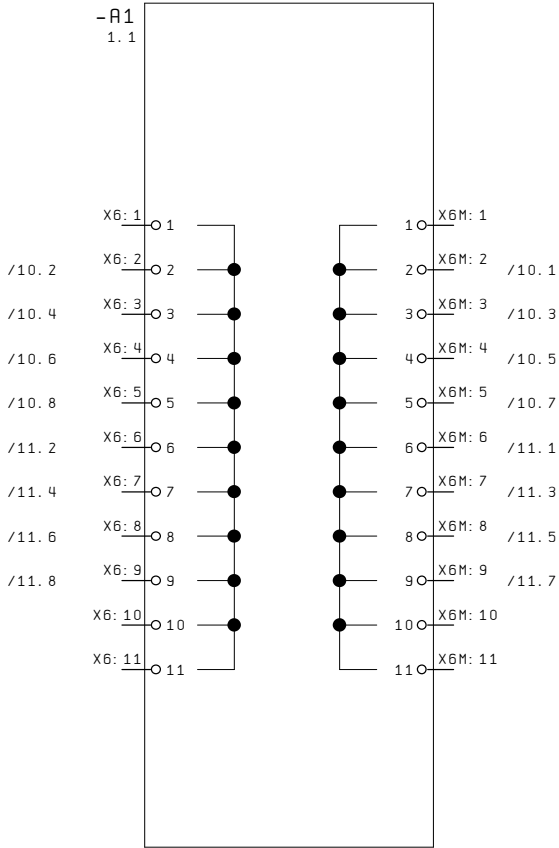
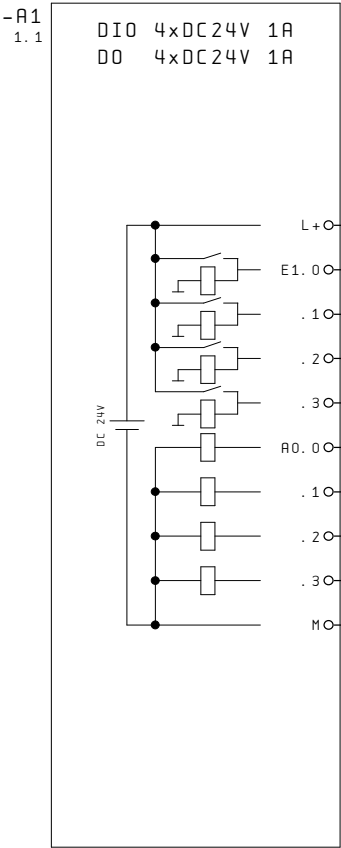


+153_4CH00/10													2
			Datum	14.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 153 DC24V, 153-6CH00		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+153_6CH00	
			Geänd.									B1. 1	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		15 Bl.	

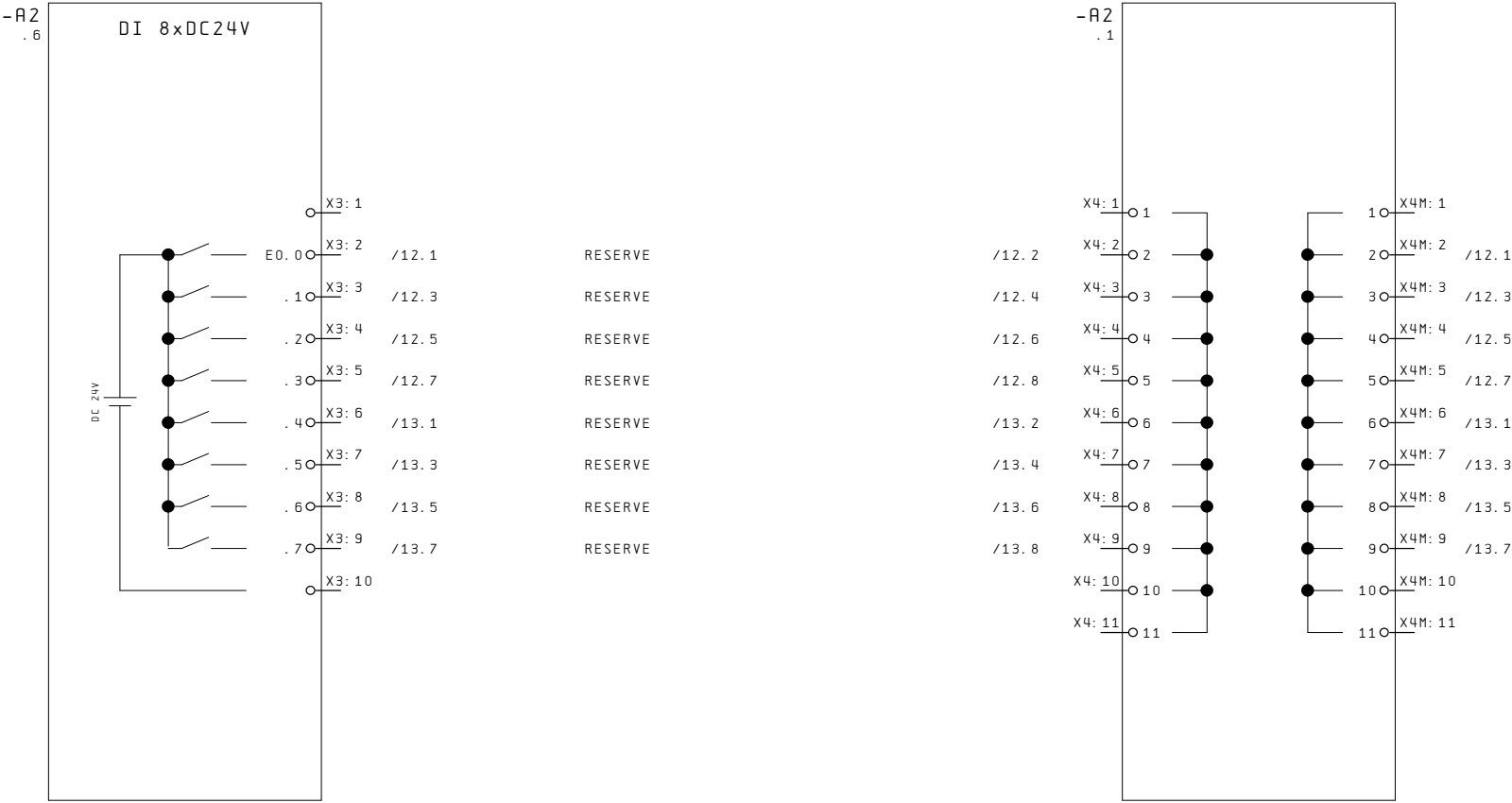
Variante 1: 12 Eingänge und 4 Ausgänge



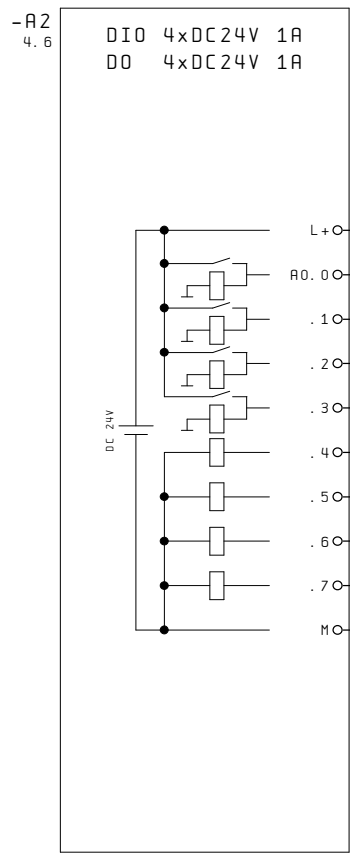
Variante 1: 12 Eingänge und 4 Ausgänge



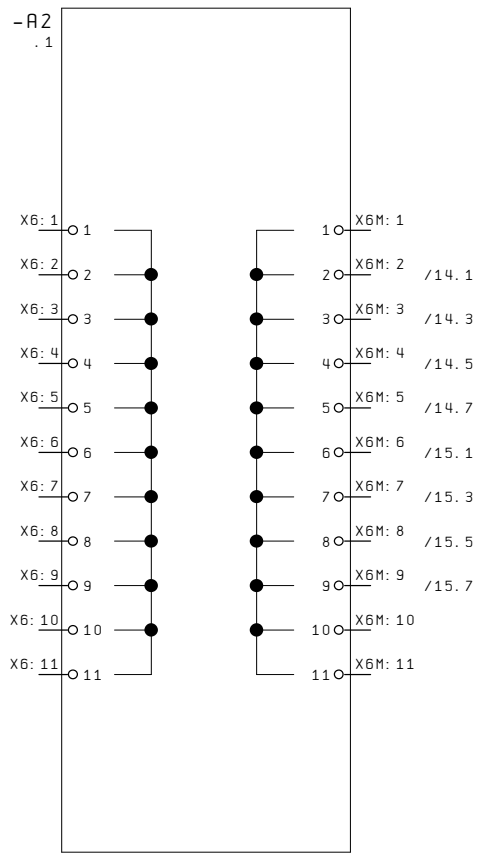
Variante 2: 8 Eingänge und 8 Ausgänge



Variante 1: 8 Eingänge und 8 Ausgänge



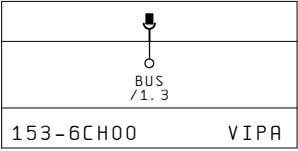
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RESERVE



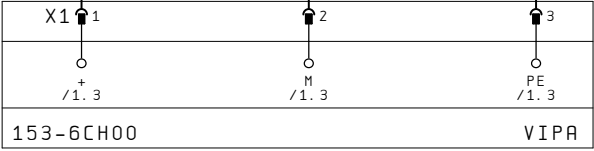


0	1	2	3	4	5	6	7	8	9
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-R1  
1.1



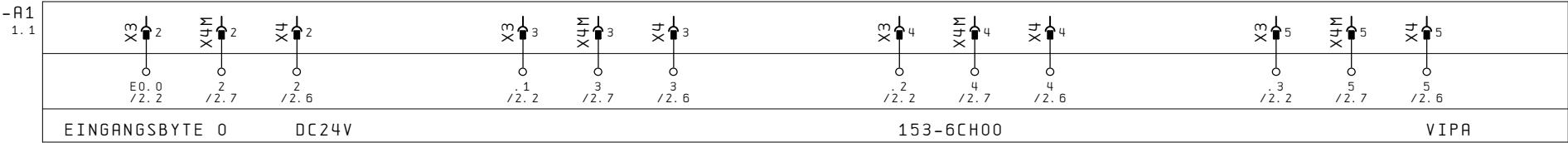
-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 153 DC24V, 153-6CH00	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+153_6CH00	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	7
											15 B1.	

0	1	2	3	4	5	6	7	8	9
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Variante 1: 12 Eingänge und 4 Ausgänge



RESERVE

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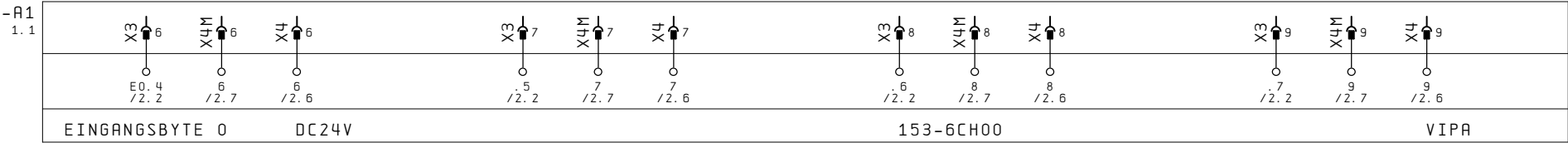
RESERVE

RESERVE



0	1	2	3	4	5	6	7	8	9
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Variante 1: 12 Eingänge und 4 Ausgänge



RESERVE

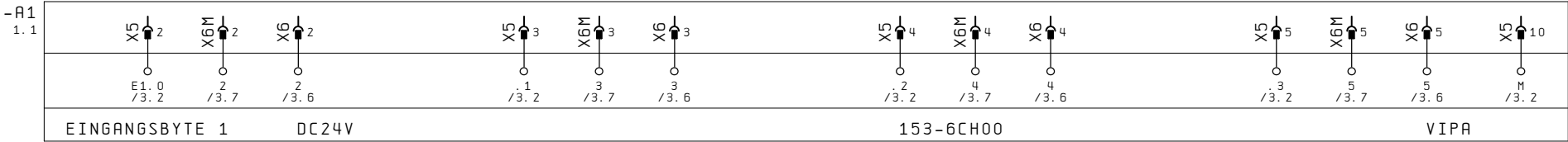
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 12 Eingänge und 4 Ausgänge



RESERVE

RESERVE

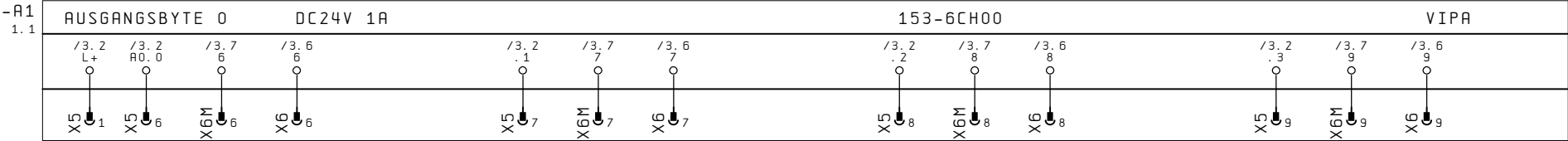
RESERVE

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9											11			
			Datum	14.07.03	Produktmakros für System 100V			Eingangsbyte 1, SM 153 DC24V, 153-6CH00		VIPA100V		=SYSTEM100V		
			Bearb.	ZBW								+153_6CH00		
			Geänd.											
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	10		
													15 B1.	

0	1	2	3	4	5	6	7	8	9
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Variante 1: 12 Eingänge und 4 Ausgänge



RESERVE

RESERVE

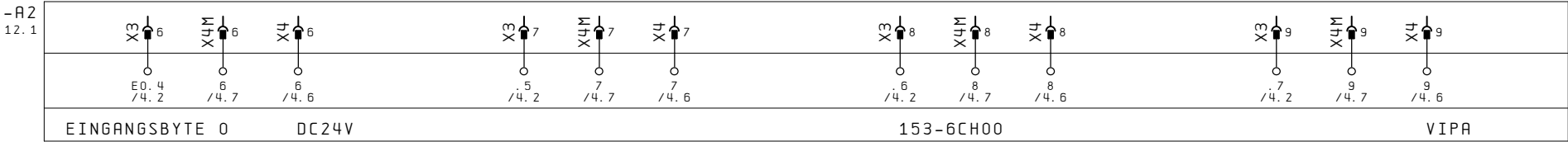
RESERVE

RESERVE



0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Eingänge und 8 Ausgänge



RESERVE

RESERVE

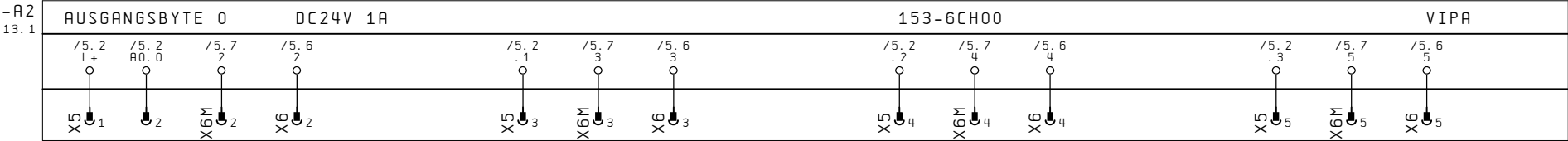
RESERVE

RESERVE

			Datum	07.05.05	Produktmakros für System 100V		<div>VIPA<sup>®</sup> art of automation</div>	Eingangsbyte 0, SM 153 DC24V, 153-6CH00	VIPA100V		=SYSTEM100V +153_6CH00		
			Bearb.	ZBW								B1.	13
			Geänd.										15 B1.
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V			

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Eingänge und 8 Ausgänge



RESERVE

RESERVE

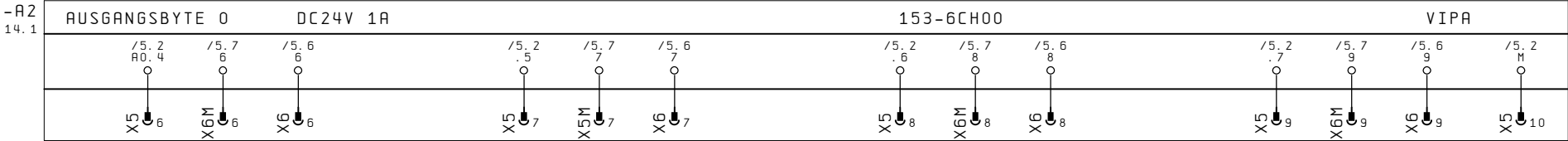
RESERVE

RESERVE

			Datum	14.07.03	Produktmakros für System 100V		Ausgangsbyte 0, SM 153 DC24V, 153-6CH00	VIPA100V		=SYSTEM100V +153_6CH00	
			Bearb.	ZBW							
			Geänd.								
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1. 14 15 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Eingänge und 8 Ausgänge



RESERVE

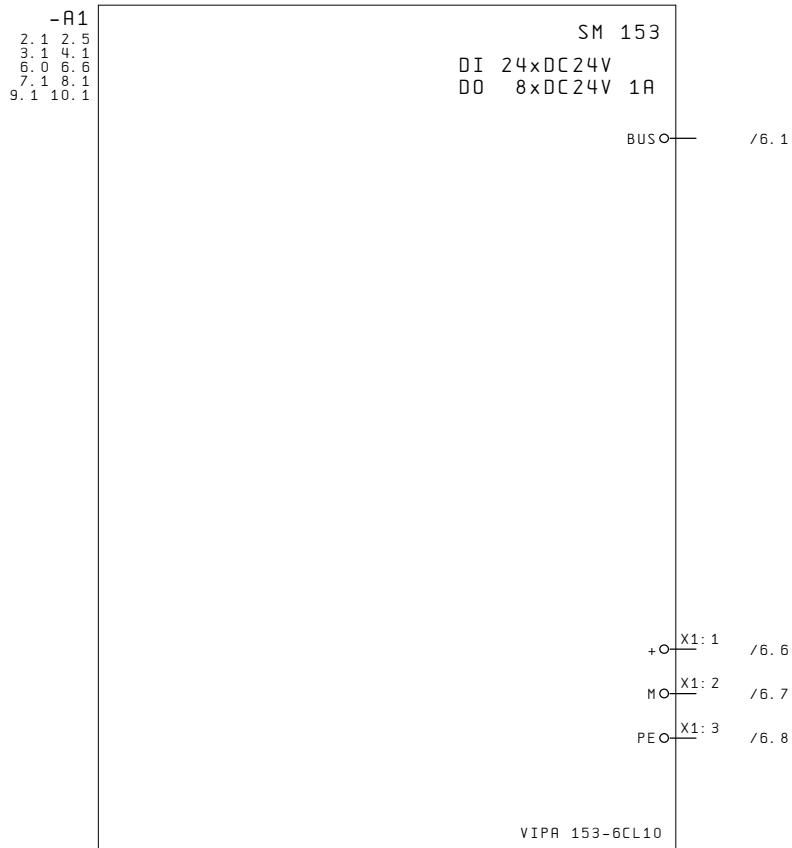
RESERVE

RESERVE

RESERVE

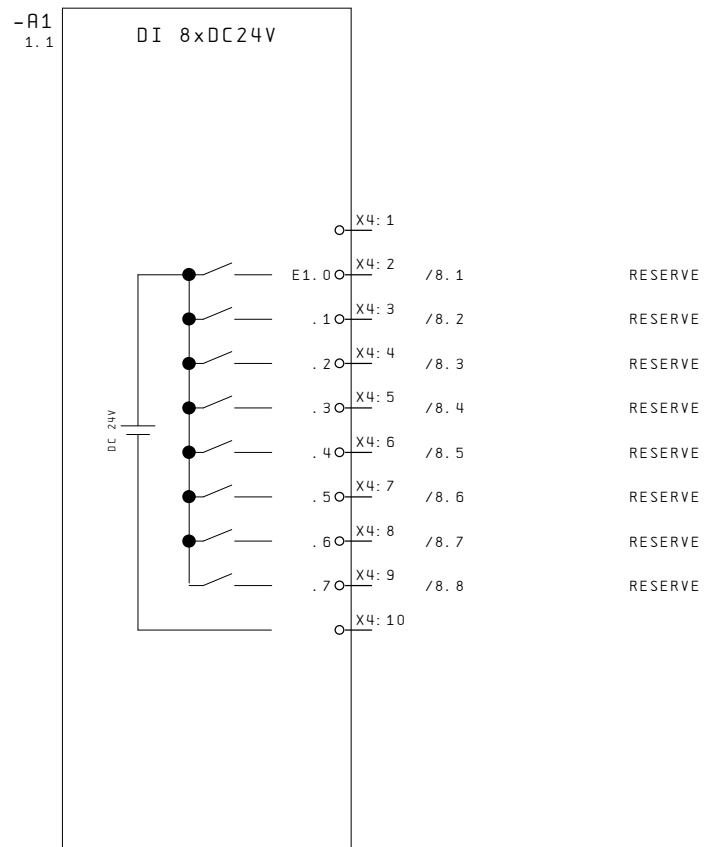
			Datum	14.07.03	Produktmakros für System 100V		Ausgangsbyte 0, SM 153 DC24V, 153-6CH00	VIPA100V		=SYSTEM100V +153_6CH00	
			Bearb.	ZBW							
			Geänd.								
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V	B1. 15 15 B1.

0	1	2	3	4	5	6	7	8	9
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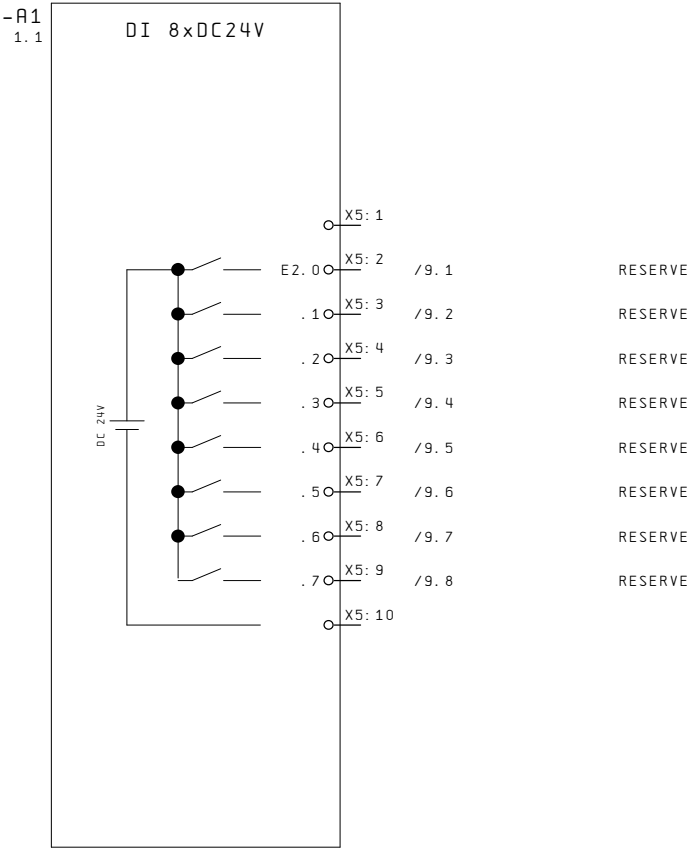


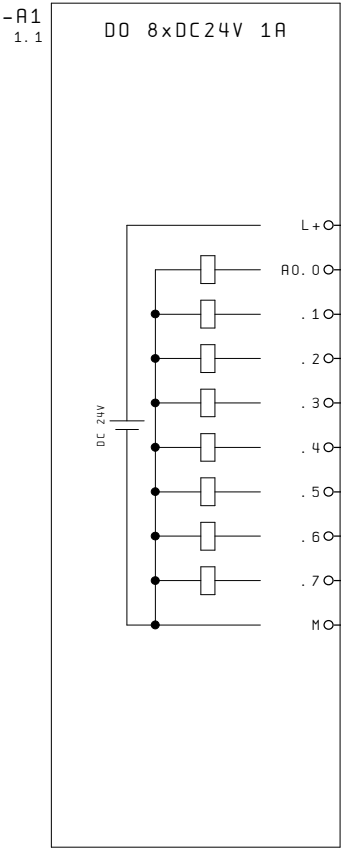
+153_6CH00/15											2		
			Datum	14.07.03	Produktmakros für System 100V			SPS-Übersicht Versorgung,		VIPA100V	=SYSTEM100V		
			Bearb.	ZBW				SM 153 DC24V,				+153_6CL10	
			Geänd.					153-6CL10				B1. 1	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		10 B1.		



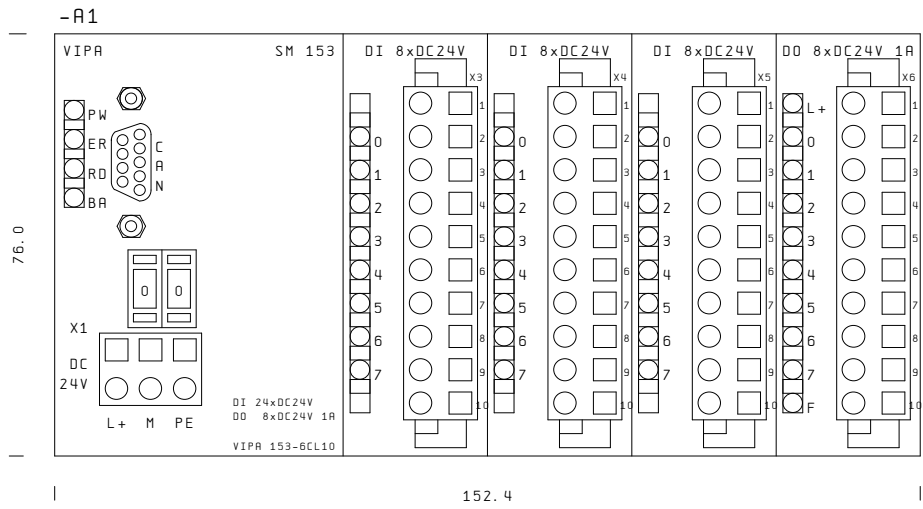


Änderung	Datum	Name	Form	Urspr.	Ers. f.	Ers. d.	155-00110	system 100V	10 B1
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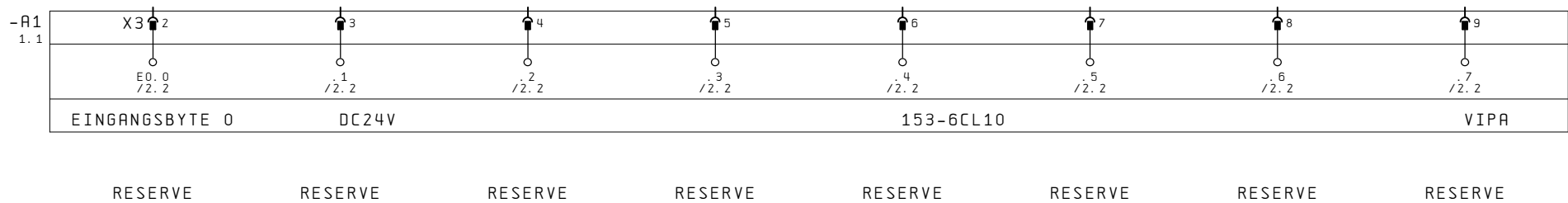
RESERVE  
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RESERVE



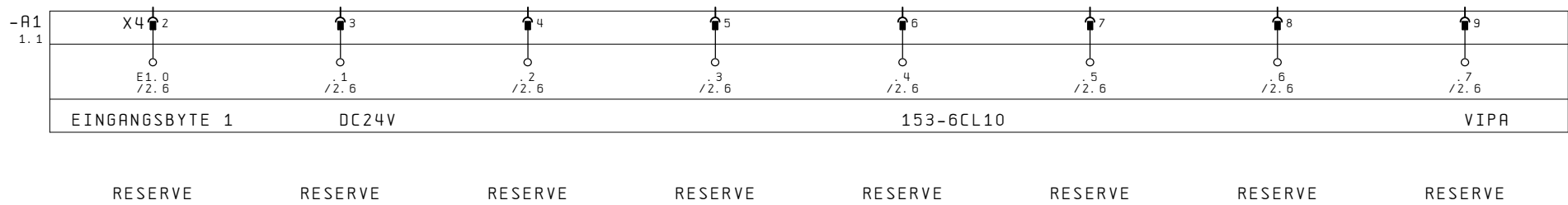
SM 153  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 152,4 x 76 x 48



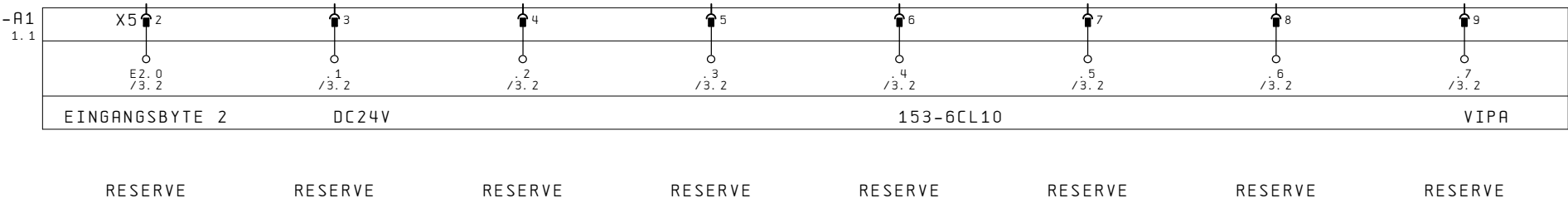
0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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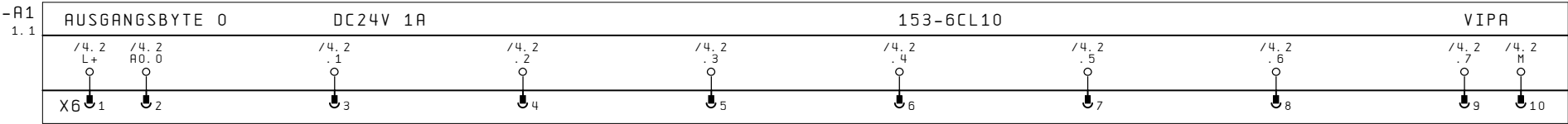


0	1	2	3	4	5	6	7	8	9
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0	1	2	3	4	5	6	7	8	9
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RESERVE

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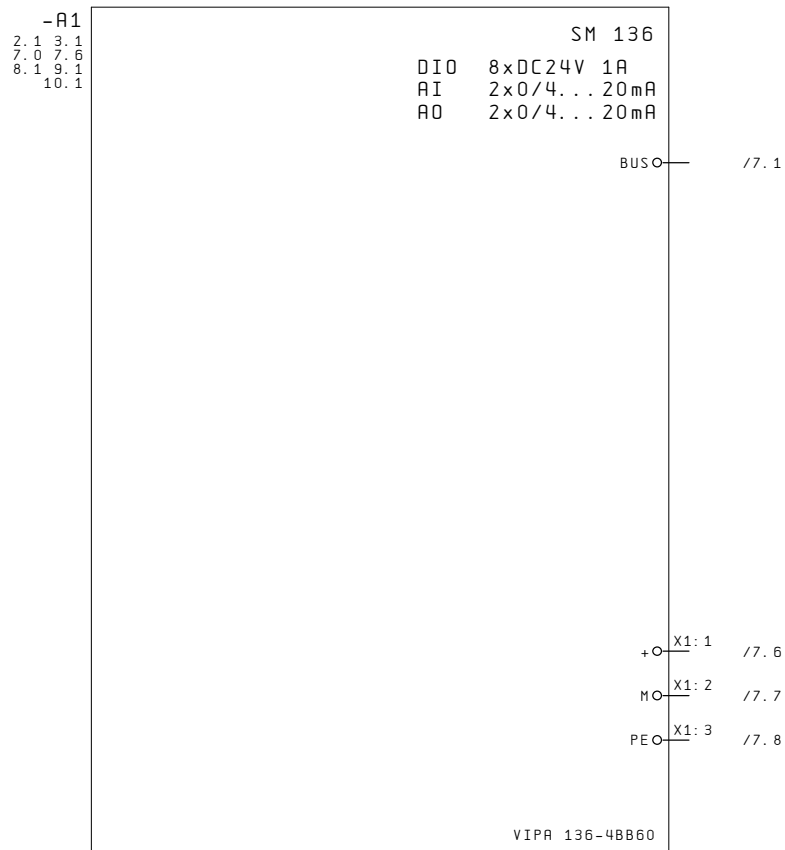
RESERVE

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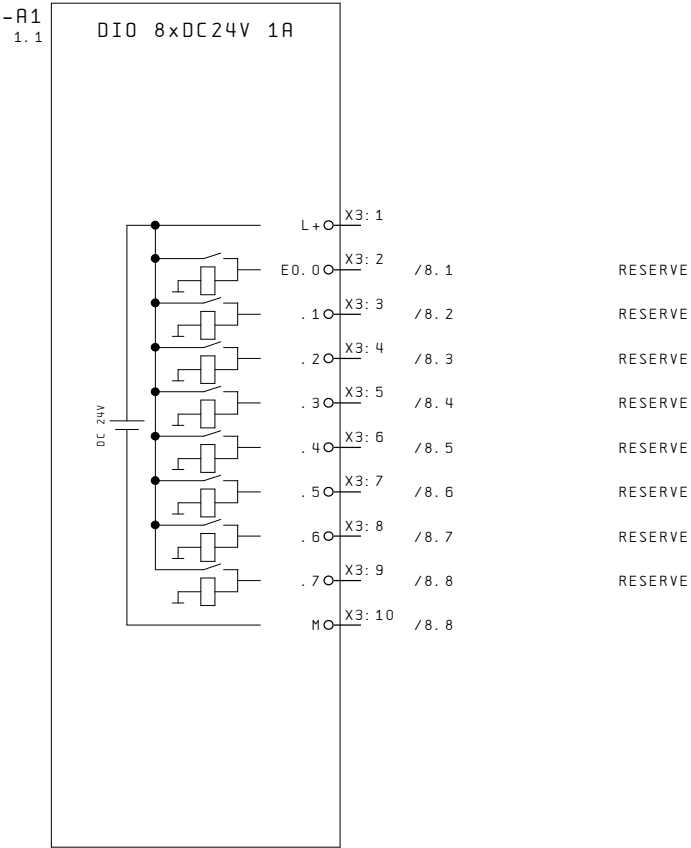
RESERVE

0	1	2	3	4	5	6	7	8	9
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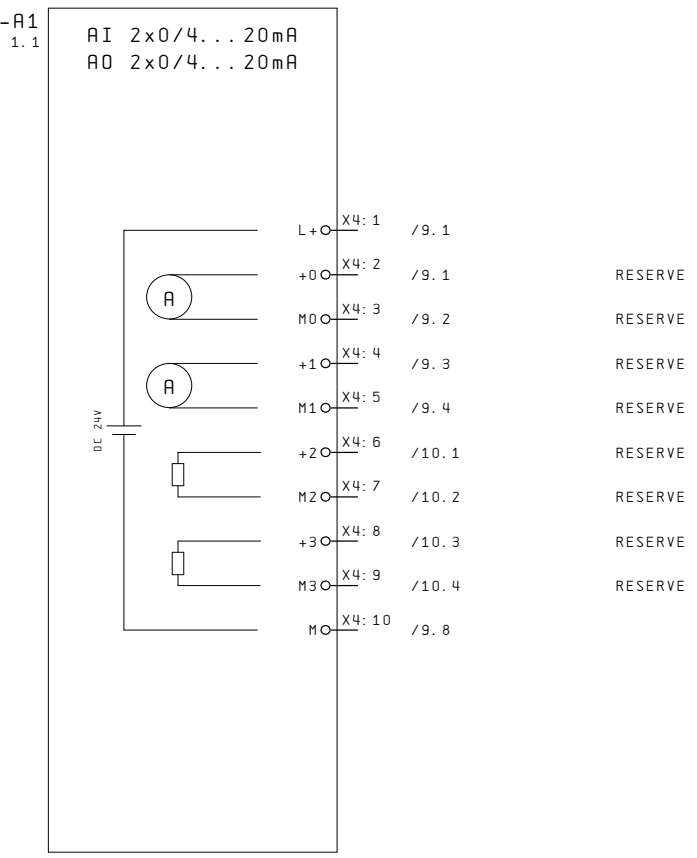


+153_6CL10/10												2	
			Datum	28.04.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 136 DC24V, 136-4BB60		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW						+136_4BB60			
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	1	
											13 B1.		

Variante 1: 8 Eingänge digital, 2 Eingänge analog und 2 Ausgänge analog

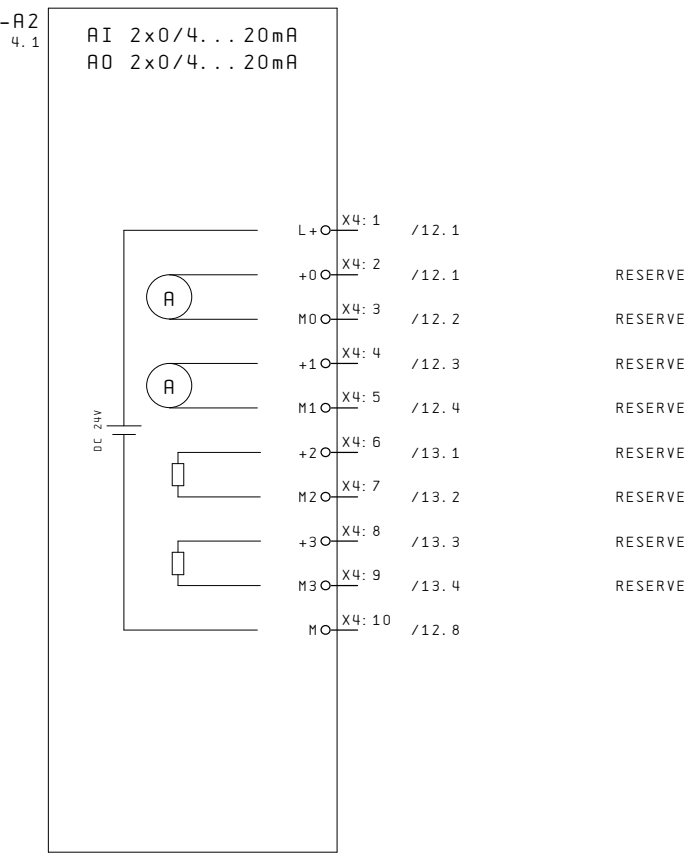


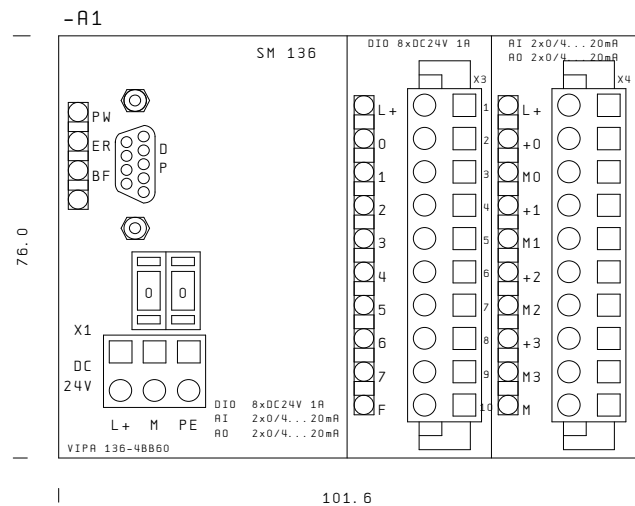
Variante 1: 8 Eingänge digital, 2 Eingänge analog und 2 Ausgänge analog





Variante 2: 8 Ausgänge digital, 2 Eingänge analog und 2 Ausgänge analog

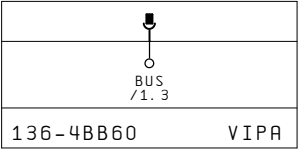




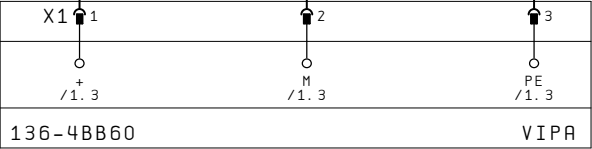
SM 136  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
1.1



-R1  
1.1

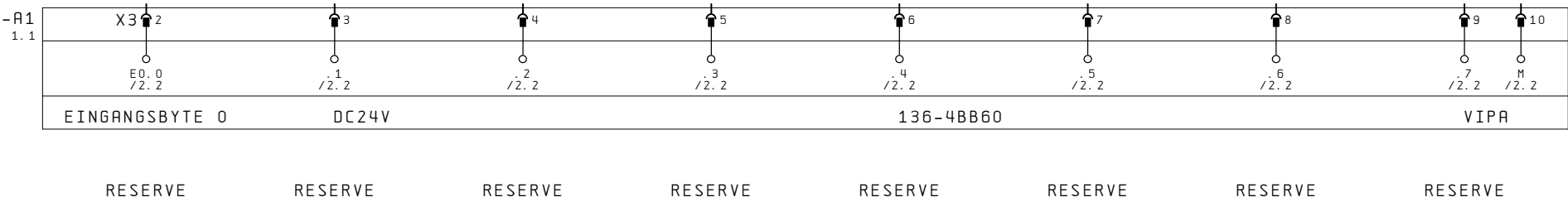


			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 136 DC24V, 136-4BB60	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+136_4BB60	
			Geänd.								B1. 7	
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		13 B1.	



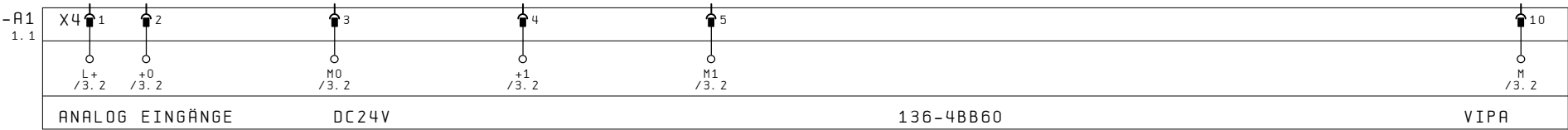
0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge digital, 2 Eingänge analog und 2 Ausgänge analog



0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge digital, 2 Eingänge analog und 2 Ausgänge analog



RESERVE                  RESERVE                  RESERVE                  RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge digital, 2 Eingänge analog und 2 Ausgänge analog

-A1 1.1	ANALOG	AUSGÄNGE	DC24V	136-4BB60	VIPA
	/3...2 +2 ○	/3...2 M2 ○	/3...2 +3 ○	/3...2 M3 ○	
	X4 6	7	8	9	

RESERVE

RESERVE

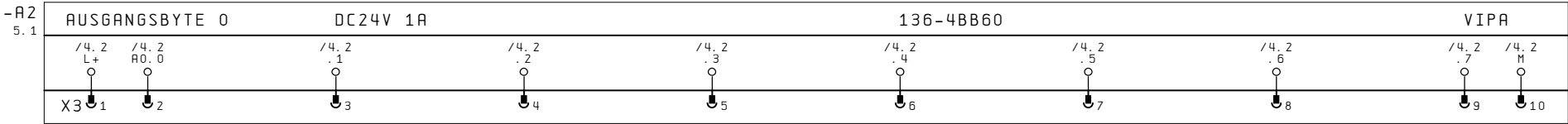
RESERVE

RESERVE

			Datum	14.07.03	Produktmakros für System 100V		Analog Ausgänge, SM 136 DC24V, 136-4BB60	VIPA100V		=SYSTEM100V
			Bearb.	ZBW						+136_4BB60
			Geänd.							
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.	System 100V		B1. 10 13 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Ausgänge digital, 2 Eingänge analog und 2 Ausgänge analog



RESERVE

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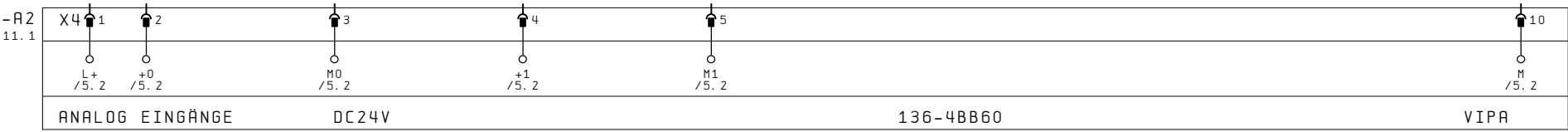
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Ausgänge digital, 2 Eingänge analog und 2 Ausgänge analog



RESERVE                  RESERVE                  RESERVE                  RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Ausgänge digital, 2 Eingänge analog und 2 Ausgänge analog

-A2 12.1	ANALOG	AUSGÄNGE	DC24V	136-4BB60	VIPA
	/5.2 +2 O	/5.2 M2 O	/5.2 +3 O	/5.2 M3 O	
	X4 6	7	8	9	

RESERVE

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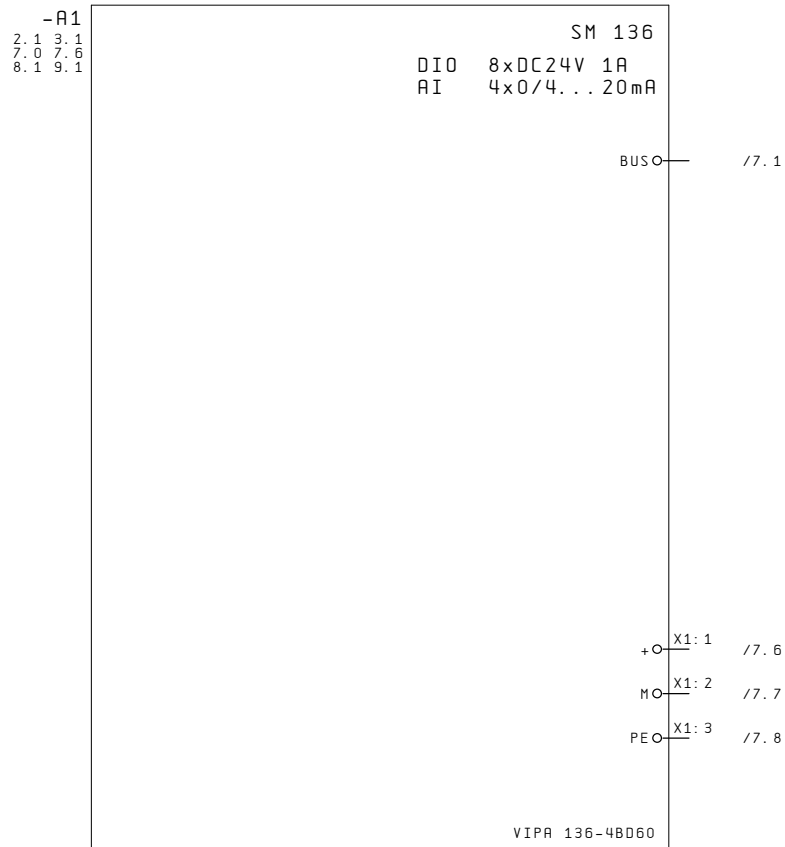
RESERVE

12

+136\_4BD60/1

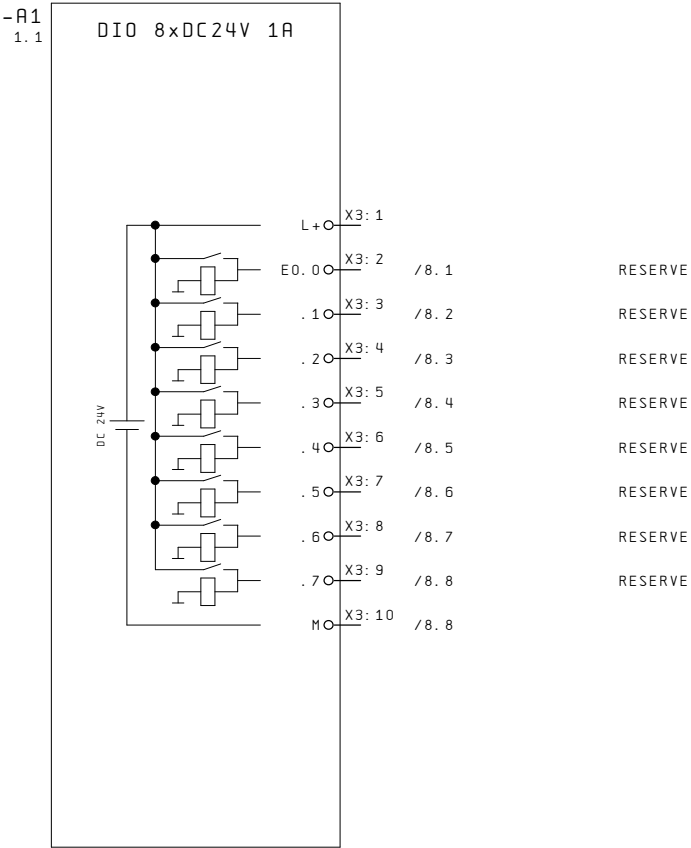
			Datum	14.07.03	Produktmakros für System 100V			Analog Ausgänge, SM 136 DC24V, 136-4BB60	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+136_4BB60	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	13
												13 B1.

0	1	2	3	4	5	6	7	8	9
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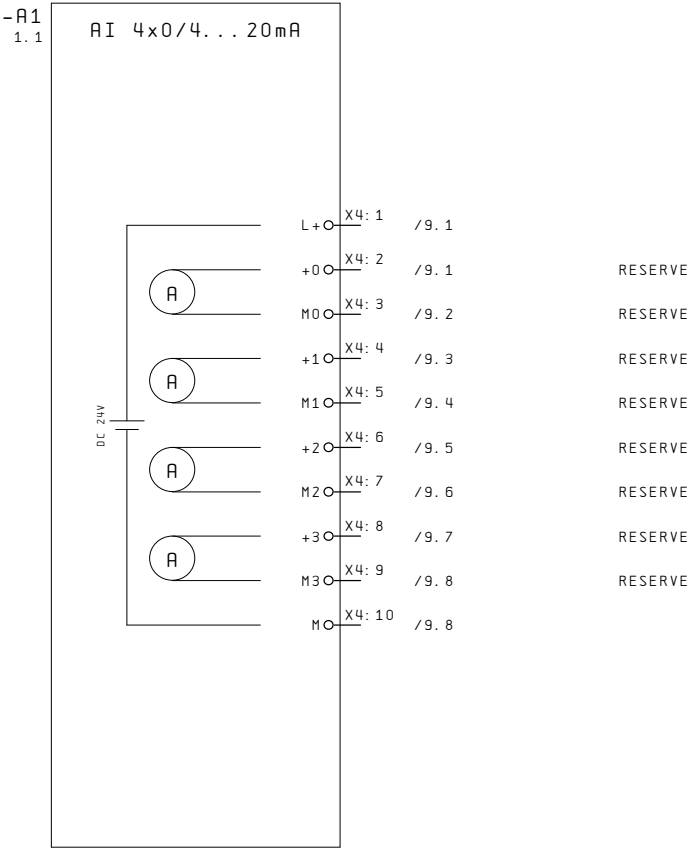
+136_4BB60/13													2
			Datum	28.04.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 136 DC24V, 136-4BD60		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+136_4BD60	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1.	1
												11 B1.	

Variante 1: 8 Eingänge digital, 4 Eingänge analog

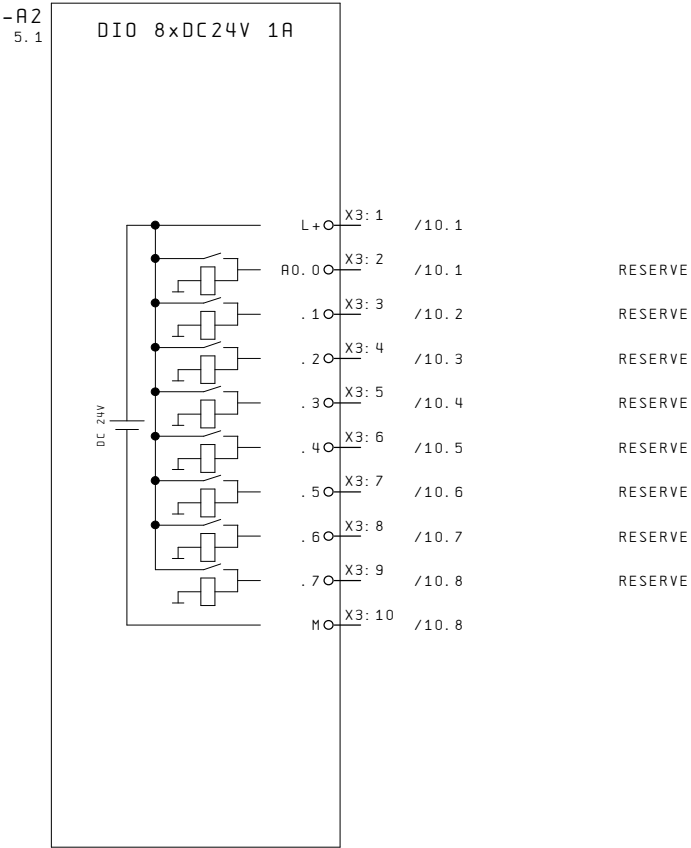




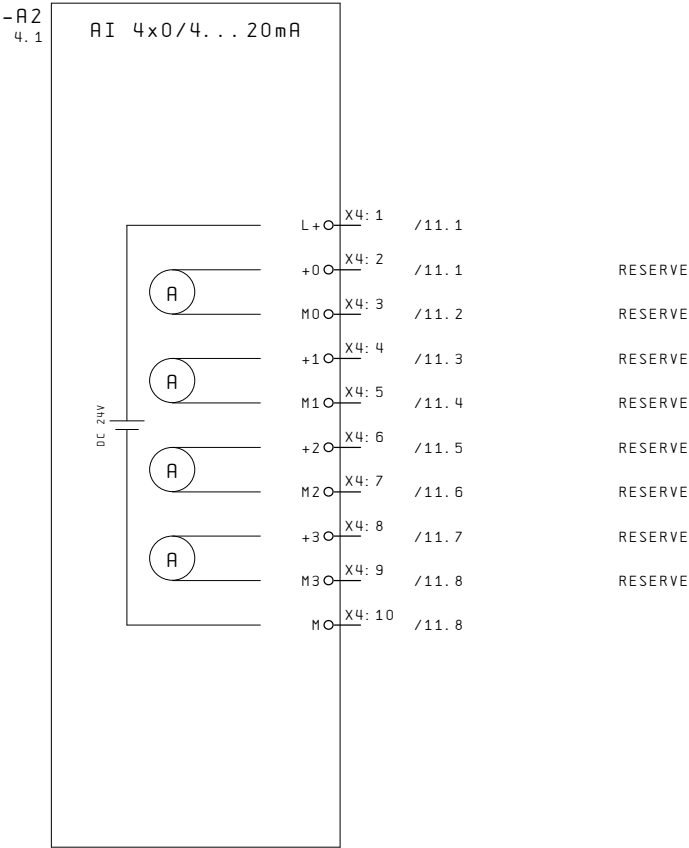
Variante 1: 8 Eingänge digital, 4 Eingänge analog

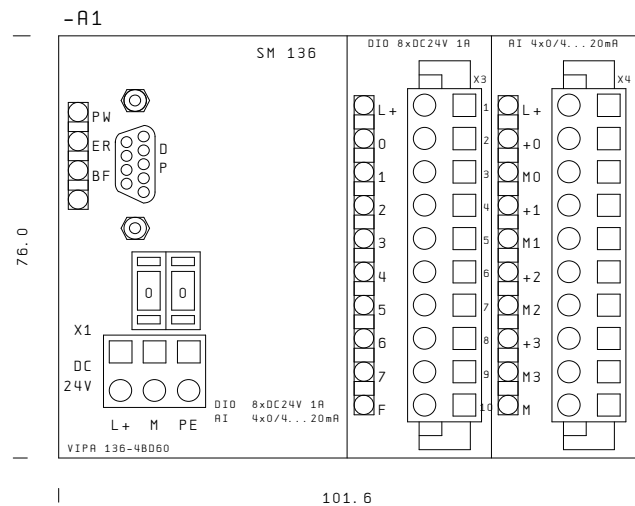


Variante 2: 8 Ausgänge digital, 4 Eingänge analog



Variante 2: 8 Ausgänge digital, 4 Eingänge analog

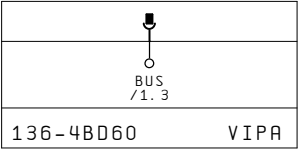




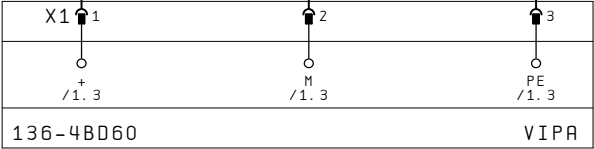
SM 136  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
1.1



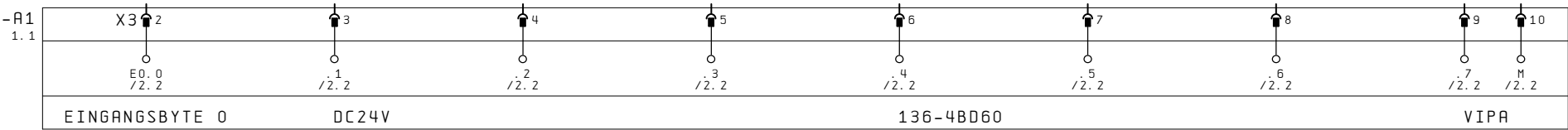
-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 136 DC24V, 136-4BD60	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW					+136_4BD60			
			Geänd.								B1.	7
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		11 B1.	

0	1	2	3	4	5	6	7	8	9
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Variante 1: 8 Eingänge digital, 4 Eingänge analog



RESERVE

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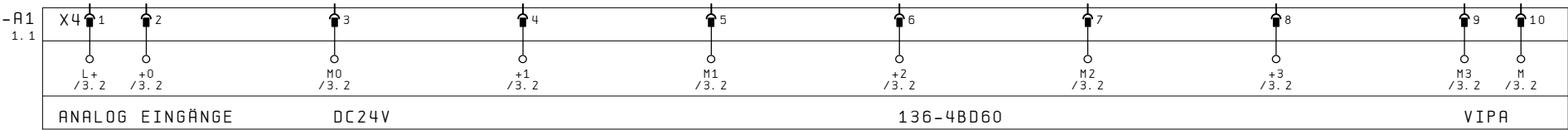
RESERVE

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0	1	2	3	4	5	6	7	8	9
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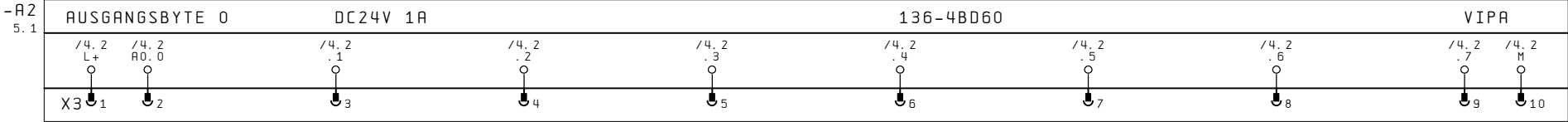
Variante 1: 8 Eingänge digital, 4 Eingänge analog



RESERVE                      RESERVE                      RESERVE                      RESERVE                      RESERVE                      RESERVE                      RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Ausgänge digital, 4 Eingänge analog



RESERVE

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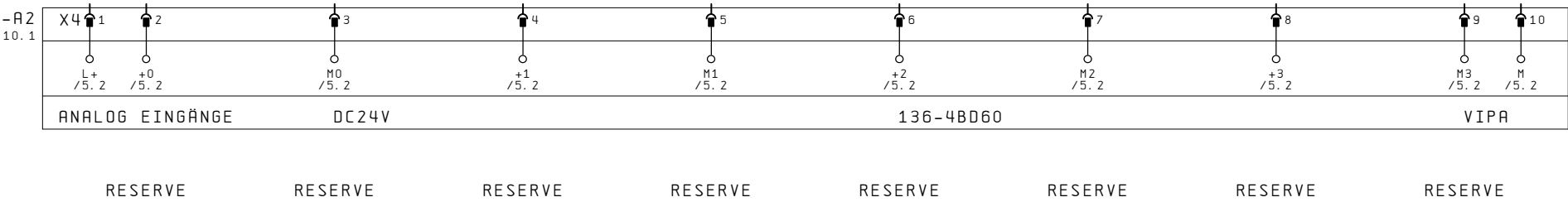
RESERVE

RESERVE



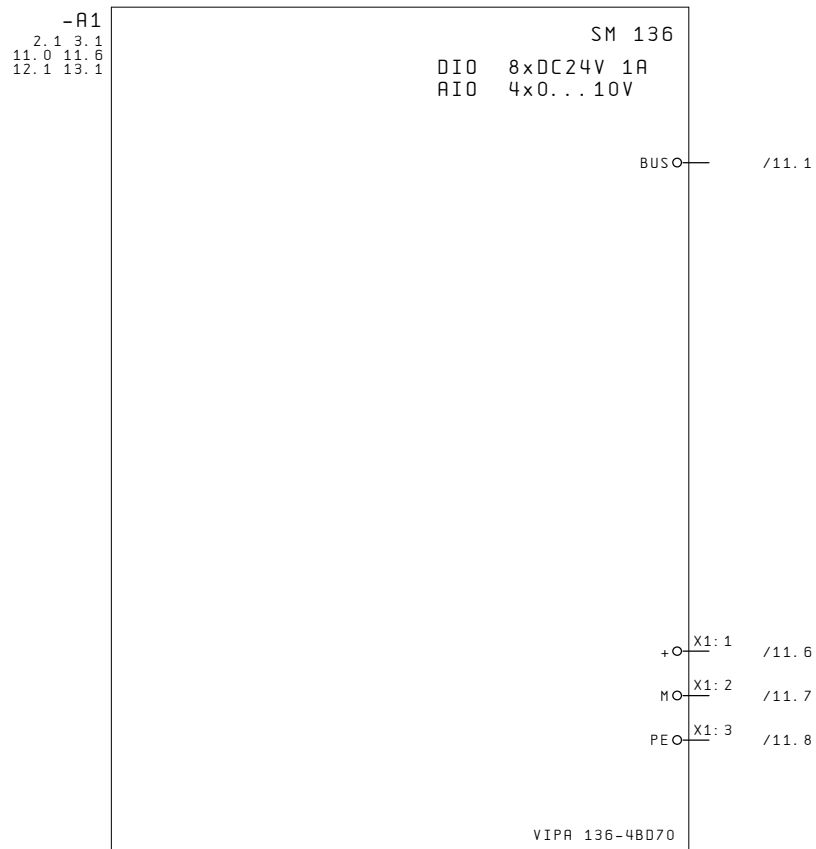
0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Ausgänge digital, 4 Eingänge analog



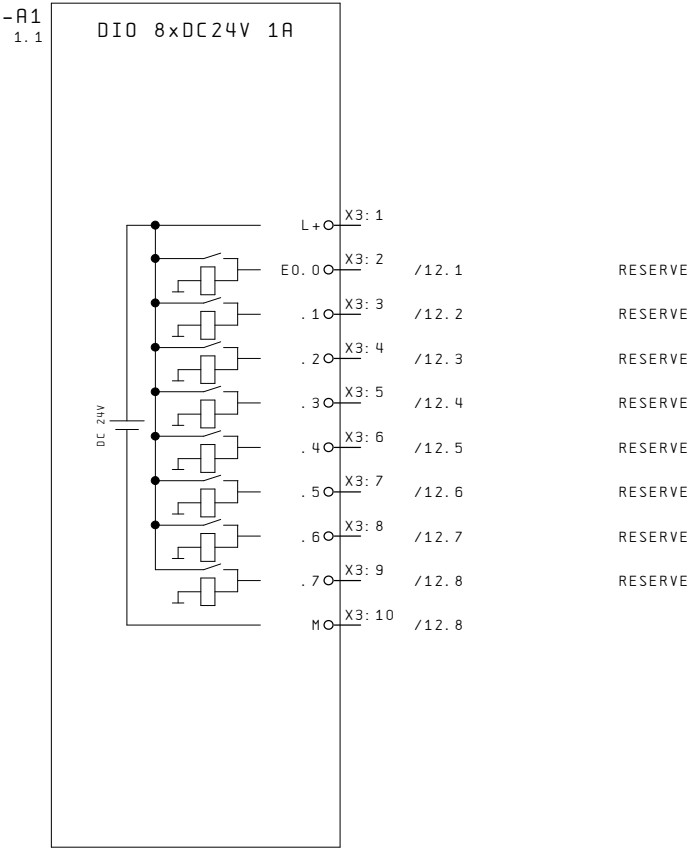
			Datum	14.07.03	Produktmakros für System 100V			Analog Eingänge, SM 136 DC24V, 136-4BD60	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+136_4BD60	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1. 11
											11 B1.	

0	1	2	3	4	5	6	7	8	9
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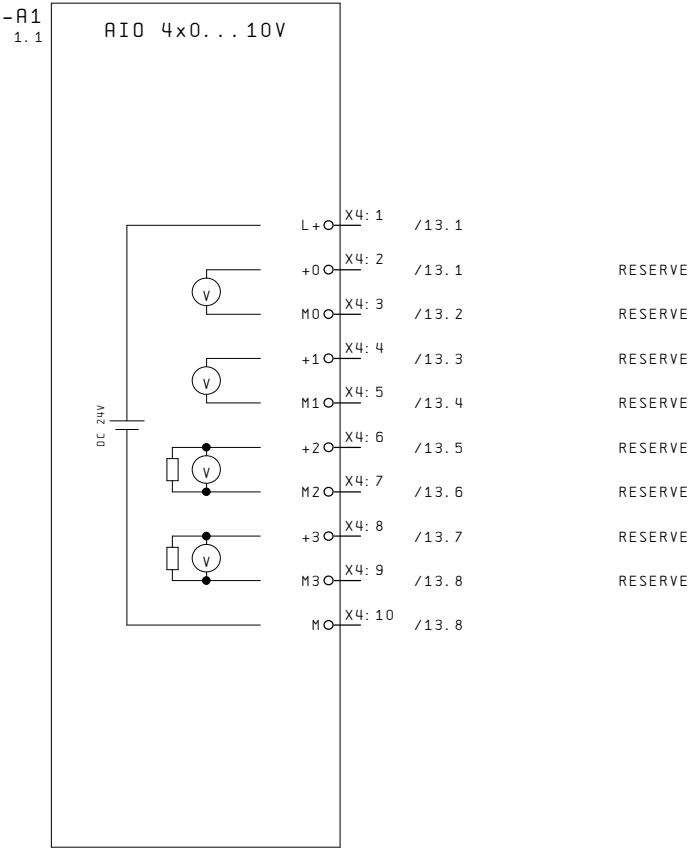


+136_4BD60/11													2
			Datum	28.04.03	Produktmakros für System 100V			SPS-Übersicht Versorgung, SM 136 DC24V, 136-4BD70		VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+136_4BD70	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	1	
											19 B1.		

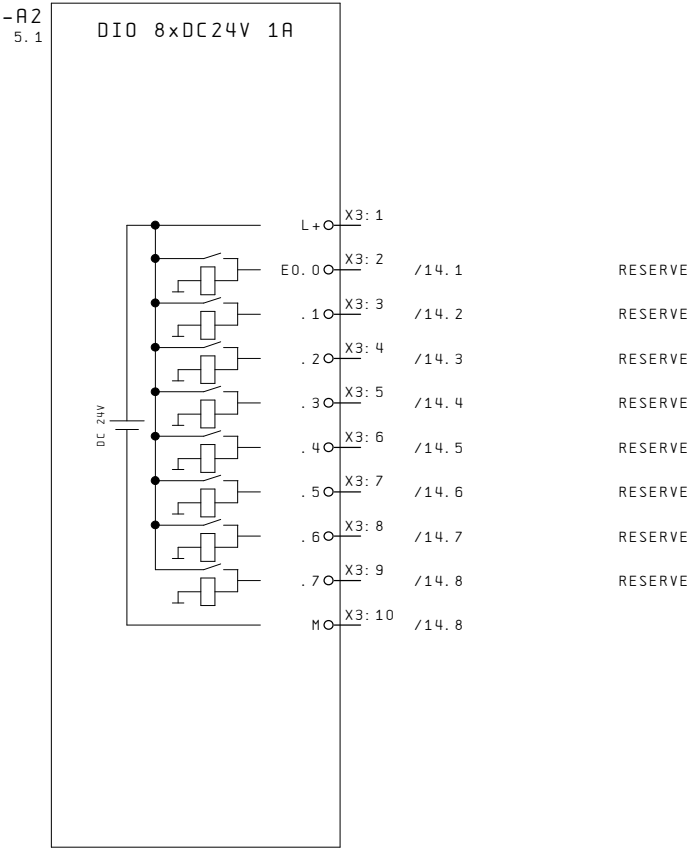
Variante 1: 8 Eingänge digital, 4 Eingänge analog



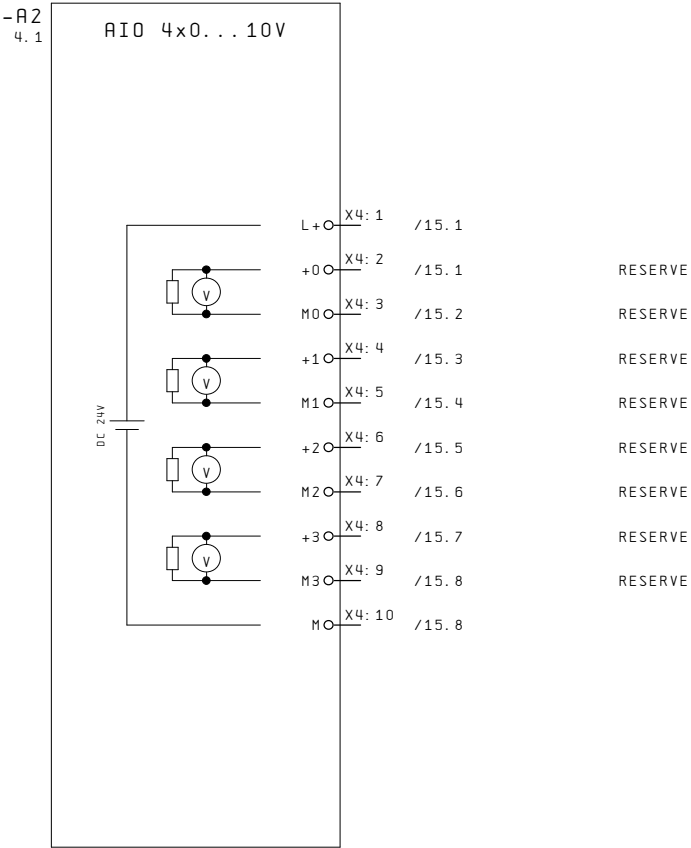
Variante 1: 8 Eingänge digital, 4 Eingänge analog



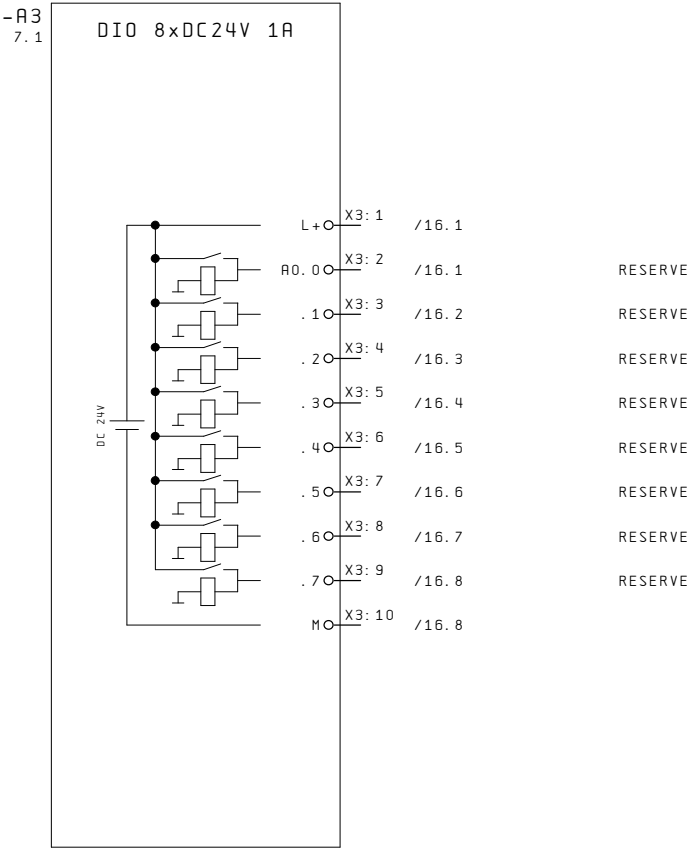
Variante 2: 8 Eingänge digital, 4 Ausgänge analog



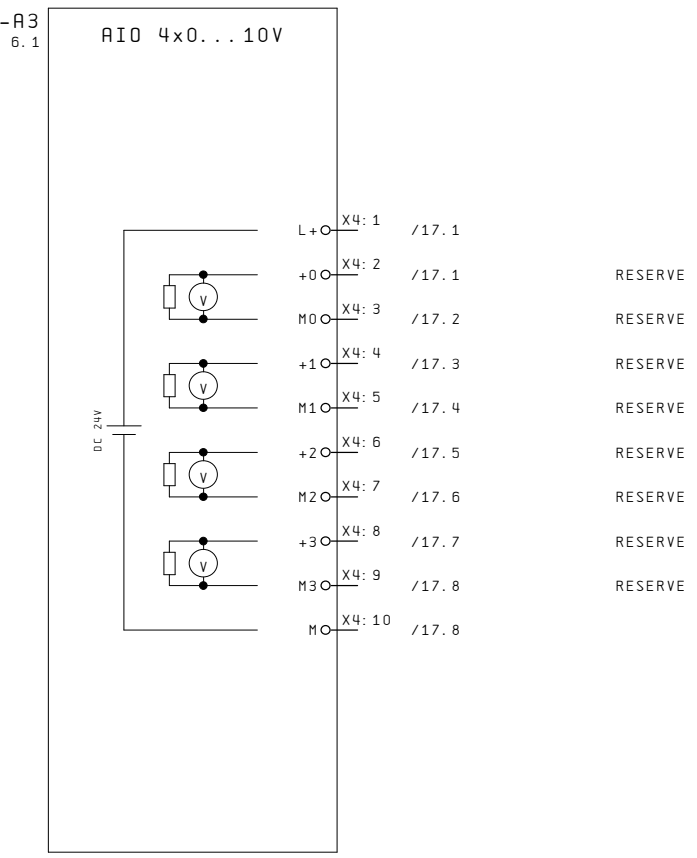
Variante 1: 8 Eingänge digital, 4 Ausgänge analog



Variante 3: 8 Ausgänge digital, 4 Eingänge analog

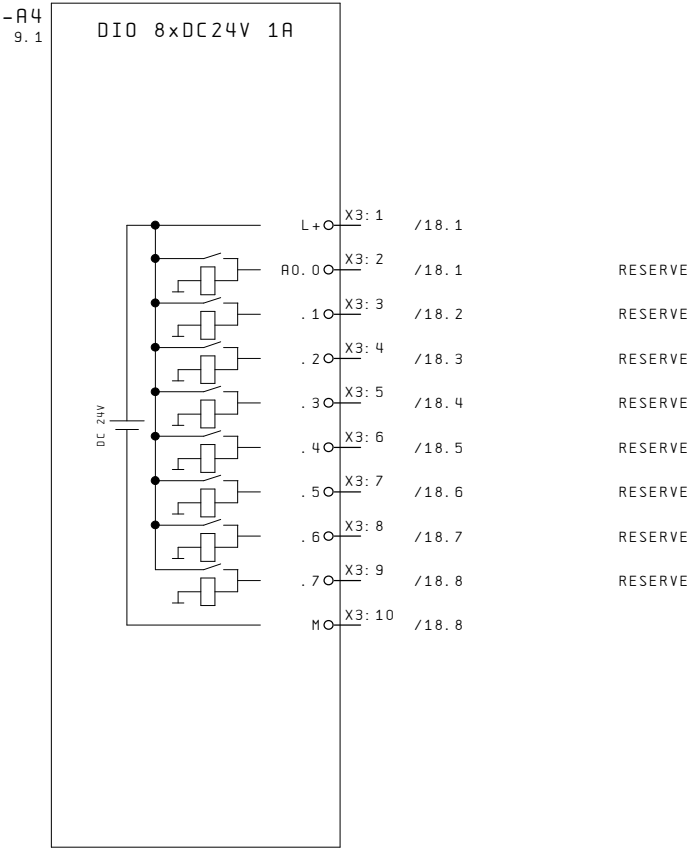


Variante 3: 8 Ausgänge digital, 4 Eingänge analog

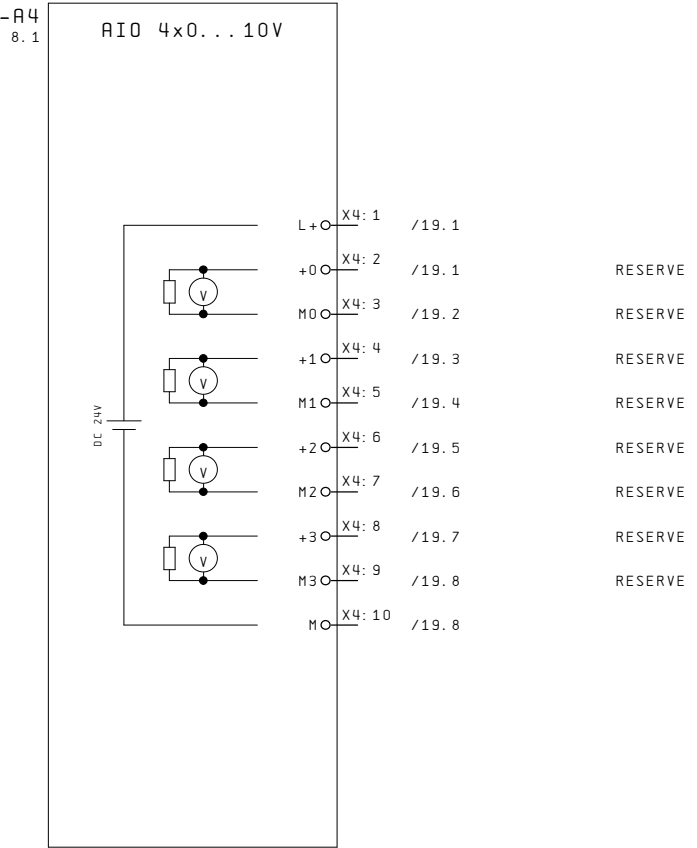


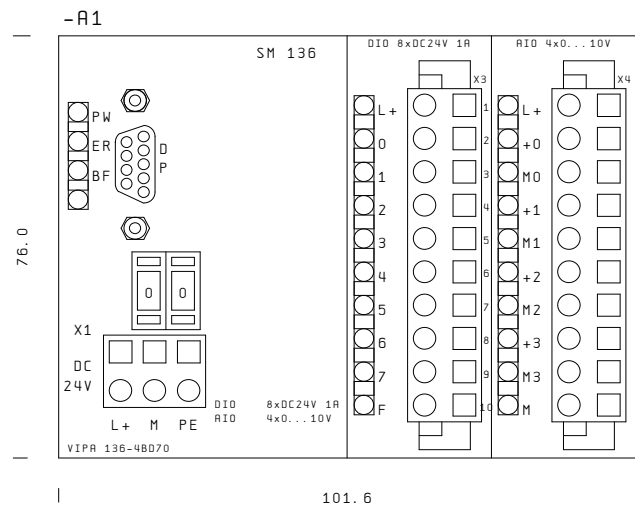


Variante 4: 8 Ausgänge digital, 4 Ausgänge analog



Variante 4: 8 Ausgänge digital, 4 Ausgänge analog

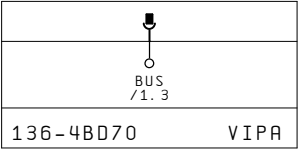




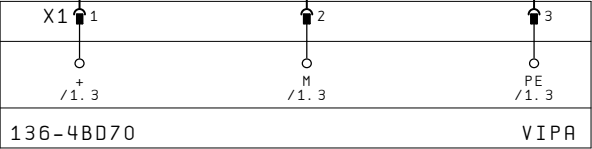
SM 136  
Integr. Spannungsversorgung DC 24V  
Abmessungen: (BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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-R1  
1.1



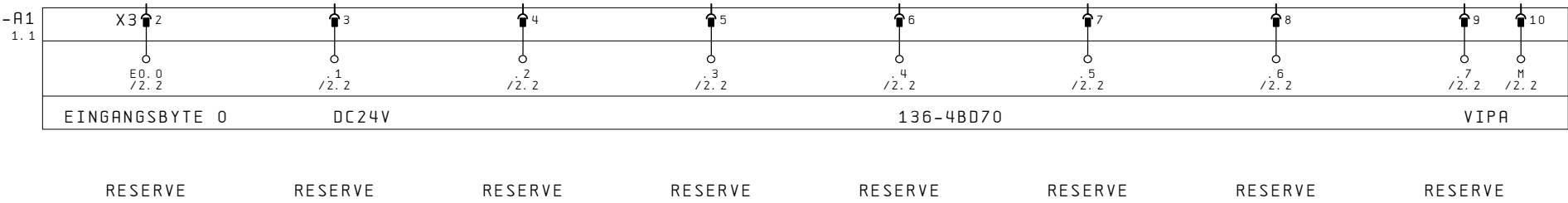
-R1  
1.1



			Datum	14.07.03	Produktmakros für System 100V			Anschlußbelegung, SM 136 DC24V, 136-4BD70	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+136_4BD70	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	11
												19 B1.

0	1	2	3	4	5	6	7	8	9
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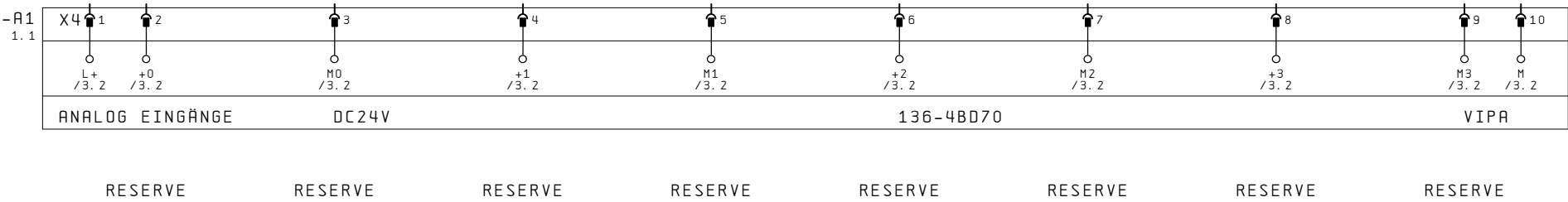
Variante 1: 8 Eingänge digital, 4 Eingänge analog



			Datum	14.07.03	Produktmakros für System 100V			Eingangsbyte 0, SM 136 DC24V, 136-4BD70	VIPA100V		=SYSTEM100V +136_4BD70			
			Bearb.	ZBW									B1.	12
			Geänd.											19 B1.
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V				

0	1	2	3	4	5	6	7	8	9
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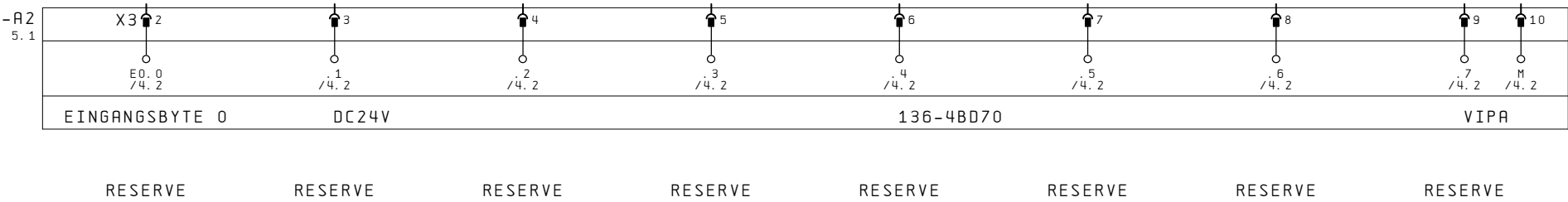
Variante 1: 8 Eingänge digital, 4 Eingänge analog



			Datum	14.07.03	Produktmakros für System 100V			Analog Eingänge, SM 136 DC24V, 136-4BD70	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+136_4BD70	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V		B1. 13
												19 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Eingänge digital, 4 Ausgänge analog



			Datum	14.07.03	Produktmakros für System 100V			Eingangsbyte 0, SM 136 DC24V, 136-4BD70	VIPA100V		=SYSTEM100V +136_4BD70			
			Bearb.	ZBW									B1.	14
			Geänd.											19 B1.
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V				

0	1	2	3	4	5	6	7	8	9
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Variante 2: 8 Eingänge digital, 4 Ausgänge analog

A2 14.1	ANALOG AUSGÄNGE		DC24V		136-4BD70			VIPA	
	/5.2 L+ O	/5.2 +0 O	/5.2 M0 O	/5.2 +1 O	/5.2 M1 O	/5.2 +2 O	/5.2 M2 O	/5.2 +3 O	/5.2 M O
	X4 1	2	3	4	5	6	7	8	9 10

RESERVE

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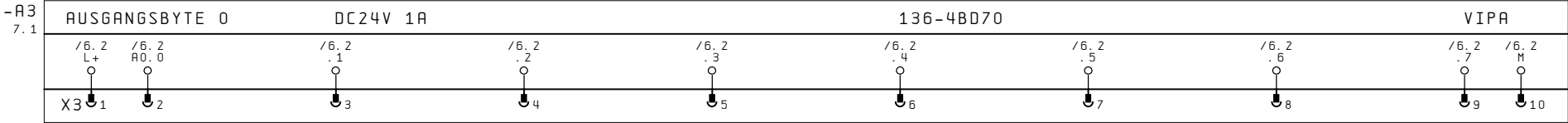
RESERVE

			Datum	14.07.03	Produktmakros für System 100V			Analog Ausgänge, SM 136 DC24V, 136-4BD70	VIPA100V		=SYSTEM100V		
			Bearb.	ZBW							+136_4BD70		
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.	System 100V		B1.	15		
												19 B1.	



0	1	2	3	4	5	6	7	8	9
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Variante 3: 8 Ausgänge digital, 4 Eingänge analog



RESERVE

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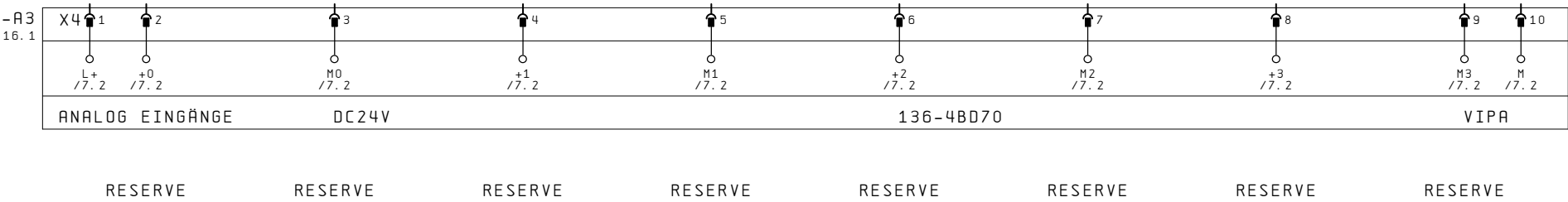
RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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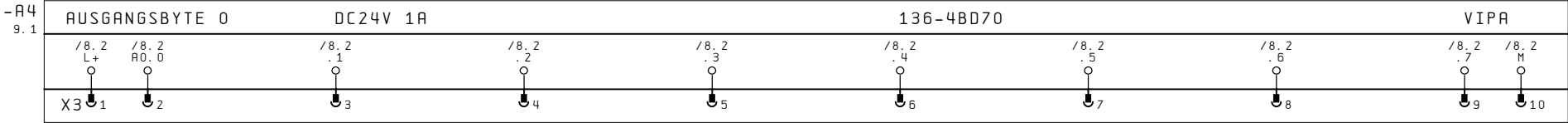
Variante 3: 8 Ausgänge digital, 4 Eingänge analog



			Datum	14.07.03	Produktmakros für System 100V			Analog Eingänge, SM 136 DC24V, 136-4BD70	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+136_4BD70	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	17
												19 B1.

0	1	2	3	4	5	6	7	8	9
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Variante 4: 8 Ausgänge digital, 4 Ausgänge analog



RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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Variante 4: 8 Ausgänge digital, 4 Ausgänge analog

-A4 18.1	ANALOG AUSGÄNGE		DC24V		136-4BD70			VIPA	
	/9.2 L+	/9.2 +0	/9.2 M0	/9.2 +1	/9.2 M1	/9.2 +2	/9.2 M2	/9.2 +3	/9.2 M3
	X4 1	2	3	4	5	6	7	8	9 10

RESERVE

RESERVE

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RESERVE

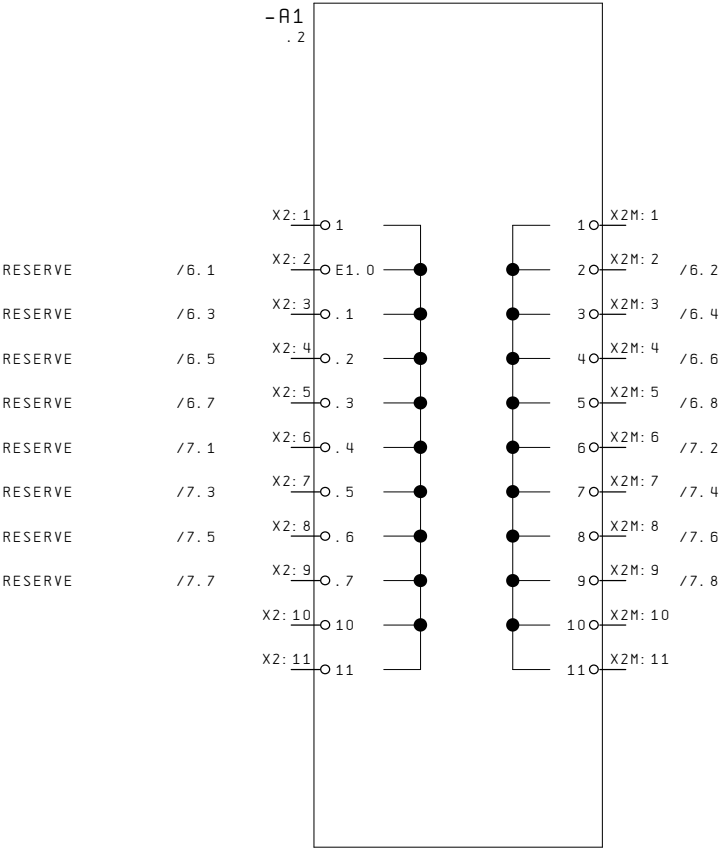
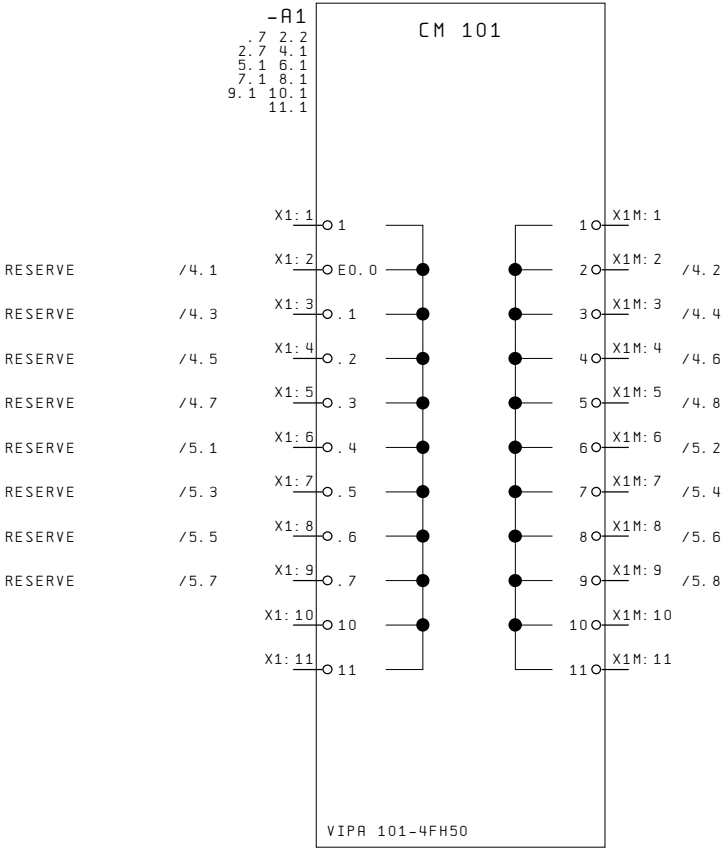
RESERVE

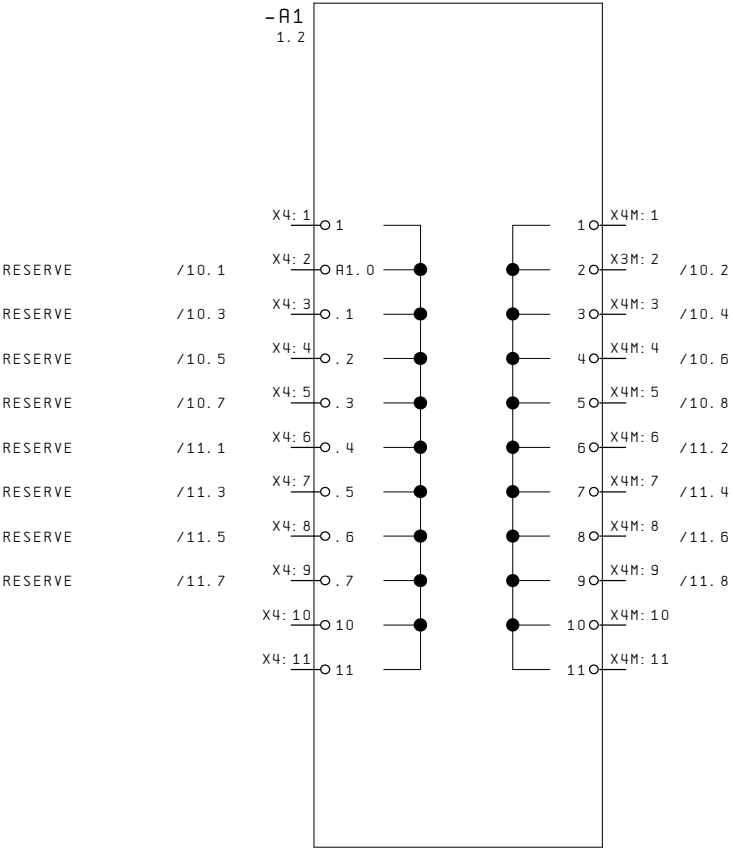
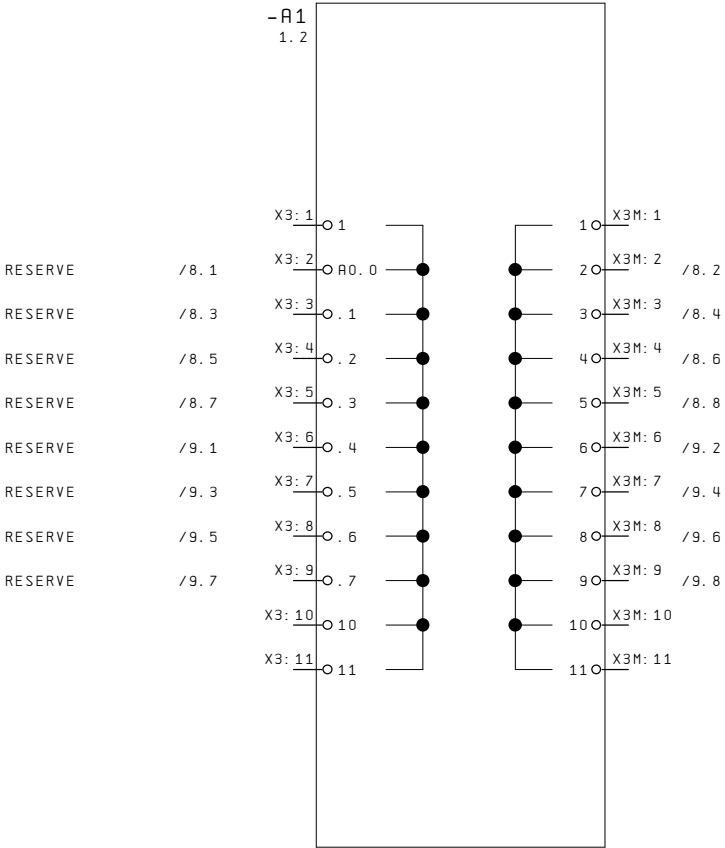
RESERVE

RESERVE

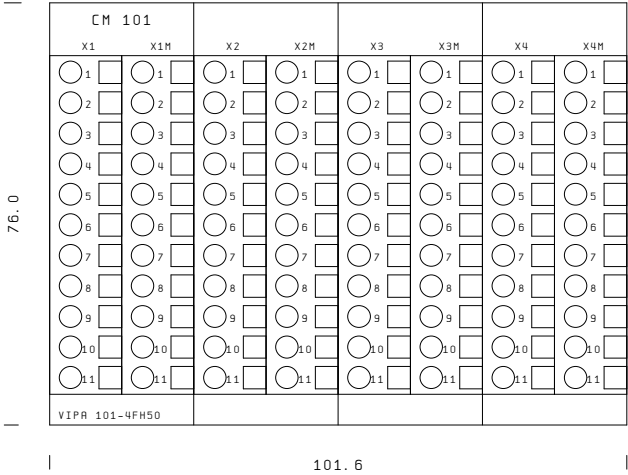
RESERVE

			Datum	14.07.03	Produktmakros für System 100V			Analog Ausgänge, SM 136 DC24V, 136-4BD70	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+136_4BD70	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.	System 100V		B1.	19	
												19 B1.





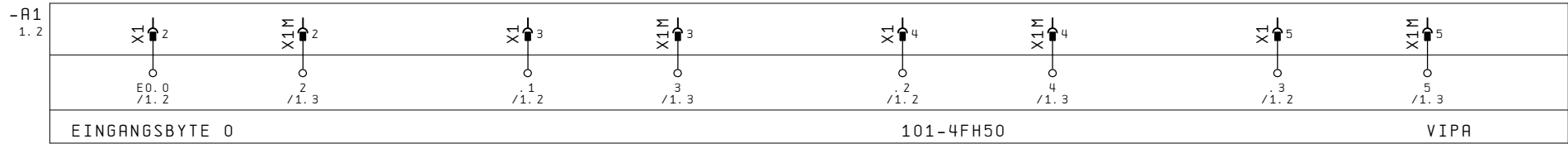
-R1



CM 101

Abmessungen: ( BxHxT) 101,6 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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RESERVE

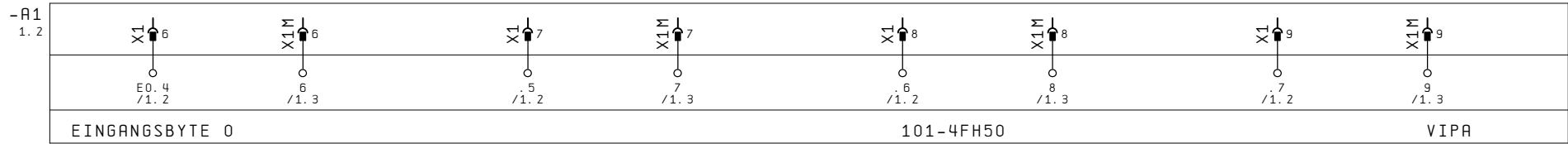
RESERVE

RESERVE

RESERVE



0	1	2	3	4	5	6	7	8	9
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RESERVE

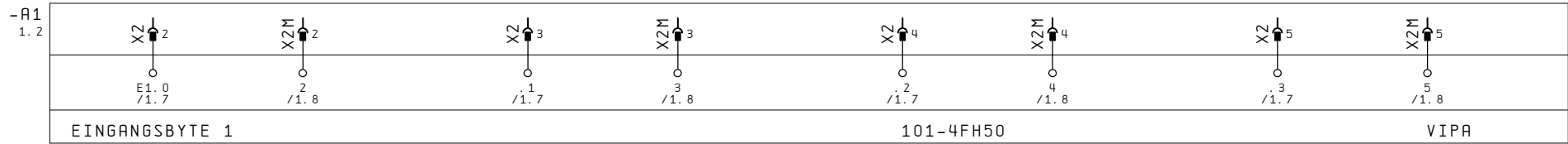
RESERVE

RESERVE

RESERVE

			Datum	14.07.03	Produktmakros für System 100V				Eingangsbyte 0, CM 101, 101-4FH50	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+101_4FH50	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	5	
											11 B1.		

0	1	2	3	4	5	6	7	8	9
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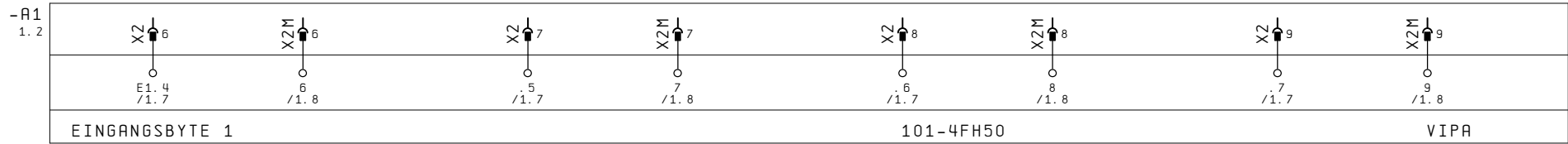
RESERVE

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0	1	2	3	4	5	6	7	8	9
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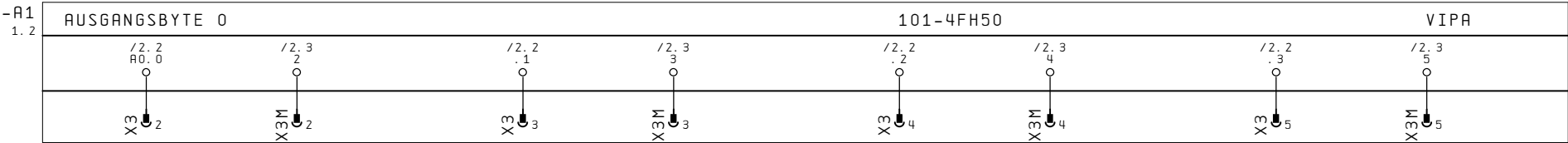
RESERVE

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RESERVE

			Datum	14.07.03	Produktmakros für System 100V				Eingangsbyte 1, CM 101, 101-4FH50	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW								+101_4FH50	
			Geänd.										
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	7	
											11 B1.		

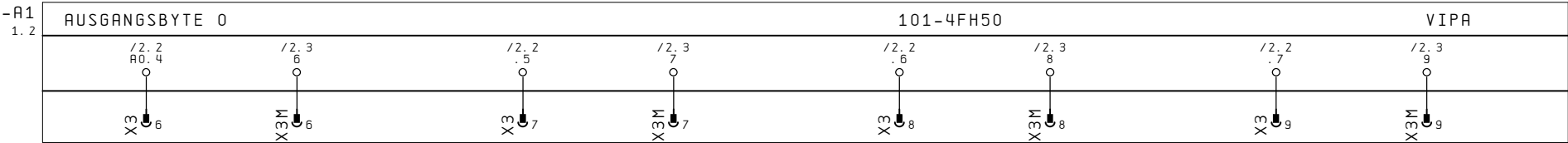


RESERVE

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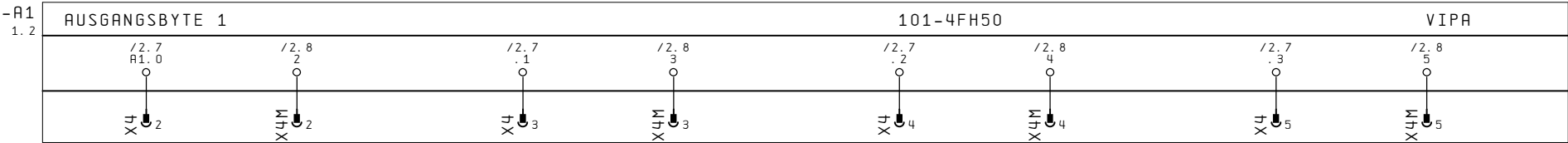


RESERVE

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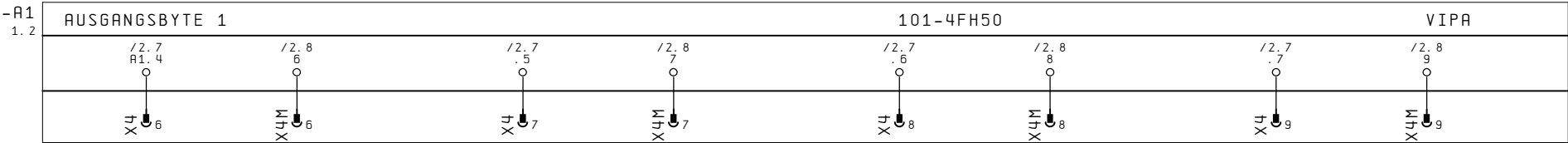
RESERVE

RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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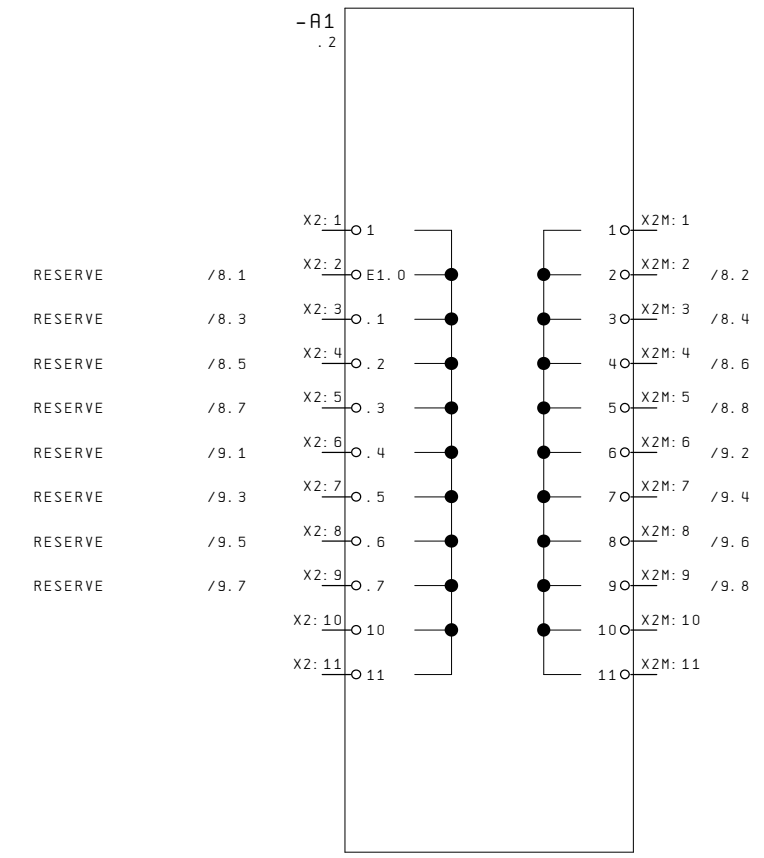
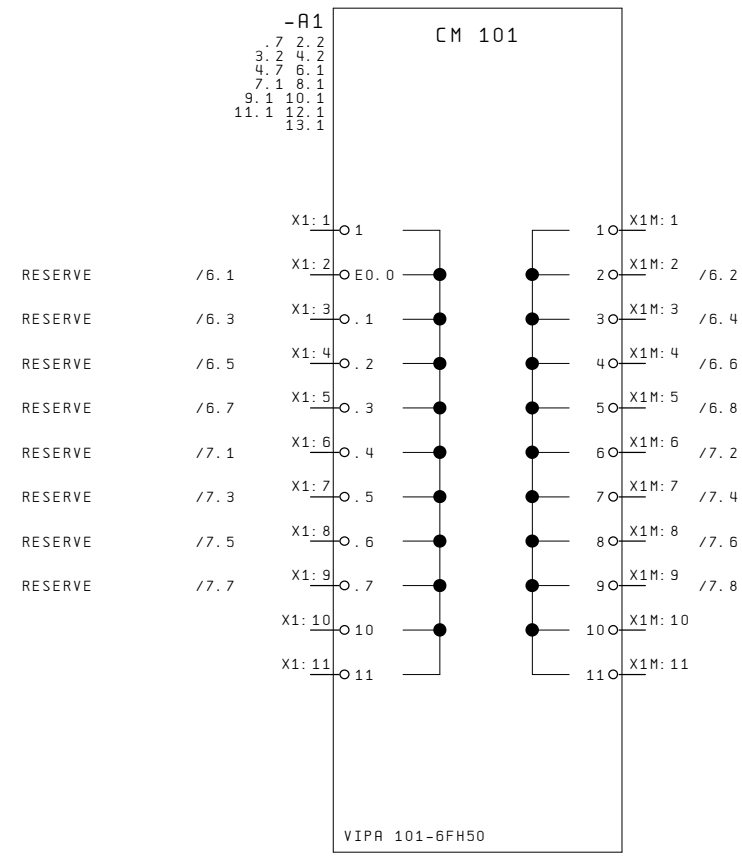
RESERVE

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RESERVE

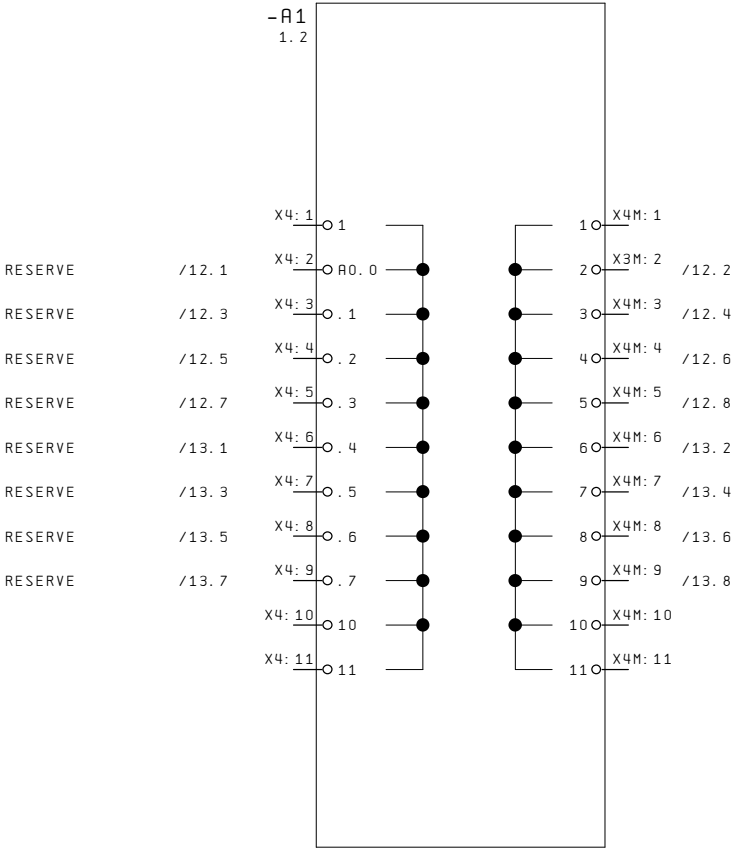
RESERVE

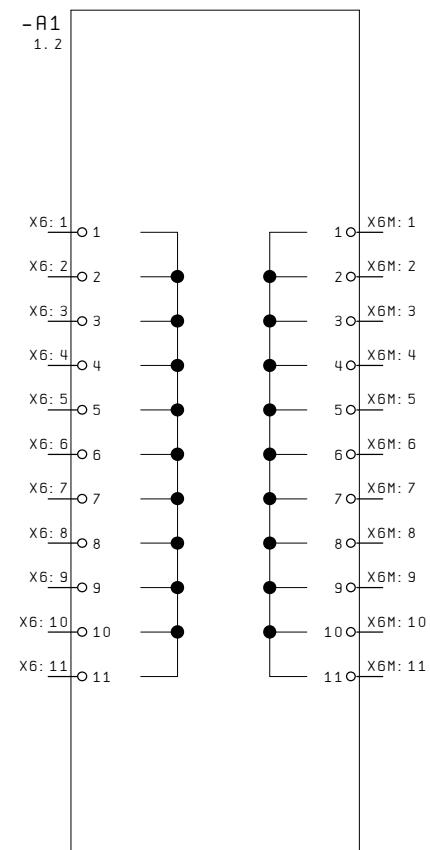
			Datum	14.07.03	Produktmakros für System 100V			Ausgangsbyte 1, CM 101, 101-4FH50	VIPA100V		=SYSTEM100V	
			Bearb.	ZBW							+101_4FH50	
			Geänd.									
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.		System 100V		B1.	11
											11 B1.	











-R1

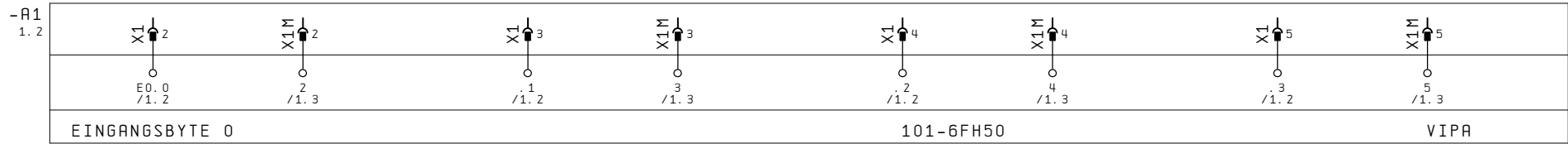
76.0	CM 101											
	X1	X1M	X2	X2M	X3	X3M	X4	X4M	X5	X5M	X6	X6M
	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>	<div><div></div><div>1</div></div>
	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>	<div><div></div><div>2</div></div>
	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>	<div><div></div><div>3</div></div>
	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>	<div><div></div><div>4</div></div>
	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>	<div><div></div><div>5</div></div>
	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>	<div><div></div><div>6</div></div>
	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>	<div><div></div><div>7</div></div>
	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>	<div><div></div><div>8</div></div>
	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>	<div><div></div><div>9</div></div>
	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>	<div><div></div><div>10</div></div>
	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>	<div><div></div><div>11</div></div>
	VIPR 101-6FH50											

152.4

CM 101

Abmessungen: (BxHxT) 152,4 x 76 x 48

0	1	2	3	4	5	6	7	8	9
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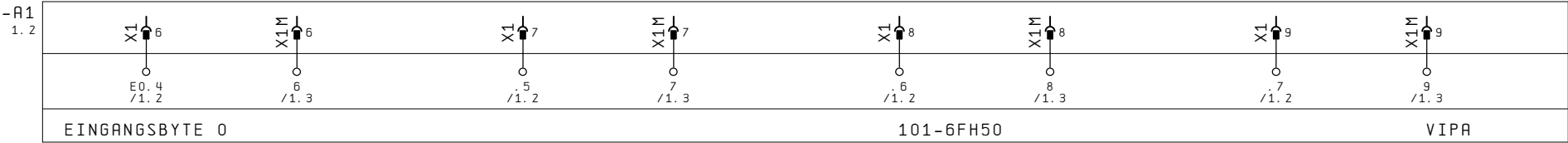
RESERVE

RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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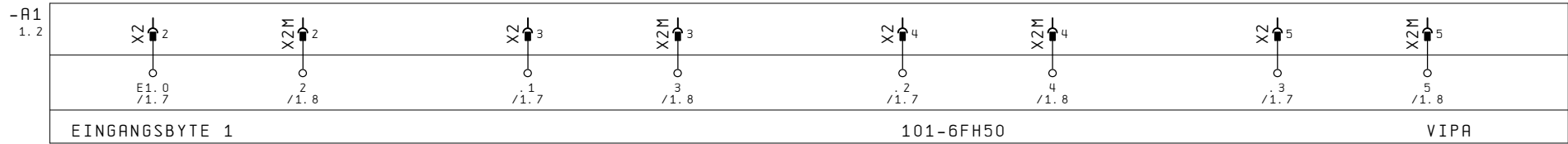
RESERVE

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RESERVE

0	1	2	3	4	5	6	7	8	9
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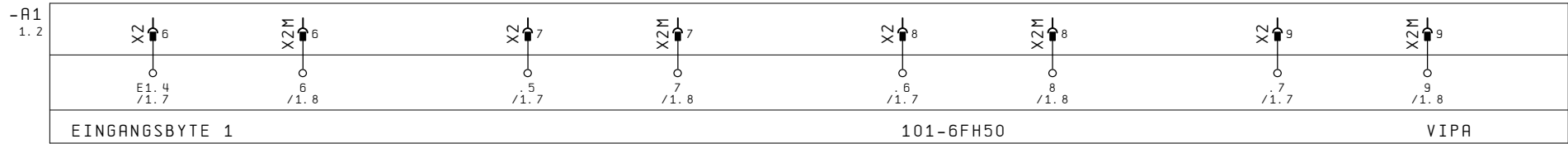
RESERVE

RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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RESERVE

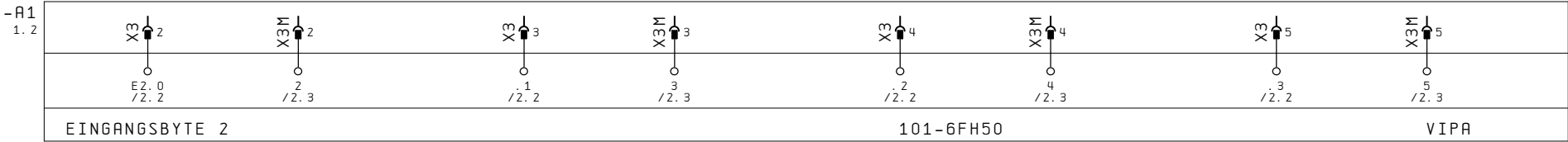
RESERVE

RESERVE

RESERVE



0	1	2	3	4	5	6	7	8	9
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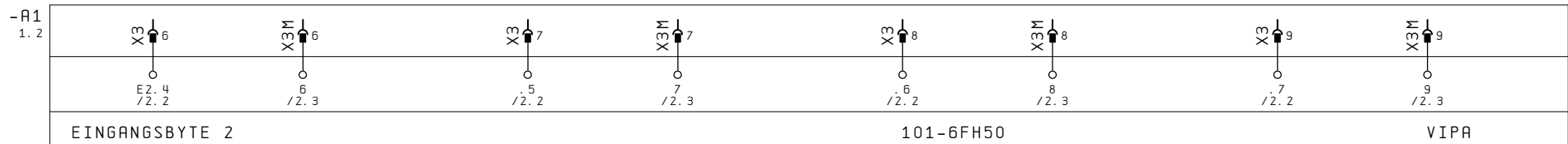
RESERVE

RESERVE

RESERVE

RESERVE

0	1	2	3	4	5	6	7	8	9
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RESERVE

RESERVE

RESERVE

RESERVE

			Datum	14.07.03	Produktmakros für System 100V			Eingangsbyte 2, CM 101, 101-6FH50	VIPA100V		=SYSTEM100V +101_6FH50			
			Bearb.	ZBW									B1.	11
			Geänd.											13 B1.
Änderung	Datum	Name	Form		Urspr.	Ers. f.	Ers. d.			System 100V				



-A1 1. 2	AUSGANGSBYTE 0								101-6FH50								VIPR							
	/3. 2 A0. 4		/3. 3 6		/3. 2 . 5		/3. 3 7		/3. 2 . 6		/3. 3 8		/3. 2 . 7		/3. 3 9									
	X4 6		X4 6		X4 7		X4 7		X4 8		X4 8		X4 9		X4 9									

RESERVE

RESERVE

RESERVE

RESERVE